

## EDUCATION

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### Stanford University

*Ph.D. candidate in Computer Science*

Stanford, CA

*Sep. 2021 –*

#### Coursework:

- Formal Methods for Computer Systems (CS357s)
- Design Projects in VLSI Systems I (EE272)

### Harvard University

*S.M. in Computer Science*

Cambridge, MA

*Sep. 2020 – May 2021*

### Harvard College

*A.B. Magna cum laude with highest honors in Computer Science*

Cambridge, MA

*Aug. 2017 – May 2021*

#### Selected coursework:

- Programming Languages (CS152)
- Research Topics in Operating Systems (CS261)
- Systems Security (CS263)
- Computational Linguistics and NLP (CS187)
- Research Topics in Computer Architecture (CS247r)
- Big Data Systems (CS265)
- Advanced Computer Architecture (CS246)
- Special Topics in Edge Computing (CS249r)
- Probabilistic Analysis and Algorithms (CS223)
- Data Systems (CS165)
- Data Structures and Algorithms (CS124)
- Computational Neuroscience (MCB131)
- Electromagnetism and Statistical Physics (Physics15b)
- Compilers (CS153)
- Theory of Computation (CS121)
- Optimization: Methods and Models (AM121)
- Circuits, Devices, and Transduction (ES152)
- Operating Systems (CS161)
- Design of VLSI Circuits and Systems (CS148)
- Systems Programming and Machine Org. (CS61)
- Computing Hardware (CS141)
- Mathematical logic (Phil140)

## PUBLICATIONS

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Zachary Yedidia and Stephen Chong. “Fast Incremental PEG Parsing” (2021). Proceedings of the 14th ACM SIGPLAN International Conference on Software Language Engineering (SLE), October 2021. **Best paper award**. PDF, Slides.

Maximilian Lam, Zachary Yedidia, Colby Banbury, Vijay Janapa Reddi. “Precision Batching: Bitserial Decomposition for Efficient Neural Network Inference on GPUs” (2021). Proceedings of the 30th International Conference on Parallel Architectures and Compilation Techniques (PACT), September 2021. PDF.

## WRITING

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Zachary Yedidia. “Incremental PEG Parsing” (2021). Senior thesis, advised by Prof. Stephen Chong. PDF.

Zachary Yedidia, “SystemVerilog Guide.” Course materials for CS 141 (Spring 2020) at Harvard. PDF.

## AWARDS

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NSF Graduate Research Fellowship.

## OPEN SOURCE PROJECTS

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### Micro Text Editor

Website, GitHub Project

I created and launched a text editor called Micro in April 2016. Micro is a Go project with over 18,000 stars on GitHub, more than 500,000 downloads, and 100+ contributors. It aims to be a successor to Nano as a simple to use terminal-based text editor. Micro was the subject of multiple news articles and has been featured on the front page of Hacker news multiple times. Micro is available in many package managers such as: Homebrew, Apt (for Ubuntu Focal and Debian Buster), Snap, AUR, Chocolatey and more.

## GPeg

Publication, Thesis, Slides, GitHub Project

Library for PEG parsing, as part of my senior thesis research with Professor Stephen Chong. GPeg uses a parsing virtual machine for dynamic parser generation, and implements a novel algorithm for efficient incremental parsing. Additionally includes a library for syntax highlighting: [github.com/zyedidia/flare](https://github.com/zyedidia/flare).

## Perforator

GitHub Project

Perforator is a `ptrace`-based tool for recording Linux “perf” metrics like cache misses, branch mispredictions, CPU cycles, etc. . . for certain parts of a program like functions or source code regions (as opposed to `perf stat` which only records over entire program lifetimes).

## Literate Programming Tool

Website, GitHub Project

A tool for compiling Literate programs written in any programming language. Featured on the front page of Hacker News in September 2015. The article “Write your Own Virtual Machine” was written using Literate.

## Eget

GitHub Project

A tool for automatically installing pre-built binaries distributed in GitHub releases.

## Go Generic Data Structures

Github Project

A library of generic data structures for Go.

## RISC-V collection

In progress

Including: a pipelined rv32 core written in Chisel (running on open-source FPGAs), a RISC-V symbolic execution engine, and a toy RISC-V assembler.

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## SERVICE

External reviewer for ACM TOPLAS.

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## EXPERIENCE

### Zero ASIC

*Intern*

Virtual

*Summer 2021*

### Harvard University

*HCRP Research Fellow (Advisor: Prof. Stratos Idreos)*

Cambridge, MA

*Summer 2020*

### Raytheon Company

*Internal Research and Development Intern*

*Advanced Missile Systems*

Tucson, AZ

*July – August Summer 2019*

### Princeton University

*Research Assistant (Advisor: Prof. Naveen Verma)*

Princeton, NJ

*May – June Summer 2019*

### Harvard University

*PRISE Research Fellow (Advisor: Prof. Eddie Kohler)*

Cambridge, MA

*Summer 2018*

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## TEACHING

### Using Bits to Control Atoms (CS49n)

*Course Assistant for Prof. Dawson Engler*

Stanford

*Autumn 2021*

- Helped with a lab-based introductory seminar on bare-metal Raspberry Pi programming.

### Systems Programming and Machine Organization (CS61)

*Teaching Fellow for Profs Eddie Kohler and Minlan Yu*

Harvard

*Fall 2020*

- Teaching evaluations: 4.9/5.0, Derek Bok Center teaching award.

### Computing Hardware (CS141)

*Teaching Fellow for Profs David Brooks and Vijay Reddi*

Harvard

*Spring 2019, Spring 2020*

- Revamped course materials with new FPGA boards and tools (updating to Xilinx Vivado and SystemVerilog).
- Teaching evaluations: 4.8/5.0, Derek Bok Center teaching award (2019). No evaluations in 2020 due to COVID.

## Compilers (CS153)

*Teaching Fellow for Prof. Stephen Chong*

Harvard

*Fall 2019*

- Teaching evaluations: 5.0/5.0.

## PROGRAMMING SKILLS

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**Primary Interests:** Computer Systems, Hardware/Architecture, Compilers.

**Languages:** Go, C/C++, Chisel/Scala, SystemVerilog, Python, Java, D, OCaml, Lua.

**Tools:** Vim, Git, Yosys, Verilator, Xilinx Vivado, L<sup>A</sup>T<sub>E</sub>X.