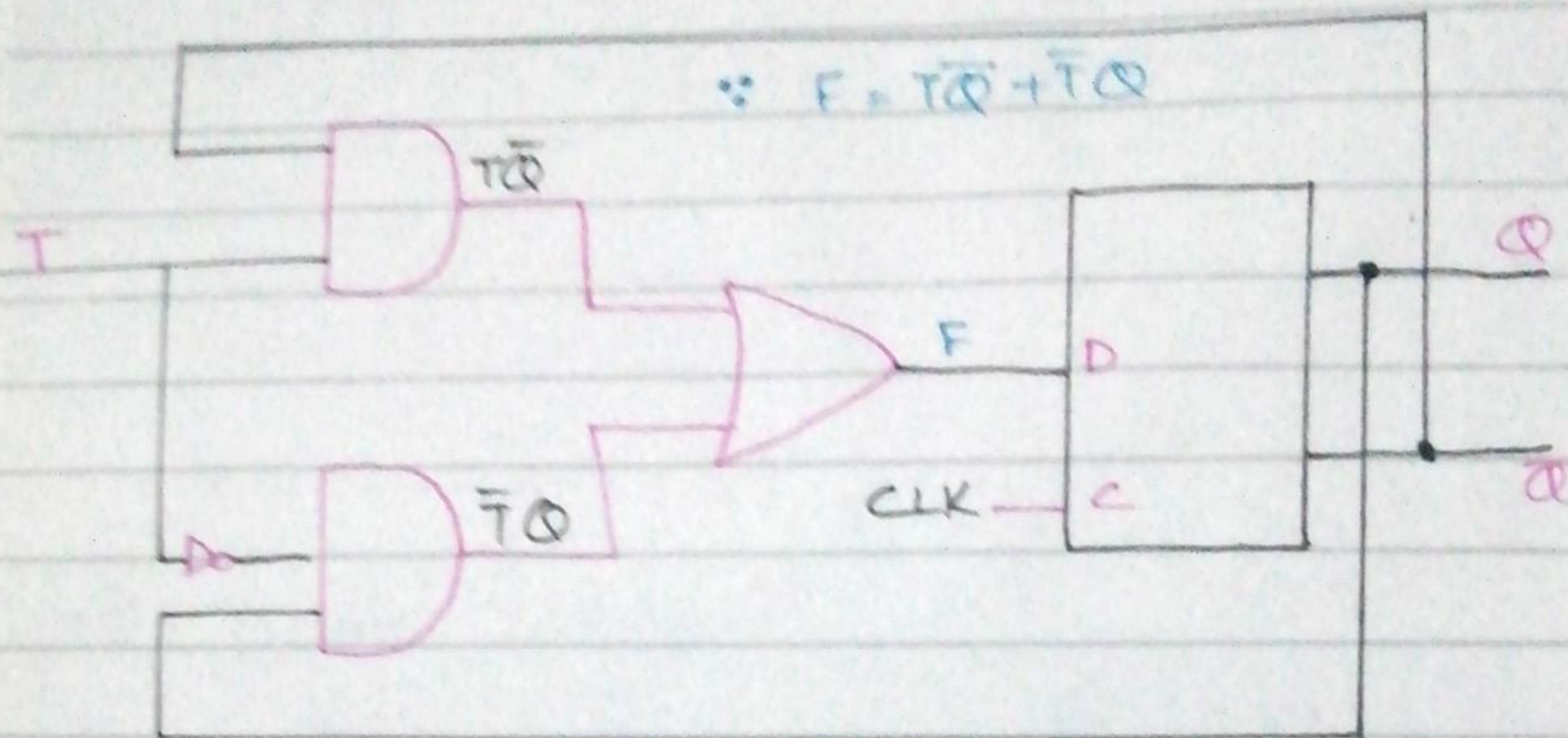


Q

## Homework

Analysis the circuit and complete the function table.

Circuit



Table

T	Q	Q(t+1)
0	0	0
0	1	1
1	0	1
1	1	0

$\therefore$  we have to compare Q with Q(t+1)

No change  
No change  
Complement  
Complement

T	Q(t+1)
0	Q(t)
1	$\bar{Q}(t)$

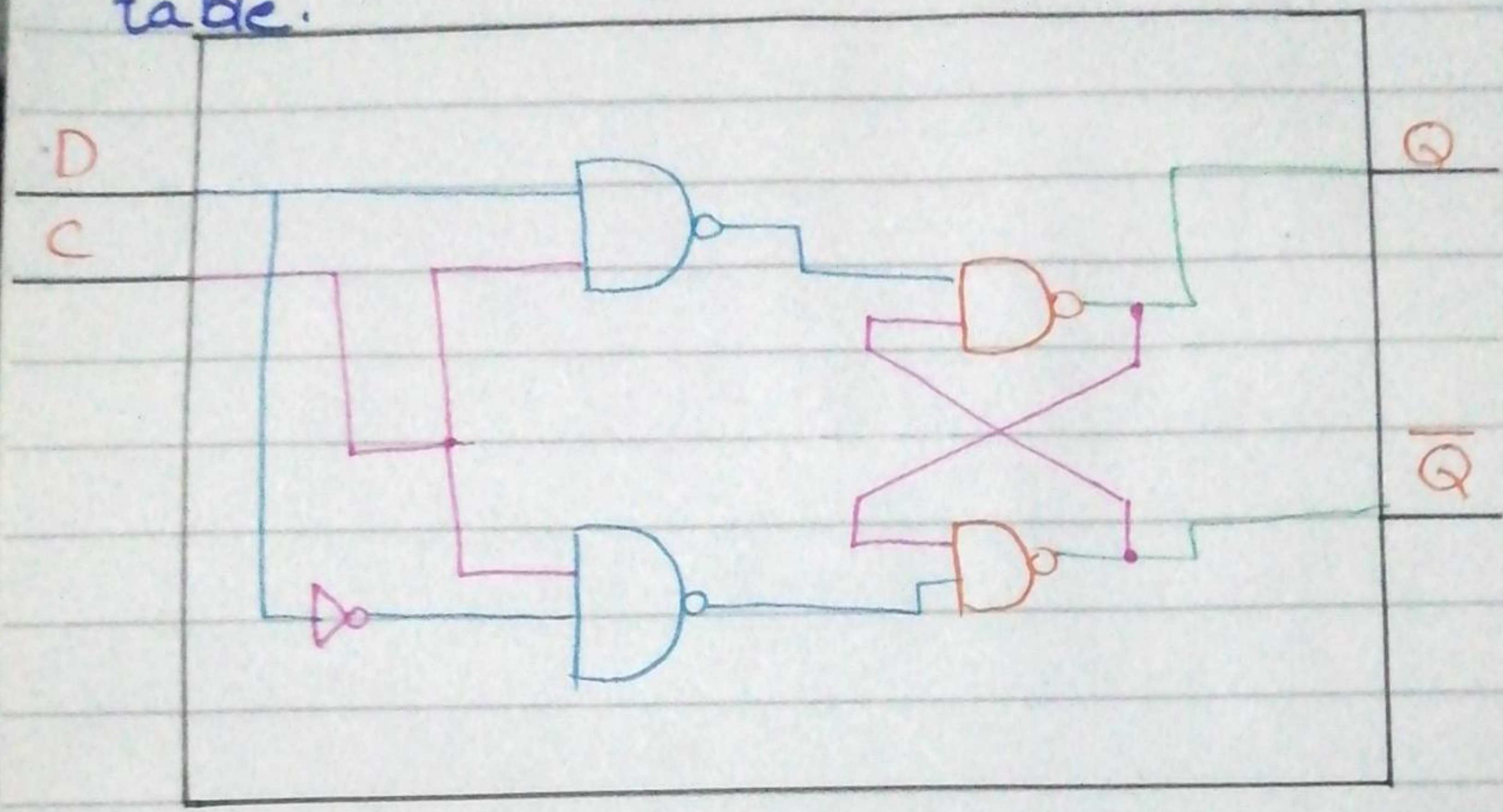
operation

No change  
Complement



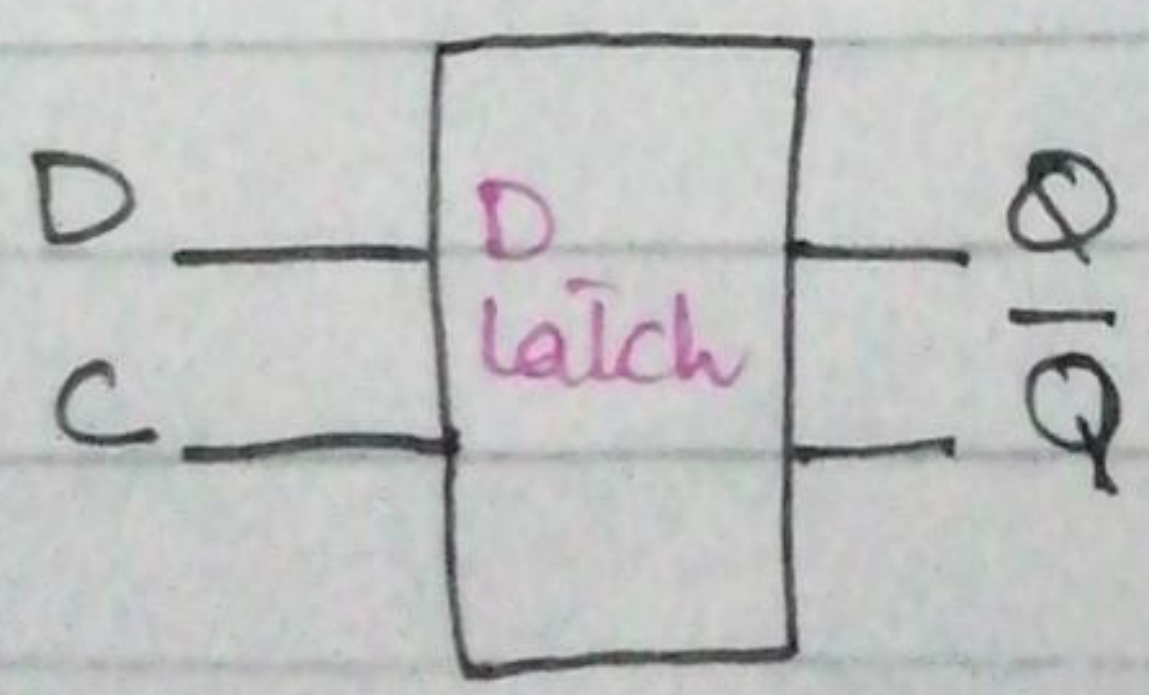
Lecture  
6-5-2020

Analysis the circuit and mention the next states of  $Q$  in a given table.



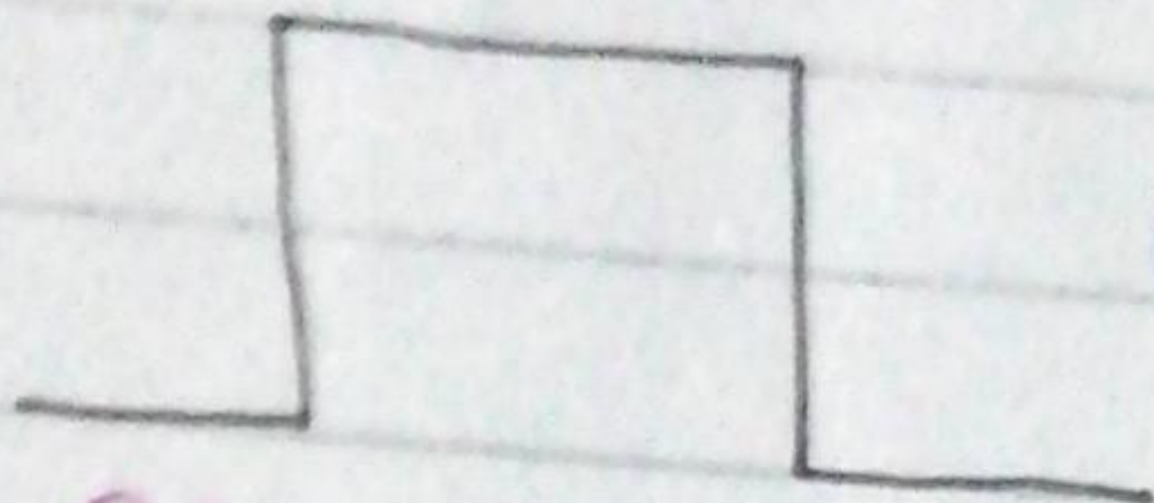
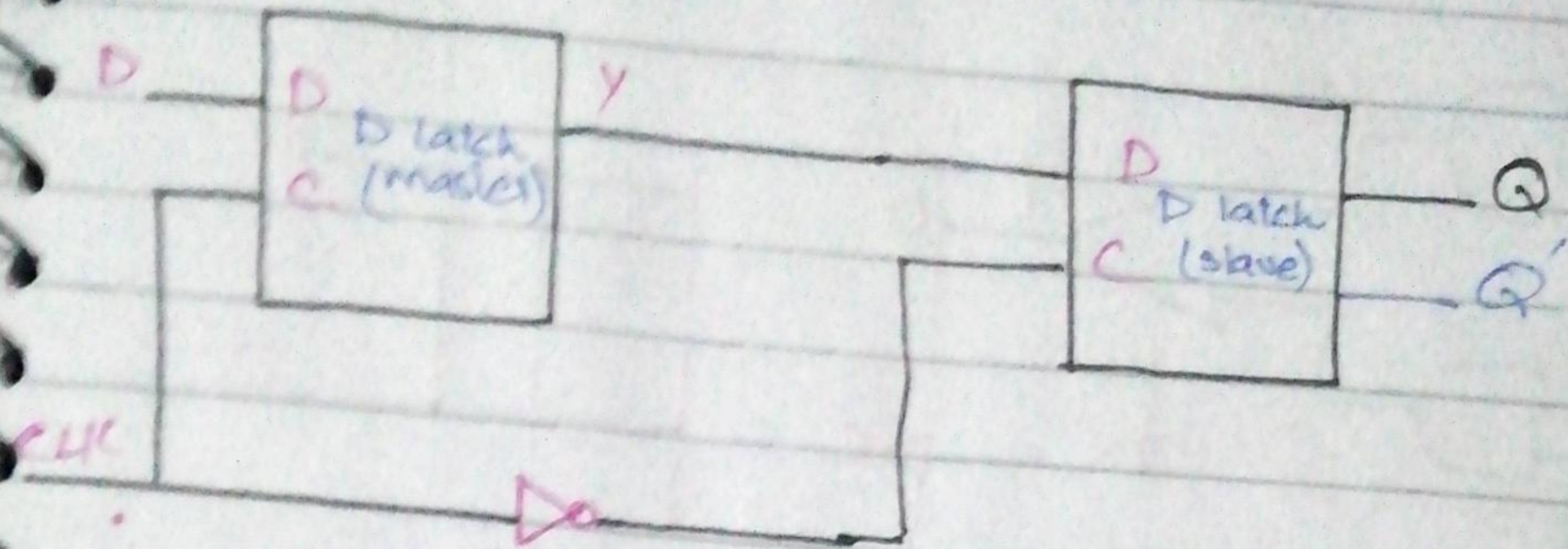
C	D	Next state of $Q$
---	---	-------------------

0	X	(no change)
1	0	Reset $Q$
1	1	set $Q$





# Flip-Flop (Master-Slave D Flip Flop)



Latch is level sensitive and Flip Flop is edge trigger.

Positive Edge Trigger  
Negative Trigger.

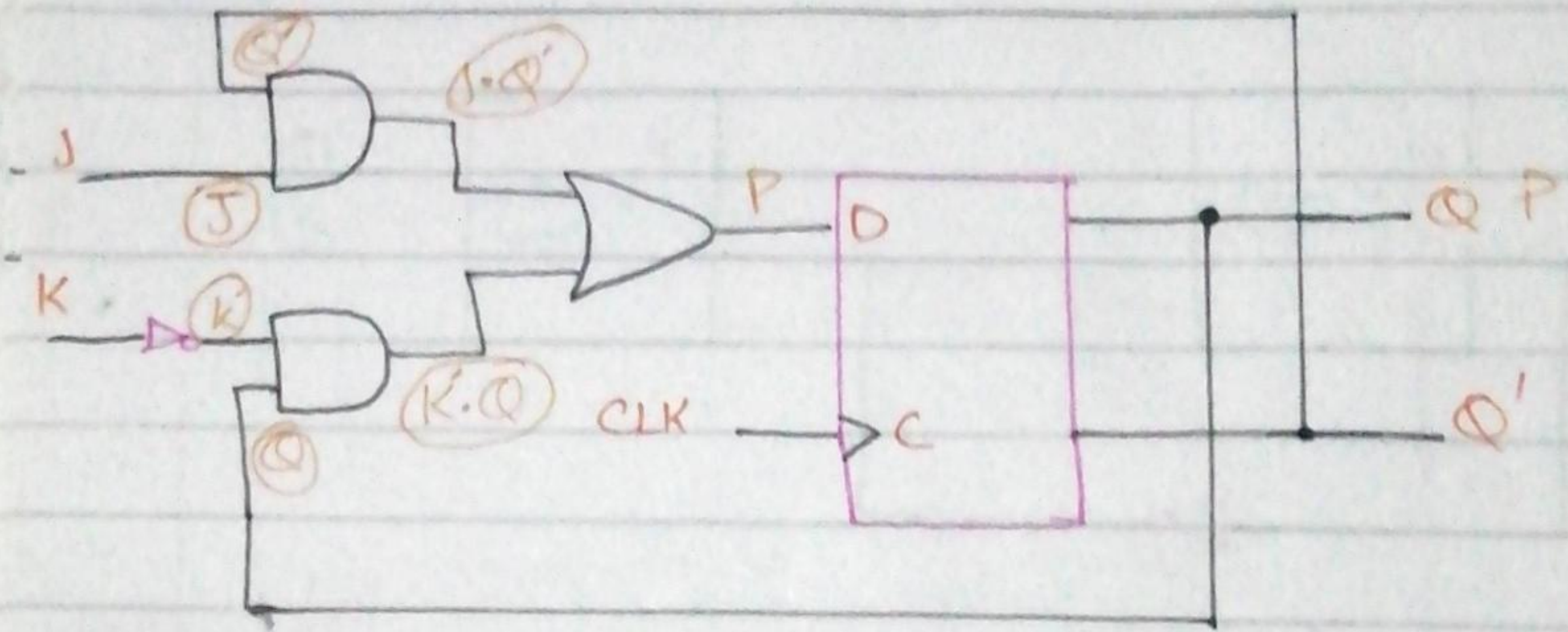
D	$Q(t+1)$	operation
0	0	Reset
1	1	Set



# J-k Flip Flop

$$P = JQ' + KQ$$

⇒ Circuit



⇒ Truth Table

J	K	Q	Q(t+1)	
0	0	0	0	No change
0	0	1	1	
0	1	0	0	Reset
0	1	1	0	
1	0	0	1	Set
1	0	1	1	
1	1	0	1	Compliment
1	1	1	0	



DLD

13-5-2020

⇒ SR Latch

S	R	$Q(t+1)$	operation
0	0	No change	No change
0	1	0	Reset
1	0	1	Set
1	1	Intermediate	Compliment

⇒ JK Flip Flop

J	K	$Q(t+1)$	operation
0	0	$Q$	No change
0	1	0	Reset
1	0	1	Set
1	1	$Q'$	Compliment

⇒ Master Slave D-Flip Flop

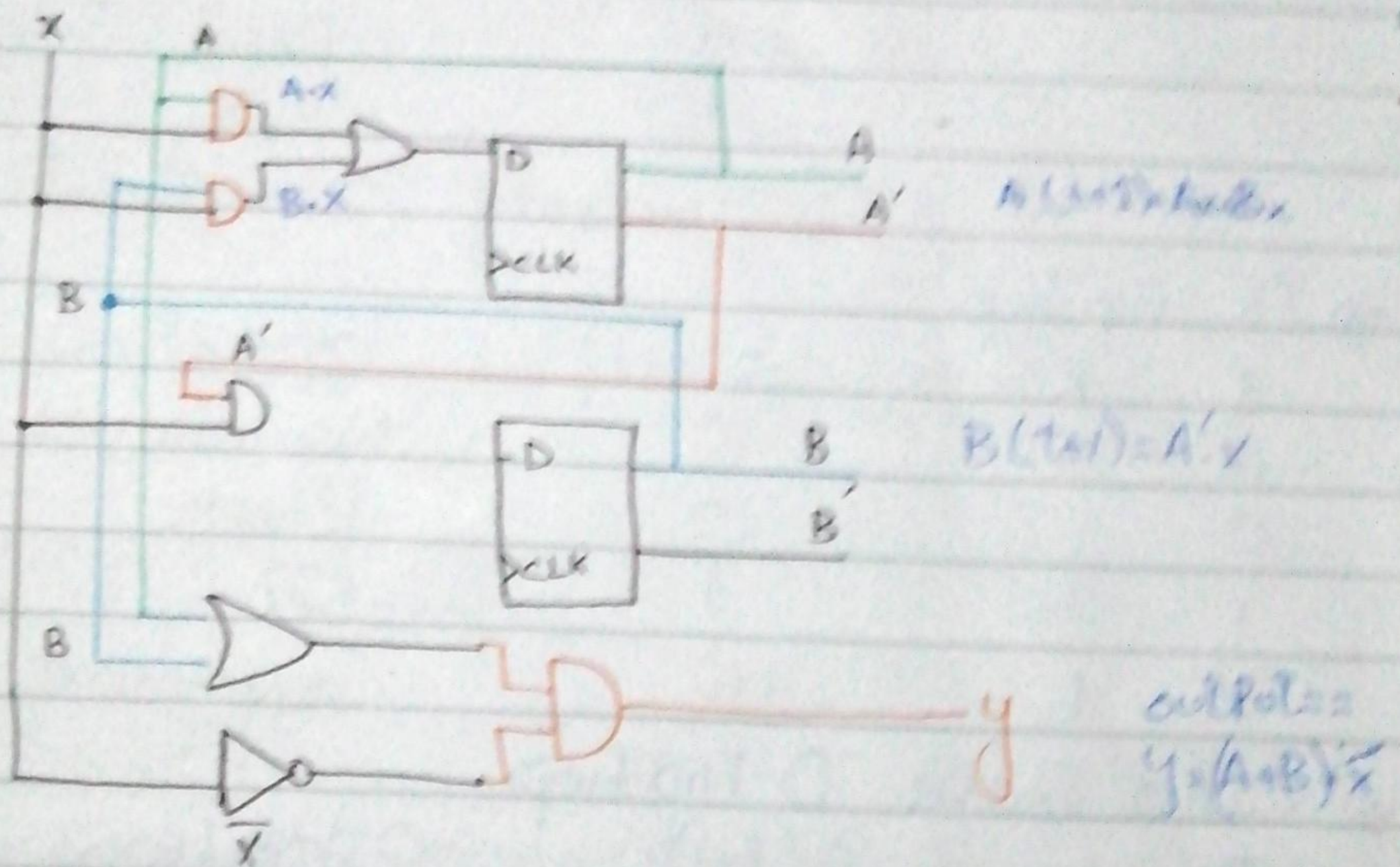
D	$Q(t+1)$	operation
0	0	Reset
1	1	Set

⇒

T	$Q(t+1)$	operation
0	$Q$	No-change
1	$Q'$	Compliment



# Analysis of Clocked Sequential Circuit



## Equations

$$A(t+1) = A \cdot X + B \cdot X$$

$$B(t+1) = \overline{A} \cdot X$$

$$Y = (A+B) \cdot \overline{X}$$



## > First state of Table:

		Input		Next State		Output
A	B	x		A	B	y
0	0	0		0	0	0
0	0	1		0	1	0
0	1	0		0	0	1
0	1	1		1	1	0
1	0	0		0	0	1
1	0	1		1	0	0
1	1	0		0	0	0
1	1	1		1	0	0

## > Second State Table

Present

when  $x=0$

when  $x=1$

$x=1$

$x=0$

A	B	A-B	A	B	y	y
0	0	0	0	1	0	0
0	1	0	1	1	0	1
1	0	0	1	0	0	1
1	1	0	1	0	0	1