EE227 – DLD Assignment 7

EE227 – Digital Logic Design

Assignment 7

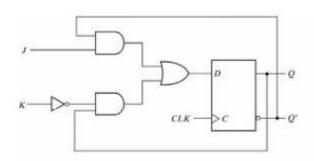
Spring 2020

Instructor Name: Engr. Amir Zahoor

Instructions for Submission:

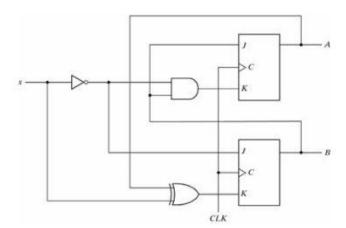
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- Partially or fully **copied assignments** will be marked as **zero**.
- You should submit only one PDF document and all text should be hand written. Equations,
- figures can only be hand written (all figures/equations can be pasted as images inside
- that document).
- Only **handwritten** solution.
- Late submissions are not allowed.
- Viva of any student can be conducted by the instructor after conducting an online
- exam in case of any doubt.
- You can submit your assignment during Google Classroom only.

Question Number 1



Analyze the circuit and complete the function table. Determine state table and state equations.

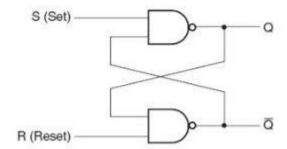
Question Number 2



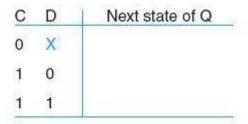
Analyze the circuit and complete the function table. Determine state table, state equations and state diagram.

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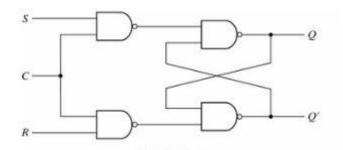
Question Number 3



Analysis the circuit and mention the next states of Q in the given table.



Question Number 4



Analysis the circuit and mention the next states of Q in the given table.



Good Luck