EE227 – Digital Logic Design

Assignment 5

Spring 2020

Instructor Name: Engr. Amir Zahoor

Instructions for Submission:
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- Partially or fully **copied assignments** will be marked as **zero**.
- You should submit only one PDF document and all text should be hand written. Equations,
- figures can only be hand written (all figures/equations can be pasted as images inside
- that document).
- Only **handwritten** solution.
- Late submissions are not allowed.
- Viva of any student can be conducted by the instructor after conducting an online
- exam in case of any doubt.
- You can submit your assignment **during Google Classroom only.**

Question Number 1

Design and implement 4-to-16 line decoder using
a) 4-16 bit decoder   b) 3-8 bit decoder

Question Number 2

Design and implement 3-to-8 line decoder and also implement the following functions with the help of 3-to-8 line decoder.
a) F= $\sum(1,4,7)$ b) F= $\sum(0,3,4,6)$

Question Number 3

Design and implement full Adder with the help of Decoder.

Good Luck