

Description

The KLXES15AAA1 polymeric ESD suppressor help protect sensitive electronic equipment against electrostatic discharge (ESD) without distorting data signals. This protection is a result of its ultra-low capacitance of only 0.05 pF (I/O to GND), and it can be used to help equipment to pass IEC61000-4-2 level 4 test (15KV air, 8KV contact discharge).

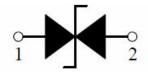
(EIA Size) 0402



Features

- Ultra low capacitance,0.05pF(typ.).
- Fast response time(<1ns).
- Low leakage current(<10nA).
- ◆ Bi-directional, single line protection.
- ◆ IEC61000-4-2(Contact): 8KV, IEC61000-4-2(Air) :15KV.

Equivalent Circuit



Applications

- Smart Phone/Mobile Internet Device.
- ◆ Laptop/Desktop Computer.

- ♦ Antennas (Cell Phones, GPS···).
- ◆ USB 3.0,USB 3.1 and high speed interface.

General Characteristics

Parameter	Max	Unit
Contact Discharge Voltage Per IEC61000-4-2	8	KV
Air Discharge Voltage Per IEC61000-4-2	15	KV
Operating Temperature	-55 to +125	$^{\circ}$
Storage Temperature	-40 to +85	°C



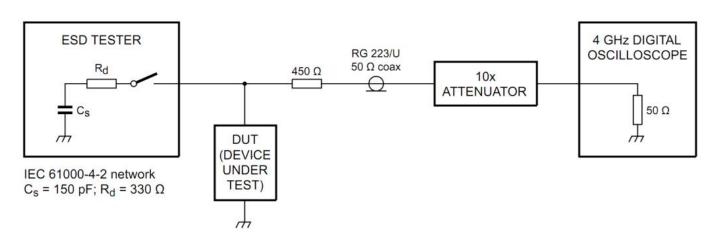
Electrical Characteristics (T_A = 25°C)

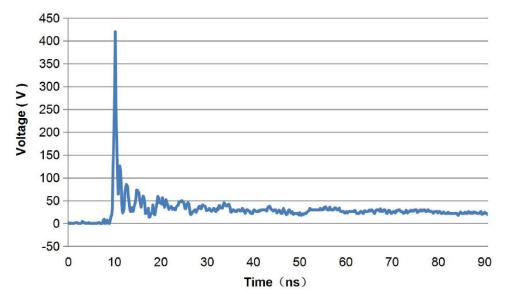
Parameter	Symbol	Test Conditions		Тур.	Max.	Units
Continuous Operating Voltage	V _{DC}				5	٧
Trigger Voltage	V _T	IEC61000-4-2 8KV contact discharge		450		V
Clamping Voltage	Vc	IEC61000-4-2 8KV contact discharge		40		V
Leakage Current	IL	DC 5V shall be applied on component			10	nA
Capacitance	C₽	Measured at 10MHz		0.05		pF
ESD Pulse Withstand	Pulses	IEC61000-4-2 8KV contact discharge	1000			

Note:

Trigger and clamping voltage are measured per IEC 61000-4-2, 8KV contact discharge method.

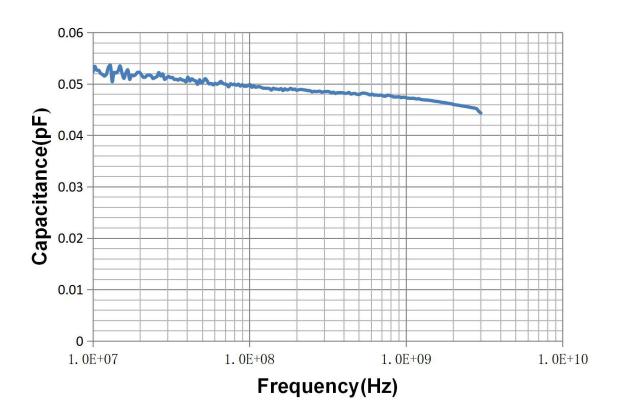
Typical ESD Response (IEC 61000-4-2, 8KV contact discharge)



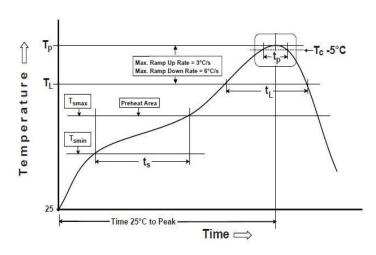




Typical Device Capacitance VS. Frequency



Soldering Parameters

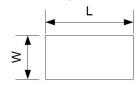


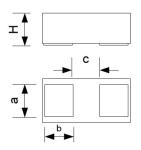
Profile Fe	eature	Pb-Free Assembly		
Pre Heat	Temperature Min (T _{smin})	150℃		
	Temperature Max (T _{smax})	200℃		
	Time (T _s) from (T _{smin} to T _{smax})	60-120 seconds		
Ramp-up	Rate (T _L to T _P)	3°C/second max.		
Liquidus	Temperature (T _L)	217 ℃		
Time (t _∟)	maintained above T _L	60-150 seconds		
Peak Pac	kage Body Temperature (T _P)	260 ^{+0/-5} °C		
, ,)* within 5℃ of the Specified ation Temperature (Tc)	30* seconds		
Ramp-do	wn Rate (T _P to T _L)	6℃/second max.		
Time 25°	C to peak Temperature	8 minutes Max		
* Tolerance for peak profile temperature (T _P) is defined as a supplier minimum and a user maximum.				



Package Dimension

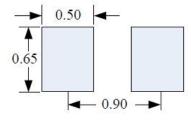
Package Outline





Dimension	Unit: Millimeters				
	Min.	Тур.	Max.		
L	0.98	1.00	1.03		
w	0.48	0.50	0.53		
Н	0.35	0.38	0.40		
а	0.35	0.38	0.40		
b	0.30	0.30	0.35		
С	0.24	0.26	0.29		

Recommended Solder Pad Footprint



Sizes in mm

Notes:

This solder pad layout is for reference purposes only.