

Yuxuan Zhang

Last update on May 8, 2025

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Education

University of Pennsylvania, Philadelphia, PA

PhD in Computer Science, 2019 -

Advisor: Sebastian Angel

GPA: 4.0/4.0

University of Michigan, Ann Arbor, MI

MS in Electrical Engineering, 2015 - 2017

Harbin Institute of Technology, Harbin, China

BS in Electrical Engineering, 2008 - 2012

Employment History

Google LLC, Sunnyvale, CA

Software Engineer Intern, May 2025 - Aug 2025

VMWare Corp., Boston, MA

Software Engineer Intern, May 2022 - Aug 2022

Microsoft Research Asia, Beijing, China

Research Intern, Jan 2018 - July 2019

Mentor: Yongqiang Xiong

NVIDIA Corporation, Beijing, China

Software Engineer Intern, May 2017 - Sep 2017

Publications

Quilt: Resource-aware Merging of Serverless Workflows.

Y. Zhang, S. Angel.

Proc. ACM Symposium on Operating Systems Principles (SOSP), under review, 2025

RPG2: Robust Profile-Guided Runtime Prefetch Generation.

Y. Zhang, N. Sobotka, S. Park, S. Jamilan, T. A. Khan, B. Kasikci, G. Pokam, H. Litz, J. Devietti.

International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS), May. 2024.

Online CODE Layout OptimizationS via Ocolos.

Y. Zhang, T. A. Khan, G. Pokam, B. Kasikci, H. Litz, J. Devietti.

IEEE Micro Volume 43, Issue 4, "Top Picks From the 2022 Computer Architecture Conferences", July. 2023.

OCOLOS: Online CODE Layout OptimizationS.

Y. Zhang, T. A. Khan, G. Pokam, B. Kasikci, H. Litz, J. Devietti.

Proc. International Symposium on Microarchitecture (MICRO), Oct. 2022.

Honor & Awards

Paper selected for IEEE Micro Top Picks in Computer Architecture from 2023

Outstanding Graduates Awards, Harbin Institute of Technology, 2012

Fuji Xerox Scholarship, Harbin Institute of Technology, 2011

Undergraduate GPA ranking top 1 for one academic year

Suzhou Industry Park Scholarship, Harbin Institute of Technology, 2010

Undergraduate GPA ranking top 2 for one academic year

Talks

RPG2: Robust Profile-Guided Runtime Prefetch Generation. May. 2024

International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS), San Diego, CA.

OCOLOS: Online CODE Layouy OptimizationS. Oct. 2022

International Symposium on Microarchitecture (MICRO), Chicago, IL.

Professional Service

Journal Reviewer

IEEE Transactions on Computers (TOC), 2023

Teaching

Teaching Assistant for CIS505 Distributed Systems, University of Pennsylvania, Fall 2020, Fall 2021

Skills

Programming: C/C++, Python, JavaScript, Rust, System Verilog

Language: Chinese (native), English (professional working proficiency), Japanese (intermediate level)