```
HN3
 2.3
   sub x30, x28, x29 // x30 ← i-j
   slli x30, x30, 3
                     11 x30 = 8*(1-1)
   add x30, x30, x10 11 x30 <- A + 8*(i-j)
   ld x30, 0(x30) // x30 ← A[i-j]
   Sd x30,64(XII) 11 BE8] < AFI-j]
 2.4
      BIgJ = AIf + iJ + AIfJ
2.7 slli x30, x28, 3
                         11 x30 = 8xi
     add x30, x30, x10
                         11 x30 = &ACI]
     Ld x30, 0(x30)
                         11 x30 = ACi]
    slli x31, x29, 3
                         11 x31 = 8xi
    add x31, x31, x10
                        1/ x31 = &ACj]
    Ld x31, 0(x31)
                        11 x31 = AC]]
   add x30, x30, x31
                        11 x30 = ACI] + ACI]
    Sd x30, 64(x11)
                         11 BE8] = AEI] + ACI]
                                  imm12
                                   rs2
2.8
      f=2*&A
                                                       funct3
                                                                      opcode
                                               rs1
                                                                rd
                                                       3 bits
                                                              5 bits
                                                                      7 bits
                                      5bits
                                              5 bits
                             7 bits
2.9
     addi x30, x10, 8
                                                      000
                                                                     00/00/1
                           0000000
                                                              11110
                                     01000
                                              01010
      addi x31, x10,0
                                             0/0/0
                                                       000
                                                              7/17/
                                                                     0010011
                           0000000
                                     00000
                                                              1100010 0000011
                                             11110
                                                       111
           x31, 0(x30)
                                    11111
      sd
                          0000000
                                                      0//
                                                              11110
                                                                    0000011
           x30, 0(x30)
                                             11110
      ld
                          0000000
                                    00000
                          0000000
                                                                    0110011
          XS , X30 , X31
                                    11111
                                             11110
                                                      000
                                                             00101
     add
```

```
2.29
  argument n - x10
  result fib(n) - X/1
  Fib: addi sp, sp, -16
       sd x1,8(sp) //save return address
       sd x10,0(sp) Il save argument n
      add x5, x0,2 1/ x5 = 2
      bge x10, x5, L1 11 if n32, go to L1
      addi sp, sp, 16 // recover sp
     jalr x0,0(x1)
  L1: addi x10, x10, +11 x10=n-1
     ial XI, Fib // XII = fib (D-1)
     add xb, x11, x0 11 x6 = fib(n-1)
    addi x10, x10, -1 11 x10 = n-2
     jal \times 1, Fib || \times || = fib(n-2)
     add x11, x11, x6 1/ x11 = fib(n-1)+fib(n-2)
    ld ×10,0(sp)
     ld x1,8(sp) 11 restore argument and return address
    addi sp, sp, 16
    jalr x0,0(x1)
```