

Computer Organization 2023 Lab 4

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Finished part:

CO_P4_test_data1.txt

```
=====
(A) basic score:      75 / 75
=====
```

```
=====
    Congratulation. You pass TA's pattern
=====
```

```
=====
$stop called at time : 270 ns : File "C:/Users/asd91/Desktop/念書/三下/計組/CO_Lab4/CO_Lab4_Template/TestBench.v" Line 585
=====
```

Problems you met and solutions:

在 simple_signgle_CPU.v 那部份要把助教預設好的線，接到各模組的時候花了點時間。要思考這條線是甚麼東西，再照 SPEC 的範例圖接線。(但最後好像有的線用不到)

If you create additional module, please give a short explanation here:

ALU 部分使用了和 HW3 一樣的架構，有多加了 ALU_1bit.v 以及 Full_adder.v。