Status report OpenCL setup, board test

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PLATFORM

System

- Core i7 6500u, 8GB RAM
- Windows 10 Pro x64
- Windows Subsystem for Linux
- PuTTY

QUARTUS STACK

- Quartus 2 14.1.0.186
- AOCL 14.1.0.186
- SoC EDS 14.1.0.186

PREQUISITES

- 1) Setup Altera Quartus stack
- 2) Setup USER¹ environment variables
 - a. Altera OpenCL License (Create LM_LICENSE_FILE)
 - b. Altera OpenCL binaries (Add to PATH)
 - c. Altera Board package path (AOCL_BOARD_PACKAGE_ROOT)

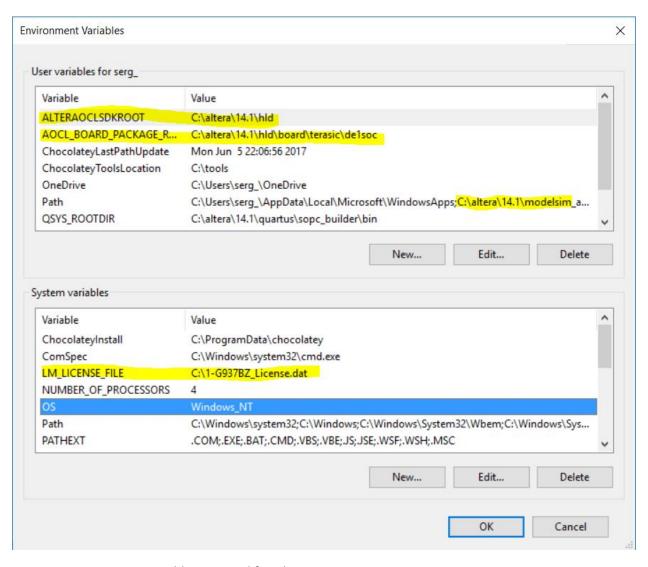


Figure 1 Environment variables required for Altera OpenCL

¹ Systemwide variables won't work without adapting Altera's own environment variables

COMPILATION (BOARD TEST)

- 1) Go to "C:\altera\14.1\hld\board\terasic\de1soc\examples\boardtest"
- 2) Prompt> aoc device/boardtest.cl --sw-dimm-partition -o bin/boardtest.aocx
- 3) Open "C:\altera\14.1\embedded\Embedded_Command_Shell.bat" embedded shell
 - a. A Cygwin ²shell opens
 - b. Go to /cygdrive/c/altera/14.1/hld/board/terasic/de1soc/examples/boardtest³
 - c. Build the board test project (See image x)

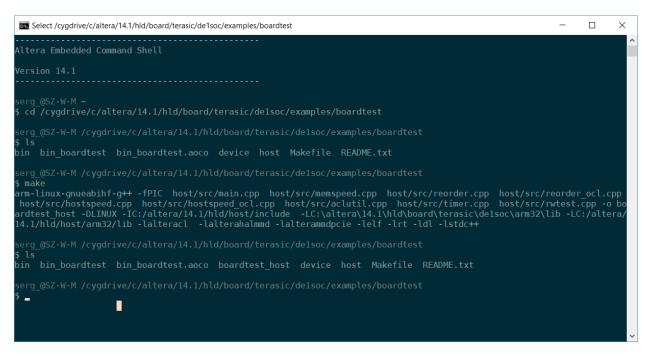


Figure 2 Altera Embedded Command Shell

² POSIX emulation layer + tools for windows, https://www.cygwin.com/

³ To access a host path via Cygwin shell /cygdrive "prefix" is required (mountpoint)

BOARD PREPARATION

Prior to deployment the DE1-SoC has to be up and running.

A prepared flash card with a linux base system is required.

- 1) Connect the power, USB/UART and Ethernet connectors.
- 2) Start PuTTY, select the proper COM port and set the baudrate to 115200
- 3) Log in as "root". A password is not required.
- 4) Set up a (static) ip address, e.g 192.168.0.123

```
Putty
                                                                                                                      П
                                                                                                                             X
  OpenCL Notification Callback: Requested memory object size exceeds device limi
 Min time:
 Max time:
               395376800
 Avg time:
Finished 4000 iterations with 0 errors
SNOOP TEST PASSED
root@socfpga:~# ifconfig
          Link encap:Ethernet HWaddr 00:07:ed:00:00:03
inet addr:192.168.0.123 Bcast:192.168.0.255 Mask:255.255.255.0
UP BROADCAST RUNNING MULTICAST MTU:1500 Metric:1
          RX packets:2477 errors:0 dropped:0 overruns:0 frame:0
          TX packets:644 errors:0 dropped:0 overruns:0 carrier:0
          collisions:0 txqueuelen:1000
          RX bytes:3332371 (3.1 MiB) TX bytes:41635 (40.6 KiB)
          Interrupt:152 Base address:0x4000
          Link encap:Local Loopback inet addr:127.0.0.1 Mask:255.0.0.0
10
          inet6 addr: ::1/128 Scope:Host
          UP LOOPBACK RUNNING MTU:65536 Metric:1
          RX packets:0 errors:0 dropped:0 overruns:0 frame:0
          TX packets:0 errors:0 dropped:0 overruns:0 carrier:0
oot@socfpga:~#
```

Figure 3 Board's IP configuration

DEPLOYMENT

From a terminal:

- Copy ACL device configuration: scp bin/boardtest.aocx root@192.168.0.123:/home/root
- Copy OpenCL host application: scp boardtest_host root@192.168.0.123:/home/root

These commandos copy files to the target board via SSH⁴.



Figure 4 Copy necessary files to the board

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⁴ Secure Shell

EXECUTION

- Make boardtest_host executable: chmod +x ./boardtest_host
- Prepare the OpenCL environment: source ./init_opencl.sh
- Program the device: aocl program /dev/acl0 boardtest.aocx
- Execute the OpenCL host application: ./boardtest_host

```
COM3 - PuTTY
         TX packets:0 errors:0 dropped:0 overruns:0 carrier:0
         collisions:0 txqueuelen:0
         RX bytes:0 (0.0 B) TX bytes:0 (0.0 B)
root@socfpga:~# ssh-keygen -t rsa
Generating public/private rsa key pair.
Enter file in which to save the key (/home/root/.ssh/id_rsa):
root@socfpga:~# ls
                boardtest.aocx init_opencl.sh
README
                                                  swapper
poardtest
                                opencl_arm32_rte vector_Add
coot@socfpga:~# 1s
              boardtest host opencl arm32 rte
ooardtest
              helloworld swapper
init_opencl.sh vector_Add
boardtest.aocx
root@socfpga:~# chmo +x boardtest host
-sh: chmo: command not found
root@socfpga:~# chmod +x boardtest_host
root@socfpga:~# source ./init_opencl.sh
coot@socfpga:~# aocl program /dev/acl0 boardtest.aocx
aocl program: Running reprogram from /home/root/opencl arm32 rte/board/c5soc/arm
32/bin
Reprogramming was successful!
root@socfpga:~# ./boardtest_host
```

Figure 5 Steps prior to execute the OpenCL program on the board

```
COM3 - PuTTY
                                                                                                       П
OpenCL Notification Callback: Requested memory object size exceeds device limi
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 OpenCL Notification Callback: Requested memory object size exceeds device limi
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OpenCL Notification Callback: Requested memory object size exceeds device limi
Min time:
 Max time:
Finished 4000 iterations with 0 errors
SNOOP TEST PASSED
:oot@socfpga:~#
```

Figure 6 OpenCL board test passed

CONCLUSION

- At least one system (Sergej Zuyev's) is properly configured.
- We can deploy and run OpenCL programs on the provided DE1-SoC
- We can use this knowledge for our own LBP-Operator implementation