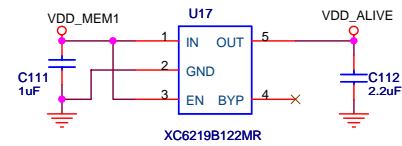
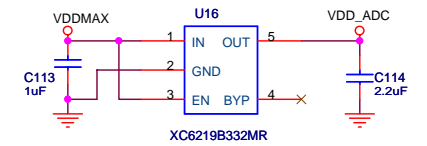


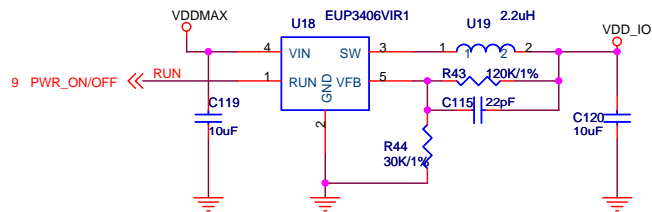
1.2V for PLL INT ARM and OTGI



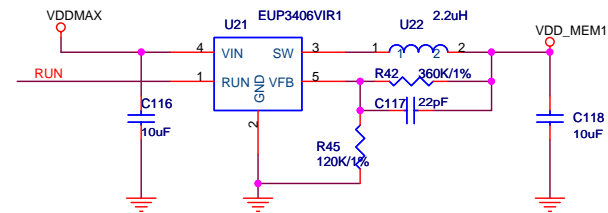
1.2V for ALIVE



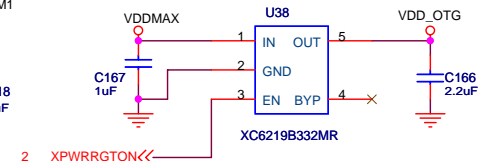
3.3V FOR ADC or DAC



3.3V FOR SYSTEM

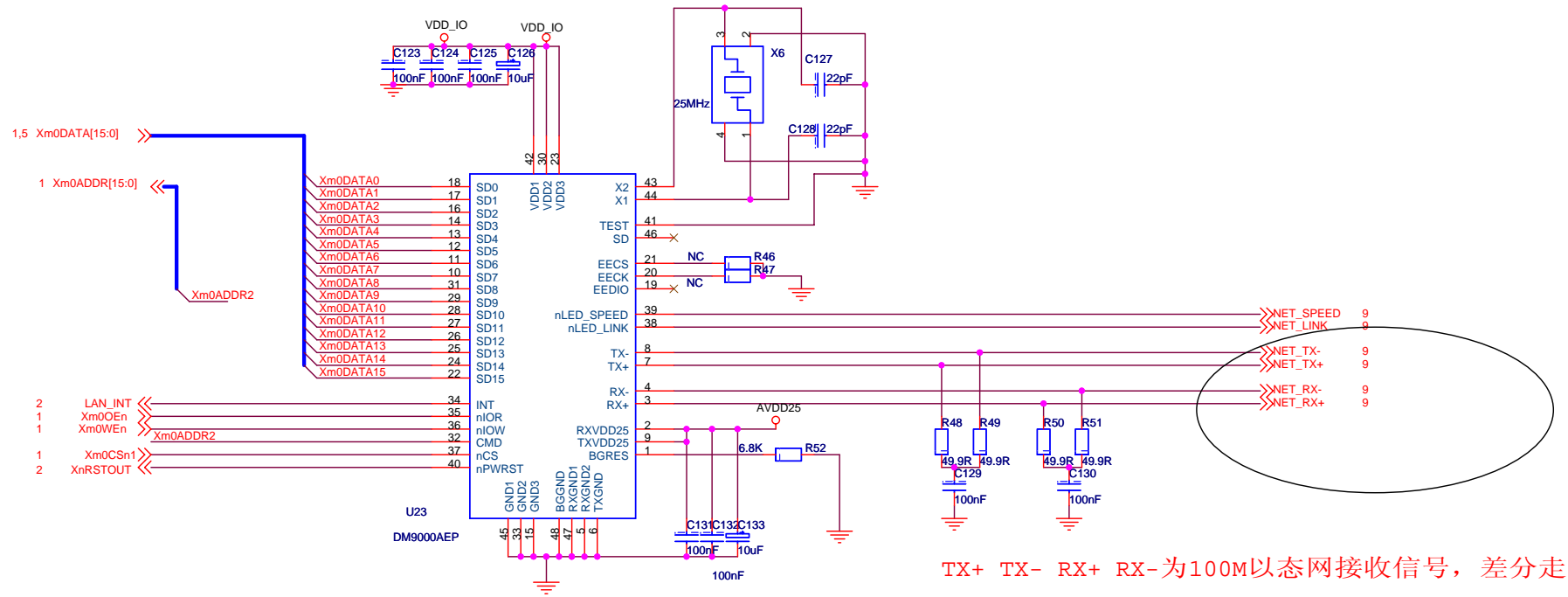


1.8V for mobile DDR



电源和LDO布线，注意线宽线距

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Power			
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