

	D		
Forward MUX	MF_GRFRD1_D	MF_GRFRD2_D	MF_ALUA_E
sequence_input forward_input	RD1	RD2	V1_E_left
	ALUResult_M	ALUResult_M	ALUResult_M
	pc4_M	pc4_M	pc4_M
	hi_M	hi_M	hi_M
	lo_M	lo_M	lo_M
	ALUResult_W	ALUResult_W	ALUResult_W
	DM_RD_W	DM_RD_W	DM_RD_W
	pc4_W	pc4_W	pc4_W
	hi_W	hi_W	hi_W
	lo_W	lo_W	lo_W
select	MF_GRFRD1_D_sel	MF_GRFRD2_D_sel	MF_ALUA_E_sel
output	V1	V2	V1_E_right

E		M
MF_ALUB_E	MF_MemData_E	MF_DMWD_M
V2_E_left	V2_E_left	V2_M
ALUResult_M	ALUResult_M	ALUResult_W
pc4_M	pc4_M	DM_RD_W
hi_M	hi_M	pc4_W
lo_M	lo_M	hi_W
ALUResult_W	ALUResult_W	lo_W
DM_RD_W	DM_RD_W	
pc4_W	pc4_W	
hi_W	hi_W	
lo_W	lo_W	
MF_ALUB_E_sel	MF_MemData_E_sel	MF_DMWD_M_sel
V2_E_right_ALUB	V2_E_right2_RegM	MemData