

指令	opcode	funct	REGorM EM	MemW rite	NPCo p	EXTO p	A1o p	A2 op	A3o p	REGo p	RegWri te	ALU_A op	ALU_Bo p	ALUO p
addu	000000	100001	0	0	000	0	00	0	00	00	1	0	00	0010
subu	000000	100011	0	0	000	0	00	0	00	00	1	0	00	0011
addi	001000		0	0	000	0	00	0	01	00	1	0	01	0010
ori	001101		0	0	000	1	00	0	01	00	1	0	01	0001
lw	100011		1	0	000	0	00	0	01	00	1	0	01	0010
sw	101011		0	1	000	0	00	0	00	00	0	0	01	0010
beq	000100		0	0	001	0	00	0	00	00	0	0	00	0000
lui	001111		0	0	000	0	00	0	01	00	1	0	01	0000
sll	000000	000000	0	0	000	0	01	0	00	00	1	0	00	0100
srl	000000	000010	0	0	000	0	01	0	00	00	1	0	00	0101
sra	000000	000011	0	0	000	0	01	0	00	00	1	0	00	0110
slt	000000	101010	0	0	000	0	00	0	00	00	1	0	00	0111
j	000010		0	0	010	0	00	0	00	00	0	0	00	0000
jal	000011		0	0	010	0	00	0	10	10	1	0	00	0000
jalr	000000	001001	0	0	011	0	00	0	00	10	1	0	00	0000
jr	000000	001000	0	0	011	0	00	0	00	00	0	0	00	0000