F		REG_D	D					
PC	IM		实例化DECODER	GRF	EXT	CMP	NPC	
c1k	рс Б	IR F	IR D	clk	IR D	V1	pc4 D	
reset	IR F	pc4 F	A1 D	reset	EXTOp	V2	pc8 D	
npc		pc8_F	A2_D	A1_D	EXTResult	zero	IR_D	
pc_en		D_en	EXTOp	A2_D			NPC0p	
pc_F		c1k	NPC0p	A3_W			zero	
pc4_F		reset		RegData			GPR_rs(V1)	
pc8 F		IR D		RegWrite W			npc	
		pc4_D		pc4_D				
		pc8_D		pc8_D				
				RD1				
				RD2				
			Tuse_RS0					
			Tuse_RS1					
			Tuse_RT0					
			Tuse RT1					
			Tuse_RT2					
			Tnew_D					

REG_E	Е		REG_M	
	实例化DECODER	ALU		实例化DECODER
IR D	IR E	ALUSrcA	IR E	IR M
V1	ALU Aop	ALUSrcB	V2 E	MemWrite M
V2	ALU_Bop	ALU0p	ALUResult_E	
EXTResult	ALU0p	shamt	pc4_E	
pc4_D	shamt_E	ALUResult_E	pc8_E	
pc8_D			c1k	
c1k			reset	
reset			IR_M	
E_clr			ALUResult_M	
IR_E			V2_M	
V1_E			$pc4_M$	
V2_E			pc8_M	
EXTResult_E				
pc4_E				
pc8_E				
	Tnew_D			Tnew_E
	Tnew_E			Tnew_M

M	RI	EG_W	
DM		实例化DECODER	
clk	IR M	IR W	
reset	ALUResult M	A3 W	
MemAddr=ALUResult_E	c1k	RegWrite_W	
MemData=V2_M	reset		
MemWrite_M	$pc4_M$		
pc4_M	pc8_M		
DMRD	DMRD		
	IR_W		
	ALUResult_W		
	pc4_W		
	pc8_W		
	DMRD_W		
		Tnew_M	
		Tnew_W	