

F		REG_D	D				
PC	IM		实例化DECODER	GRF	EXT	CMP	NPC
clk	pc F	IR F	IR D	clk	IR D	V1	pc4 D
reset	IR F	pc4 F	A1 D	reset	EXTOp	V2	pc8 D
npc		pc8 F	A2 D	A1 D	EXTResult	zero	IR D
pc_en		D_en	EXTOp	A2 D			NPCOp
pc F		clk	NPCOp	A3 W			zero
pc4 F		reset		RegData			GPR rs(V1)
pc8 F		IR D		RegWrite W			npc
		pc4 D		pc4 D			
		pc8 D		pc8 D			
				RD1			
				RD2			
			Tuse RS0				
			Tuse RS1				
			Tuse RT0				
			Tuse RT1				
			Tuse RT2				
			Tnew D				

[illegible]

[illegible]