指令	opcode	funct	RT	REGorMEM	MemWrite	NPCop	EXTOp	A1op	A2op	A3op	REGop	RegWrite	ALU_Aop	ALU_Bop
and	000000	100100		0	0	000	0	00	0	00	000	1	0	00
or	000000	100101		0	0	000	0	00	0	00	000	1	0	00
xor	000000	100110		0	0	000	0	00	0	00	000	1	0	00
nor	000000	100111		0	0	000	0	00	0	00	000	1	0	00
add	000000	100000		0	0	000	0	00	0	00	000	1	0	00
addu	000000	100001		0	0	000	0	00	0	00	000	1	0	00
sub	000000	100010		0	0	000	0	00	0	00	000	1	0	00
subu	000000	100011		0	0	000	0	00	0	00	000	1	0	00
addi	001000			0	0	000	0	00	0	01	000	1	0	01
addiu	001001			0	0	000	0	00	0	01	000	1	0	01
andi	001100			0	0	000	1	00	0	01	000	1	0	01
ori	001101			0	0	000	1	00	0	01	000	1	0	01
xori	001110			0	0	000	1	00	0	01	000	1	0	01
slti	001010			0	0	000	0	00	0	01	000	1	0	01
sltiu	001011			0	0	000	0	00	0	01	000	1	0	01
指令	opcode	funct	RT	REGorMEM	MemWrite	NPCop	EXTOp	A1op	A2op	A3op	REGop	RegWrite	ALU_Aop	ALU_Bop
lw	100011			1	0	000	0	00	0	01	000	1	0	01
lb	100000			1	0	000	0	00	0	01	000	1	0	01
lbu	100100			1	0	000	0	00	0	01	000	1	0	01
lh	100001			1	0	000	0	00	0	01	000	1	0	01
lhu	101001			1	0	000	0	00	0	01	000	1	0	01
sw	101011			0	1	000	0	00	0	00	000	0	0	01
sb	101000			0	1	000	0	00	0	00	000	0	0	01
sh	101001			0	1	000	0	00	0	00	000	0	0	01
beq	000100			0	0	001	0	00	0	00	000	0	0	00
bne	000101			0	0	001	0	00	0	00	000	0	0	00
blez	000110			0	0	001	0	00	0	00	000	0	0	00
bgtz	000111			0	0	001	0	00	0	00	000	0	0	00
bltz	000001		00000	0	0	001	0	00	0	00	000	0	0	00
bgez	000001		00001	0	0	001	0	00	0	00	000	0	0	00
lui	001111			0	0	000	0	00	0	01	000	1	0	01
sll	000000	000000		0	0	000	0	00	0	00	000	1	0	00
srl	000000	000010		0	0	000	0	00	0	00	000	1	0	00
sra	000000	000011		0	0	000	0	00	0	00	000	1	0	00
sllv	000000	000100		0	0	000	0	00	0	00	000	1	0	00
srlv	000000	000110		0	0	000	0	00	0	00	000	1	0	00
srav	000000	000111		0	0	000	0	00	0	00	000	1	0	00
slt	000000	101010		0	0	000	0	00	0	00	000	1	0	00
sltu	000000	101011		0	0	000	0	00	0	00	000	1	0	00
j	000010			0	0	010	0	00	0	00	000	0	0	00
jal	000011			0	0	010	0	00	0	10	001	1	0	00
jalr	000000	001001		0	0	011	0	00	0	00	001	1	0	00
jr	000000	001000		0	0	011	0	00	0	00	000	0	0	00
指令	opcode	funct	RT	REGorMEM	MemWrite	NPCop	EXTOp	A1op	A2op	A3op	REGop	RegWrite	ALU_Aop	ALU_Bop
mult	000000	011000		0	0	000	000	00	0	00	000	0		
multu	000000	011001		0	0	000	000	00	0	00	000	0		
div	000000	011010		0	0	000	000	00	0	00	000	0		
divu	000000	011011		0	0	000	000	00	0	00	000	0		
mfhi	000000	010000		0	0	000	000	00	0	00	010	1		
mflo	000000	010010		0	0	000	000	00	0	00	011	1		

mthi	000000	010001		0	0	000	000	00	0	00	000	0		
mtlo	000000	010011		0	0	000	000	00	0	00	000	0		

ALUOp
0000
0001
1100
1101
0010
0010
0011
0011
0010
0010
0000
0001
1100
0111
1110

ALUOp
0010
0010
0010
0010
0010
0010
0010
0010
0000
0000
0000
0000
0000
0000
1000
0100
0101
0110
1001
1010
1011
0111
1110
0000
0000
0000
0000

ALUOp	MDUOp	start
	0000	1
	0001	1
	0010	1
	0011	1
	1111	0
	1111	0

	0100	0
	0101	0