

# Hyperlinked Summary of Predictive Models

- To access the hyperlinks on this summary slide, please download the PDF file first.
- Multiple fingers cannot be set for MOSFET when using the PTM model; multiplier can be used instead.
- Write *simulator lang = spice* at top of the .pm (model) file to simulate.

Developer	Layout availability	Device type	Device flavors	Technology node (nm)	Latest Release Version
<a href="#">Nanoscale Integration &amp; Modeling group @ ASU (PTM)</a>	No	Bulk CMOS	-	<a href="#">130</a> , <a href="#">90</a> , <a href="#">65</a>	1.0
			HP (High-Performance)	<a href="#">45</a> , <a href="#">32</a> , <a href="#">22</a> , <a href="#">16</a>	2.1
			LP (Low-Power)	<a href="#">45</a> , <a href="#">32</a> , <a href="#">22</a> , <a href="#">16</a>	2.1
		<a href="#">Multi-gate transistors</a>	HP (High-Performance)	NMOS: <a href="#">20</a> , <a href="#">16</a> , <a href="#">14</a> , <a href="#">10</a> , <a href="#">7</a>	-
				PMOS: <a href="#">20</a> , <a href="#">16</a> , <a href="#">14</a> , <a href="#">10</a> , <a href="#">7</a>	
			LSTP (Low-Standby Power)	NMOS: <a href="#">20</a> , <a href="#">16</a> , <a href="#">14</a> , <a href="#">10</a> , <a href="#">7</a>	-
				PMOS: <a href="#">20</a> , <a href="#">16</a> , <a href="#">14</a> , <a href="#">10</a> , <a href="#">7</a>	
<a href="#">ASU + ARM collaboration (ASAP)*</a>	Yes	Multi-gate transistors	4 threshold voltage, 3 corner	7	1.6

\* downloading requires registration with academic email.