Programming Assignment Submission Guidelines

CS141 Spring 2019

Submission Guidelines

General guidelines for preparing your Xilinx project:

- Make sure that you only have one module per source file.
- Comment every module so that graders can see the overall structure of your design and test-benches
- Rerun simulation/synthesis to make sure that your final submission still works.
- Run `Project->Clean Up Project Files`, and close Xilinx.

All submitted files should be a compressed archive named:

`firstnameYou_lastnameYou_firstnamePartner_lastnamePartner_<assignment>.<tar.gz/zip>`.

For example: `john_smith_jane_doe_pa1.tar.gz` or `john_smith_jane_doe_pa1.zip`.

Compressing a folder for submission:

1. Example for tar:

To compress a folder using tar: `tar -czvf archive_name.tar.gz folder_to_archive` To decompress a file using tar: `tar -xzvf archive_name.tar.gz uncompressed_output_folder_name`

2. Example for zip:

Windows: right click on the top-most folder and select `Send to -> Compressed (zip) file`.

Command line on Linux: To compress a folder using zip: `zip -r archive_name.zip folder_to_archive`. To decompress a folder using zip: `unzip archive_name.zip`.

Sketches

If you are asked to make a sketch for the assignment you may include pictures or scans of the sketch in the zipfile/tarball.

Description

If you are asked to make a writeup for the assignment please include the writeup in the zipfile/tarball in PDF form.

Recommendations

Git

I recommend using `git` to keep track of your code. However Xilinx creates many extraneous files that you don't want to commit to your repository but its hard to know which ones are important and which

ones aren't. You can find a good `.gitignore` file meant for Xilinx projects here. Since downloading git on your virtual machine is a pain, you should also consider creating a shared folder, putting all your programming assignments in there, and using git from your host machine for version control while using Xilinx on the virtual machine for editing.