

APSD result sometimes is wrong as SDP when insert DCP after slow insertion.

Below change is to rerun APSD when it is detected as SDP.

```
diff --git a/drivers/power/supply/qcom/smb-lib.c b/drivers/power/supply/qcom/smb-lib.c
index 42833a2..e4ab41b 100644
--- a/drivers/power/supply/qcom/smb-lib.c
+++ b/drivers/power/supply/qcom/smb-lib.c
@@ -685,6 +685,7 @@ static void smbllib_uusb_removal(struct smb_charger *chg)
chg->voltage_max_uv = MICRO_5V;
chg->usb_icl_delta_uA = 0;
chg->pulse_cnt = 0;
+ chg->uusb_apsd_rerun_done = false;

/* clear USB ICL vote for USB_PSY_VOTER */
rc = vote(chg->usb_icl_votable, USB_PSY_VOTER, false, 0);
@@ -752,6 +753,7 @@ int smbllib_rerun_apsd_if_required(struct smb_charger *chg)
rc);
}

+ chg->uusb_apsd_rerun_done = true;
smbllib_rerun_apsd(chg);

return 0;
@@ -3482,6 +3484,17 @@ irqreturn_t smbllib_handle_usb_source_change(int irq, void *data)
}
smbllib_dbg(chg, PR_REGISTER, "APSD_STATUS = 0x%02x\n", stat);

+ if (chg->micro_usb_mode && (stat & APSD_DTC_STATUS_DONE_BIT)
+ && !chg->uusb_apsd_rerun_done) {
+ /*
+ * Force re-run APSD to handle slow insertion related
+ * charger-mis-detection.
+ */
+ chg->uusb_apsd_rerun_done = true;
+ smbllib_rerun_apsd(chg);
+ return IRQ_HANDLED;
}
```

```
+ }  
+  
smblib_handle_apsd_done(chg,  
(bool)(stat & APSD_DTC_STATUS_DONE_BIT));
```

```
diff --git a/drivers/power/supply/qcom/smb-lib.h b/drivers/power/supply/qcom/smb-lib.h  
index 49b9d3d..4b277c4 100644
```

```
--- a/drivers/power/supply/qcom/smb-lib.h  
+++ b/drivers/power/supply/qcom/smb-lib.h  
@@ -312,6 +312,7 @@ struct smb_charger {  
int vconn_attempts;  
int default_icl_ua;  
int otg_cl_ua;  
+ bool uusb_apsd_rerun_done;
```

```
/* workaround flag */  
u32 wa_flags;  
--  
1.8.2.1
```

Qualcomm

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