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Read/write SPMI register to PMIC [HOW-TO] - [INSTRUCTION] - [SPMI] - [KERNEL – LINUX]

Solution Number 00030485

Please Note:

If Qualcomm documentation is referenced in this solution, your access to it is based on your company's

Language Key Words

Detail Information

Solution Title Read/write SPMI register to PMIC [HOW-TO] - [INSTRUCTION] - [SPMI] - [KERNEL – LINUX]

Solution Details Describes how a register reads/writes to a specific PMIC over SPMI on chipsets with multiple PMICs

Solution: The chipsets with multiple PMICs can be written to or read from the SPMI based on their SPMI slave IDs. The primary PMIC of a chipset uses SPMI slave IDs 0 and 1. If the chipset has a PMI (interface/charger PMIC), then the PMI uses SPMI slave IDs 2 and 3. If the chipset has a secondary PMIC that is not a PMI (interface/charger PMIC), then the secondary PMIC uses SPMI slave IDs 4 and 5.

For example:

When PM8941 uses slave IDs 0 and 1; PM8841 uses slave IDs 4 and 5:

ADB shell register reads from PM8941 0x810 (slave ID 0):
adb shell echo 0x810 > /sys/kernel/debug/spmi/spmi-0/address
adb shell cat /sys/kernel/debug/spmi/spmi-0/data

ADB shell register reads from PM8941 0x14F10 (slave ID 1):
adb shell echo 0x14F10 > /sys/kernel/debug/spmi/spmi-0/address
adb shell cat /sys/kernel/debug/spmi/spmi-0/data

ADB shell register reads from PM8841 0x810 (slave ID 4):
adb shell echo 0x40810 > /sys/kernel/debug/spmi/spmi-0/address
• adb shell cat /sys/kernel/debug/spmi/spmi-0/data

- When PM8994 uses slave IDs 0 and 1; PMI8994 uses slave IDs 2 and 3:

ADB shell register reads from PM8994 0x810 (slave ID 0):
adb shell echo 0x810 > /sys/kernel/debug/spmi/spmi-0/address
adb shell cat /sys/kernel/debug/spmi/spmi-0/data

ADB shell register reads from PMI8994 0x810 (slave ID 2):
adb shell echo 0x20810 > /sys/kernel/debug/spmi/spmi-0/address
adb shell cat /sys/kernel/debug/spmi/spmi-0/data

Applicable Products PM8110, PM8226, PM8841, PM8916, PM8916-1, PM8926, PM8941, PM8994, PMA8084-2, PMA8084-4, PMC8974, PMD9635, PMD9645, PMI8994