

[B-Family][PMIC] Detail info of PMIC dts setting to registers

Solution Number	00029640
<div>Please Note:</div> <div>If Qualcomm documentation is referenced in this solution, your access to it is based on your company's</div>	
Language Key Words	
Detail Information	
Solution Title	[B-Family][PMIC] Detail info of PMIC dts setting to registers
Solution Details	<div>The PMIC GPIO register offset is same on different module and different chips.</div> <div>Here is the table that maps the GPIO dtsi settings to the PMIC register</div> <div>40[6:4] => qcom,mode</div> <div>40[3:0] => qcom,src-sel</div> <div>41[2:0] => qcom,vin-sel</div> <div>42[2:0] => qcom,pull</div> <div>45[5:4] => qcom,output-type</div> <div>45[1:0] => qcom,out-strength</div> <div>You can verify the settings by dump the PMIC registers like following:</div> <div>adb root</div> <div>adb shell</div> <div>mount -t debugfs debugfs /sys/kernel/debug</div> <div>cd /sys/kernel/debug/spmi/spmi-0</div> <div>echo 0x10 > count</div> <div>echo 0x**40 > address</div> <div>/*The ** at above is the PMIC base address high part.*/</div> <div>cat data</div>
Applicable Products	PM8026, PM8110, PM8226, PM8841, PM8909, PM8916, PM8926, PM8941, PMD9635, PMD9645, PMI8994