

follow below steps to disable HVDCP, to clear bit2 of 0x1362

1. in XBL: pm_driver_post_init():

```
+pm_comm_write_byte_mask(0, 0x1362, 0x04, 0, 0); //bit2
```

2. in HLOS: /drivers/power/supply/qcom/smb-lib.c

```
static int smblib_hvdcpc_enable_vote_callback(struct votable *votable,void *data,int hvdcpc_enable,  
const char *client)
```

```
{
```

```
...
```

```
--if (!hvdcpc_enable)
```

```
--val = HVDCPC_EN_BIT;
```

```
++val = 0;
```

```
...
```

```
}
```

Qualcomm
2019-04-09 04:20:19 PDT
zk_sw@wingtech.com