# OpenCL based HMC Development Snapshot

Lars Zeidlewicz

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Institut für Theoretische Physik Goethe-Universität Frankfurt

#### **Outline**

- LOEWE-CSC
- Our code: desired features
- Code & project management
- Code overview
- Some words on OpenCL

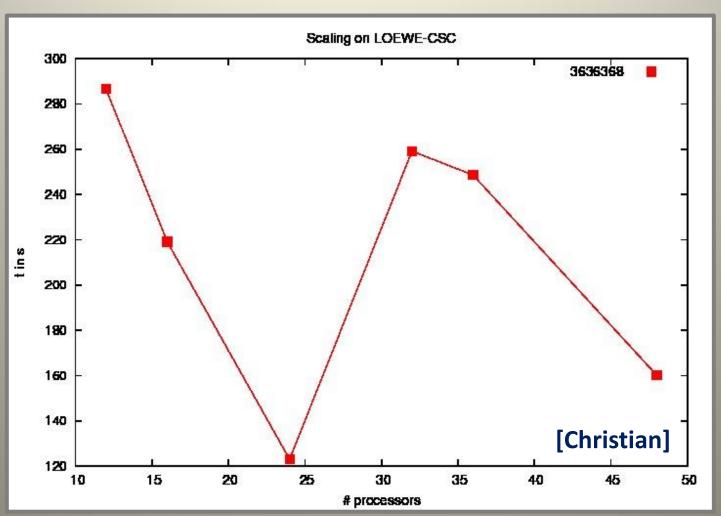
#### Some numbers...

- Top500 rank 22 (Nov 2010)
   (285 Tflops, #2 in Germany)
- Green500 rank 8
   (741 Mflops/W, #4 in Germany)
- About 800 nodes with
  - 2 AMD Opteron 12 core
  - 1 AMD ATI Radeon 5870 (Cypress)

#### **Status**

- Testing phase
- Monthly CSC-admin meetings
- Current issues:
  - Queue: Large jobs vs. single-core
     (Jobs wait for days and weeks)
  - Infiniband problems
  - 2 week downtime starts on Friday
- GPUs so-far unused

## **Status**



#### **SLURM**

 Admins hope to overcome queuing problems by switching to the SLURM scheduler

(https://computing.llnl.gov/linux/slurm/slurm.html)

- Today: 29 nodes
   After downtime: 100 nodes
- OpenMPI tested (only!)

#### **SLURM**

```
#! /bin/sh
#
#SBATCH --nodes=4
#SBATCH --time=1
#SBATCH --cpus-per-task=1
#SBATCH --job-name=testjob
#
OMP_NUM_THREADS=1
Srun -ln4 --mpi=none hostname
```

- How does that work for a "real" MPI program?
- SLURM can be loaded as a module:

```
module load slurm
module show slurm
(sinfo, sqeue,...)
```

- Working examples have been promised
- OpenMPI tested (only!)

# **Physics**

- SU(3) heatbath
- Fermion inverter (different actions)
- Hybrid Monte-Carlo
- Chemical potential
- Smearing

# **Programming**

- Hybrid structure
  - Within nodes
  - Beyond one node
- OOP for host code
- Large flexibility
  - Switches (single/double etc)
  - User defined types, access functions to faciliate changes

#### **GPUs**

Today:

Memory Management [Matthias]

Benchmarking

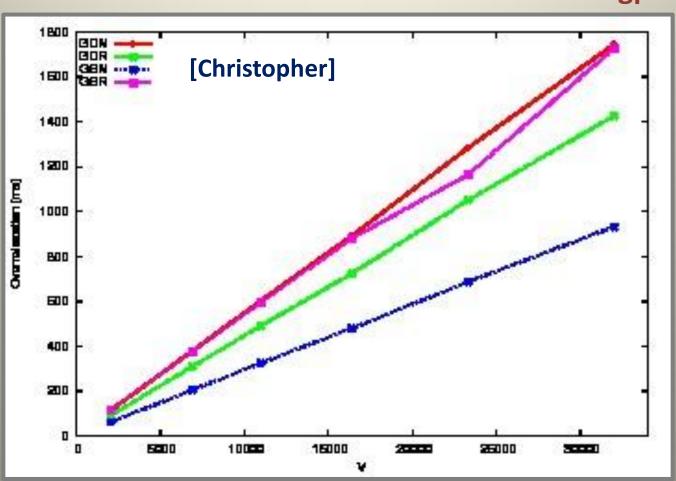
Workgroup sizes

[Matthias]

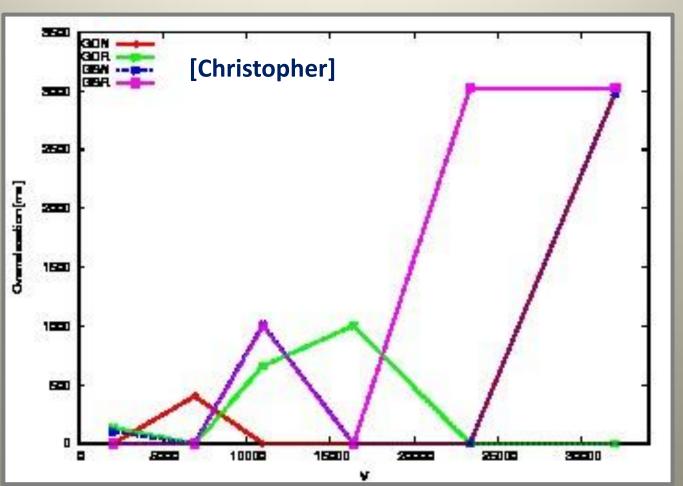
• Future:

Several devices

# GPUS benchmark – gpu-dev

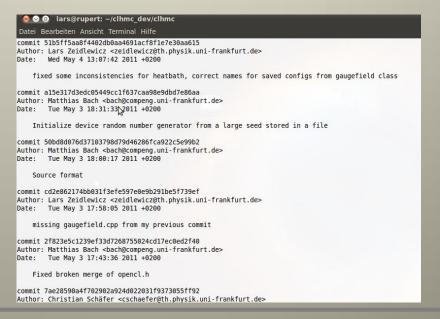


# GPUS benchmark - loewe



## git

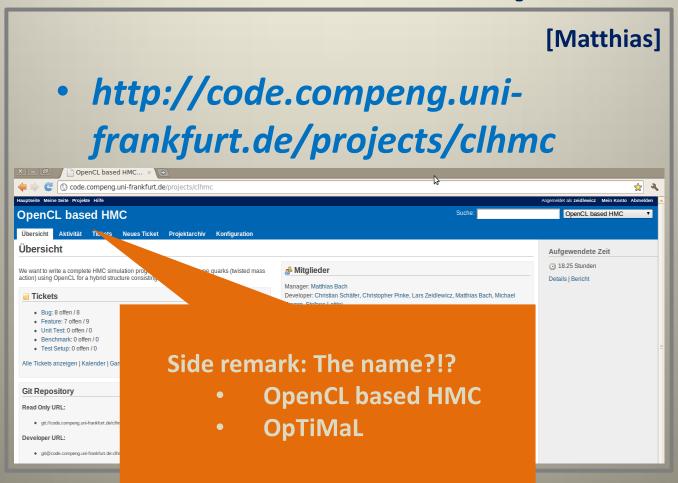
 Code is subject to version management by git



# **Online repository**



# **Online repository**



# Common style

[Matthias]

 Matthias has suggested to enforce a common coding style via

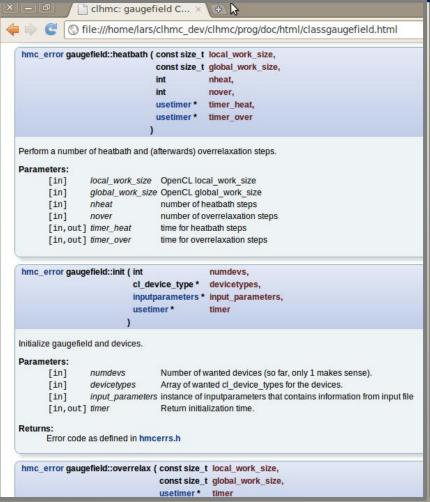
astyle --options=astylerc <FILENAME>

• To be discussed...

## Doxygen

```
File Edit Options Buffers Tools C Help
class gaugefield {
public:
* Initialize gaugefield and devices.host
* @param[in] numdevs Number of wanted devices (so far, only 1 makes sense).
* @param[in] devicetypes Array of wanted cl device types for the devices.
 * @param[in] input parameters instance of inputparameters that contains information from input file
* @param[in.out] timer Return initialization time. S@rupert: S
* @return Error code as defined in hmcerrs.h tillpromord.pdf
 hmc error init(int numdevs, cl device type* devicetypes, inputparameters* input parameters, usetimer* timer);
* Free gaugefield and device allocations.
*/s@rupert:~/clhmc dev/clhmc/prog/doc$ ls
 hmc error finalize(): stall cmake Doxyfile
/**koFiles CMakeLists.t
* Save gaugefield to file.mc/prog/docs make doc
*/e[3]: execvp: ./Doxvfile: Keine Berechtigung
 hmc error save(int number): Fehler 127
/**e[2]: *** [CMakeFiles/doc.dir/all] Fehler 2
* Copy gaugefield to devices (currently: to device).
* @param[in,out] timer2copy-time
*/s@rupert:~/clhmc dev/clhmc/prog/doc$ make cle
 hmc error copy gaugefield to devices(usetimer* timer):
* Copy random array to devices (currently: to device).
 * @param[in,out] timer/copy-timeg/docs doxywiza
(*/s@rupert:~/clhmc dev/clhmc/prog/doc$)
 hmc error copy rndarray to devices(hmc rndarray host rndarray, usetimer* timer);
* Copy random array from devices (currently: from device).
 * @param[in,out] timer copy-time
 hmc error copy rndarray from devices(hmc rndarray rndarray, usetimer* timer);
-UU-:----F1 qauqefield.h 14% L62 Git-master (C/l Abbrev)---------
```

Doxygen



Use doxygen format for comments

#### cmake

- Nice build-tool
- Have a look at clhmc/prog/INSTALL
- Creates Makefile:
  - make hmc
  - make heatbath

#### **Overview**

- Three different types of code:
  - Host management code

```
(→ C++)
```

Host functional code

```
(deprecated?)
```

Device functional code

```
(→ OpenCL)
```

#### Heatbath

- Based on Christopher's code
- Check: Boyd et al.:

Nucl. Phys. B469 (1996) 419-444

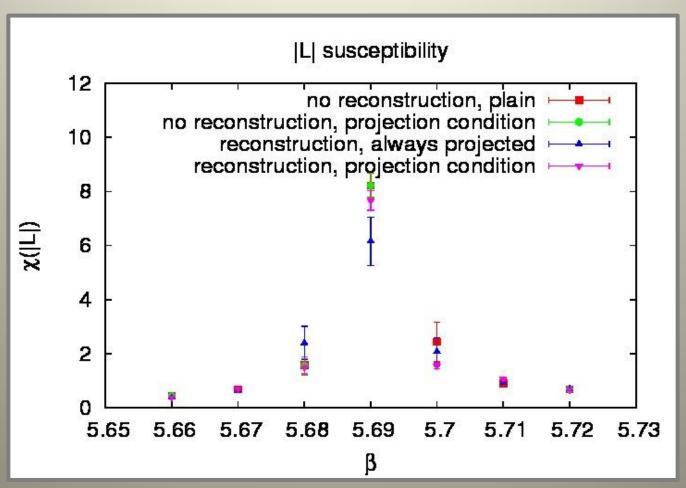
Host and device versions

still to check device version with final status

Current improvement:

Implement an optimal projection to SU(3) [Christian]

#### Heatbath



#### **HMC & inverter**

- Work in progress [Christopher, Steo]
- Host code inverter needs to be checked [Lars]
- So-far host code only
- Molecular dynamics force?

[Christopher]

## Management

```
• int main(){} in
   – hmc.cpp
     (mixes all progs by compiler flags)
   heatbath.cpp

    Management in classes:

   gaugefield
   opencl
```

## globaldefs.h

- NC, NSPIN, NDIM,...
- Right now: NT, NS
- Right now: work\_sizes
- Derived constants (VOLSPACE...)
- Available in all of the code

```
(sometimes useful:
```

kernel flag INKERNEL )

# types.h

```
hmc_float
```

- hmc complex
- hmc\_su3matrix
- •
- Switches: INKERNEL,
  USEDOUBLEPREC,
  RECONSTRUCT\_TWELVE

## inputparameters

Class inputparameters

Reads input file

• Look at method set defaults()

```
kappa=.125
mu=0.06
cgmax=1000
beta=6
#cgmax=3
prec=64
startcondition=cold
sourcefile=conf.save
thermalizationsteps=5
heatbathsteps=1000
```

# gaugefield.h

- ILDG storage format
- Gauge field is really the central object:
  - hmc trajectories
  - heatbath
  - inversion
- Gaugefield class: the place to manage inter-device communication

# opencl.h

- Wraps all device operations
- class opencl

```
openci Class Reference
#include <opencl.h>
List of all members.
Public Member Functions
                                                                opencl (cl. device type wanted, const size t ls, const size t gs, usetimer *timer, inputparameters *parameters)
                                hmc_error init (cl_device_type wanted_device_type, const size_t local_work_size, const size_t global_work_size, usetimer *timer, inputparameters *parameters)
                                hmc_error_copy_gaugefield_to_device (hmc_gaugefield *host_gaugefield, usetimer *timer)
                               hmc_error copy_rndarray_to_device (hmc_rndarray host_rndarray, usetimer *timer)
                                hmc_error copy_rndarray_from_device (hmc_rndarray rndarray, usetimer *timer)
                                hmc_error_get_gaugefield_from_device (hmc_gaugefield *host_gaugefield, usetimer *timer)
                                hmc_error run_heatbath (hmc_float beta, const size_t local_work_size, const size_t global_work_size, usetimer *timer)
                                hmc_error run_overrelax (hmc_float beta, const size_t local_work_size, const size_t global_work_size, usetimer *timer)
                                hmc error gaugeobservables (const size t local work size, const size t global work size, hmc float *plaq, hm
                               hmc_error finalize ()
Public Attributes
   std::vector< std::string > cl kernels file
```

#### Random numbers

- Random numbers on GPU are tricky; avoid cross correlations between threads
- Vectorized, parallel RNG?!?
- Right now: NR generator
- Implement RanLux [Matthias]

## **Operations**

 Implement operations for different types:

```
host_operations_complex
host_operations_fermionmatrix
host_operations_gaugefield
host_operations_spinor
host_operations_spinorfield
```

 Maybe we can get that a bit more sorted?

[currently: Steo, Christian?]

## **Testing function**

Deprecated?

```
oid testing correlator(hmc gaugefield* gf, inputparamete
oid testing fermionmatrix();
oid testing eoprec spinor();
roid print fullspinorfield(hmc spinor* in);
void testing spinor();
oid print su3mat(hmc su3matrix* A):
oid print staplemat(hmc staplematrix* A);
oid print linkplusstaplematrix(hmc gaugefield * in, int
oid testing su3mat();
oid testing gaugefield();
oid testing geometry();
oid testing su3matrix(hmc gaugefield * in, int spacepos,
oid testing adjoin(hmc gaugefield * in, int spacepos, in
oid testing det global(hmc gaugefield * in);
oid testing matrix ops(hmc gaugefield * in);
oid testing heatbath norandommat no123(hmc su3matrix * s
oid testing heatbath no123(hmc su3matrix * su3 in, hmc s
y, int * cter out);
oid testing heatbath(hmc su3matrix * su3 in, hmc staplem
 * cter out);
oid testing colorvector ops();
oid testing matrix spinor ops();
oid testing matrix spinor functions();
oid testing fermionmatrix functions();
oid unit spinor(hmc spinor * in);
oid i spinor(hmc spinor * in);
oid print spinor(hmc spinor * in);
oid set comp to one spinor(hmc spinor * in, int comp);
```

# Compile time flags

- USEDOUBLEPREC, USE\_EOPREC
- RECONSTRUCT\_TWELVE
- FERMIONS, HMC, BENCHMARKS
  - Untangle into different programs

# SU(3) reconstruct

M. Clark et al.

Comput. Phys. Commun., 181:1517-1528, 2010

$$\left(egin{array}{c} \mathbf{a} \ \mathbf{b} \ \mathbf{c} \end{array}
ight) = \left(egin{array}{ccc} a_1 & a_2 & a_3 \ b_1 & b_2 & b_3 \ c_1 & c_2 & c_3 \end{array}
ight)$$
  $\mathbf{c} = (\mathbf{a} imes \mathbf{b})^*$ 

• hmc\_complex
reconstruct\_su3(hmc\_su3matrix \*in,
int j);

#### Device code

- .cl files read in by opencl.cpp
- Compilation at runtime
- cl\_kernelsfiles.cl for debug info

#### Device code

```
/** @file
* Inclusion and definition of types and definitions required in the Device code.
//opencl header.cl
#ifdef USEDOUBLEPREC
#pragma OPENCL EXTENSION cl amd fp64 : enable
//#pragma OPENCL EXTENSION cl khr fp64 : enable
#endif
#include "globaldefs.h" //NDIM, NSPIN, NC
#include "types.h"
//!!CP: why is this here?
// #define VOLSPACE NSPACE*NSPACE*NSPACE
#define VOL4D VOLSPACE*NTIME
//for hmc ocl su3matrix
#ifdef RECONSTRUCT TWELVE
#define SU3SIZE NC*(NC-1)
#define STAPLEMATRIXSIZE NC*NC
#else
#define SU3SIZE NC*NC
```

#### Some remarks...

- Build-log
- Kernels, kernel args
- Pragmas (single, double)
- Optimise worksizes [Matthias...]

#### Some remarks...

```
init device #0
                                                                           3
OpenCL being initialized...
       CL PLATFORM NAME:
                             ATI Stream
                             Advanced Micro Devices, Inc.
       CL PLATFORM VENDOR:
       CL PLATFORM VERSION: OpenCL 1.1 ATI-Stream-v2.3 (451)
       1 device of wanted type has been found.
       Device information:
                CL DEVICE NAME:
                                  Intel(R) Core(TM) i5 CPU
                                                                 M 430 @ 2.27GHz
                CL DEVICE VENDOR: GenuineIntel
                CL DEVICE TYPE:
                CL DEVICE VERSION: OpenCL 1.1 ATI-Stream-v2.3 (451)
               CL DEVICE EXTENSIONS: cl amd fp64 cl khr global int32 base atomics cl khr global int32 extended atomics
cl khr local int32 base atomics cl khr local int32 extended atomics cl khr int64 base atomics cl khr int64 extended atom
ics cl khr byte addressable store cl khr gl sharing cl ext device fission cl amd device attribute guery cl amd media ops
cl amd popent cl amd printf
Create context...
Create command queue...
Read kernel source from file: /home/lars/clhmc dev/clhmc/prog/opencl header.cl
Read kernel source from file: /home/lars/clhmc dev/clhmc/prog/opencl geometry.cl
Read kernel source from file: /home/lars/clhmc dev/clhmc/prog/opencl random.cl
Read kernel source from file: /home/lars/clhmc dev/clhmc/prog/opencl operations complex.cl
Read kernel source from file: /home/lars/clhmc dev/clhmc/prog/opencl operations gaugefield.cl
Read kernel source from file: /home/lars/clhmc dev/clhmc/prog/opencl update heatbath.cl
Read kernel source from file: /home/lars/clhmc dev/clhmc/prog/opencl gaugeobservables.cl
Create program...
Build program...
       build options: -D INKERNEL -DNSPACE=8 -DNTIME=4 -DVOLSPACE=512 -DSPINORSIZE=12 -DHALFSPINORSIZE=6 -DSPINORFIEL
DSIZE=24576 -DEOPREC SPINORFIELDSIZE=12288 -D USEDOUBLEPREC -I/home/lars/clhmc dev/clhmc/prog
finished building program
Build Log:
Create buffer for gaugefield...
Create buffer for random numbers...
Create buffer for gaugeobservables...
Create heatbath kernels...
Create gaugeobservables kernels...
uriting assumptiold to lime file
```

## That's it

#### To do

- Gaugefield class [Lars]
- Inter-device strategy
- Device code (esp. hmc) [Christopher]
- Memory management [Matthias]

OpenSource, GPL?