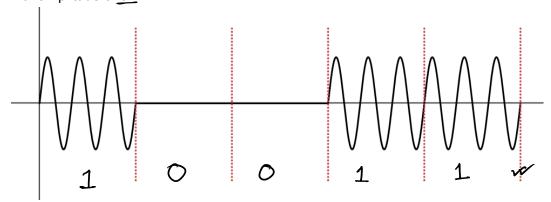
Marks: 15	CSE320: Data Communication	Assignment 3
ID:	Name:	Section:

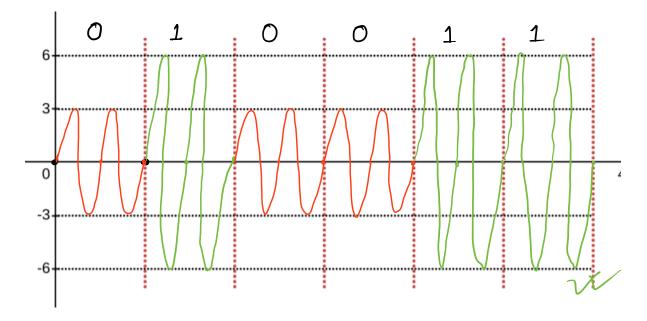
## **Amplitude Shift Keying (ASK)**

### Binary ASK:

1. Determine the digital bit stream from the analog signal below. The signal was modulated using Binary ASK where 0 means signal element with no amplitude and 1 means signal element with amplitude of 3v.

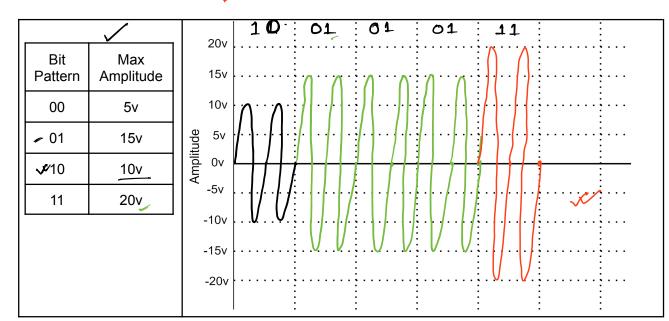


2. Draw the analog signal for the digital bit stream <u>010011</u> using Binary ASK where <u>0</u> means signal element with amplitude of <u>3v</u> and <u>1 means signal element with amplitude of <u>6v</u> [ frequency = <u>2 for each signal element and phase <u>0 rad</u>]</u></u>



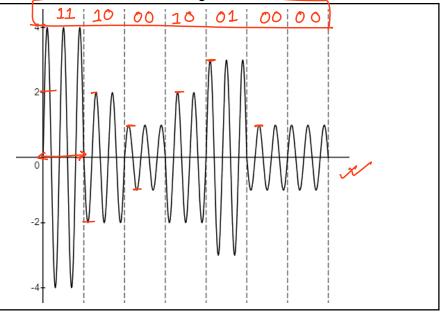
#### **Multi-level ASK**

3. In a Multi level ASK, for each signal element, we want to send 2 bits at a time. We have used a carrier signal that has a frequency of 10 Hz (Each signal element has 2 cycles) and phase is 0 rad. If the amplitude changes according to the following table, draw the modulated signal for the bit sequence 1001010111



4) For the following Multi-level ASK, find the bitstream form the signal below:

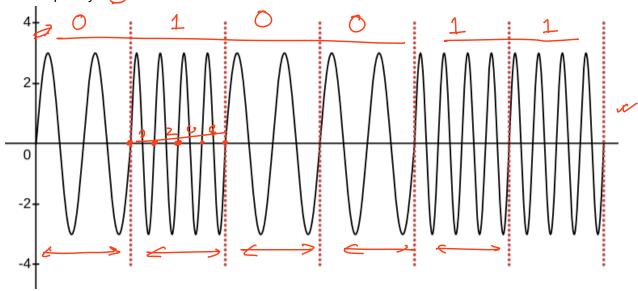
Bit Pattern	Amplitude		
00 ′	1v		
01 -	3v 💆		
10 <b>′</b>	2v <b>′</b>		
11	4v		
	X		



# Frequency Shift Keying (FSK)

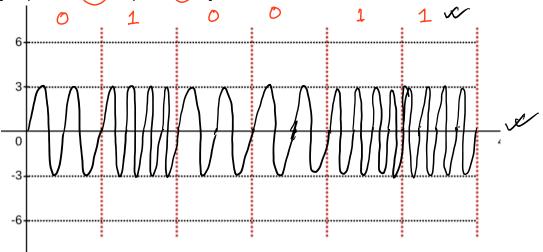
#### **Binary FSK:**

Determine the digital bit stream from the analog signal below. The signal was modulated using Binary FSK where <u>0</u> means signal element with frequency of <u>2</u> and <u>1</u> means signal element with frequency of <u>4</u>.



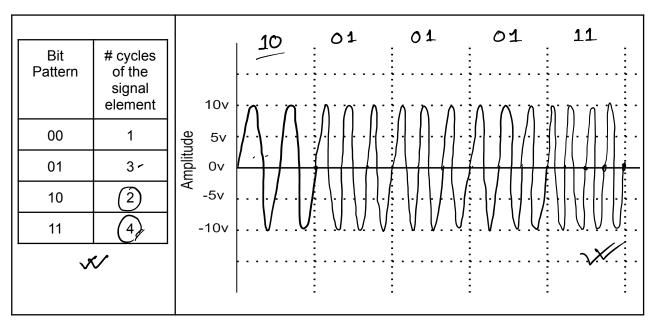
6 Draw the analog signal for the digital bit stream 010011 using Binary FSK where 0 means signal element with frequency of 2 and 1 means signal element with frequency of 4.

[ Amplitude = (3v) and phase (0) ad]



#### **Multi level FSK**

7. In a Multi level FSK, for each signal element, we want to send 2 bits at a time. We have used a carrier signal that has an amplitude of 10v and phase is 0 degree. If the frequency changes according to the following table, draw the modulated signal for the bit sequence 10010111



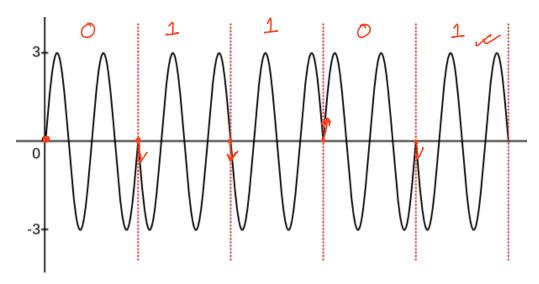
8. For the following Multi-level FSK, find the bitstream form the signal below:

Bit Pattern	Frequency (each signal element)	0 O	01	11	00	10 m
00 /	1 1/2					$\bigwedge$
10	2 4 •	-3-			ve	

### Phase Shift Keying (PSK)

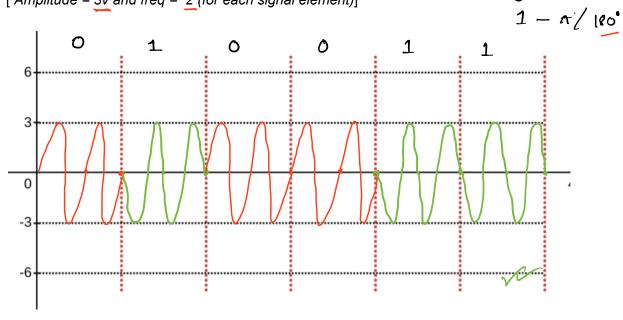
## **Binary PSK**

9. Determine the digital bit stream from the analog signal below. The signal was modulated using Binary PSK where 0 means signal element with phase of 1 means signal element with phase of 1 rad.



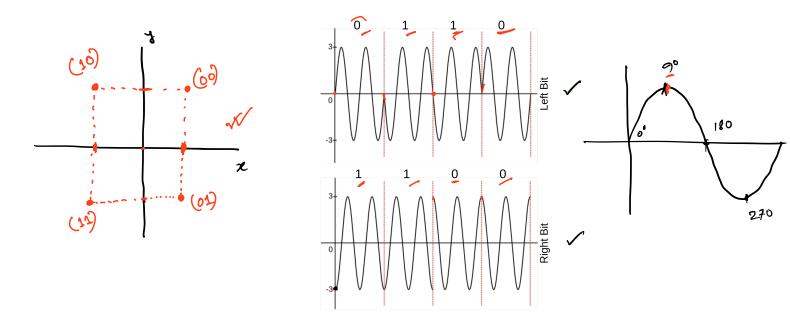
10. Draw the analog signal for the digital bit stream 010011 using Binary PSK where 0 means signal element with phase of 0 rad and 1 means signal element with phase of π rad.

[ Amplitude = 3v and freq = 2 (for each signal element)]



#### **QPSK & Constellation Diagram:**

11. Draw the constellation diagram for the QPSK given below:



12. Draw the analog signal for the bit stream 1011001011 using the constellation diagram given below [frequency = 2 for each signal element]

