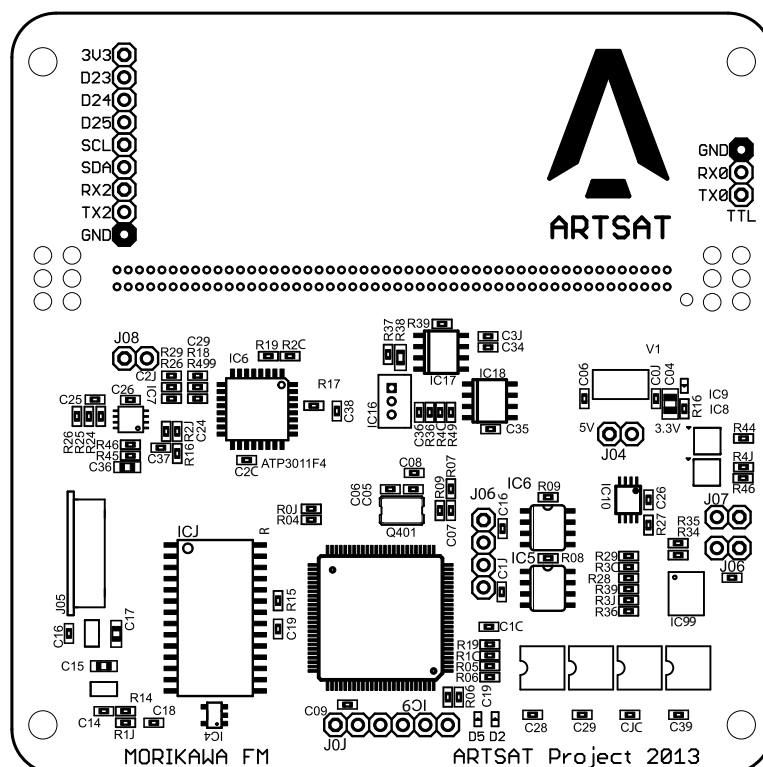


Optional
I/O ports



Main OBC



Jumper pins



J03 FTDI header
(UART0 port)



SPI (ICSP)



J07: Bypass Main-VDD
J06: EEPROM select
(short= mission)