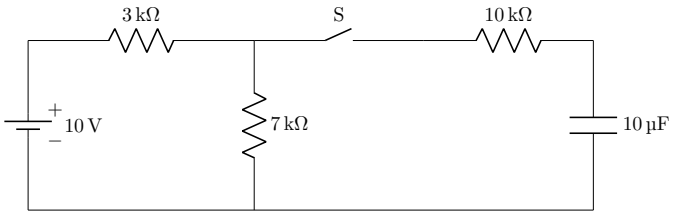
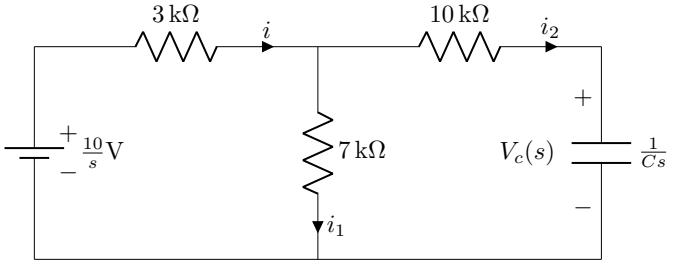
GATE 2023 BM 30

EE23BTECH11007 - Aneesh Kadiyala*

Question: In the following circuit, the switch S is open for $t < 0$ and closed for $t \geq 0$. What is the steady state voltage (in Volts) across the capacitor when the switch is closed?



Solution:
In s-domain:



$$I(s) = \frac{\frac{10}{s}\text{V}}{3\text{k}\Omega + \frac{(7\text{k}\Omega)(10\text{k}\Omega + \frac{1}{sC})}{17\text{k}\Omega + \frac{1}{sC}}} \tag{1}$$

$$I_2(s) = \frac{7\text{k}\Omega}{17\text{k}\Omega + \frac{1}{sC}} I(s) \tag{2}$$

$$V_c(s) = i_2(s)\frac{1}{sC} \tag{3}$$

$$= \frac{7}{s(0.121s + 1)} \tag{4}$$

$$= 7\left(\frac{1}{s} - \frac{1}{s + \frac{1}{0.121}}\right) \tag{5}$$

Taking inverse Laplace transform:

$$v_c(t) = 7u(t)\left(1 - e^{-\frac{t}{-0.121}}\right) \tag{6}$$