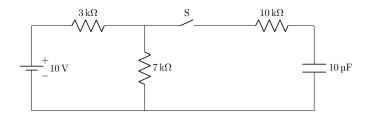
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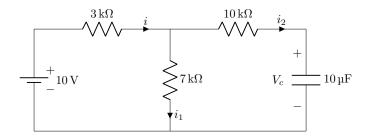
EE23BTECH11007 - Aneesh Kadiyala*

Question: In the following circuit, the switch S is open for t < 0 and closed for $t \ge 0$. What is the steady state voltage (in Volts) across the capacitor when the switch is closed?



Solution:

For $t \ge 0$:



In steady state, no current flows through capacitor.

$$i_2 = 0 \tag{1}$$

$$\implies i = i_1$$
 (2)

$$V_c = (7k\Omega)(i_1) \tag{3}$$

$$10V = (10k\Omega)i_1 \tag{4}$$

$$\implies i_1 = 1 \text{mA}$$
 (5)

$$V_c = 7V \tag{6}$$