

# Module 3 State Feedback Current Control: Discrete-Time Design

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#### **Outline**

#### Introduction

**Discretization of a Continuous-Time Controller** 

Sampled-Data System

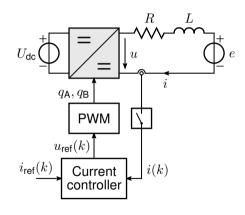
**Direct Discrete-Time Control** 

#### Introduction

- ► Switching losses in the converter are proportional to the switching frequency
- Sampling frequency depends on the switching frequency
- ► If the sampling frequency is very high (as compared to the highest frequencies to be controlled), we may simply discretize the controller designed in the continuous-time domain
- ► If high sampling frequencies cannot be reached, sampling and hold should be properly taken into account to avoid poor performance
- Direct discrete-time design is an efficient method, considered in this course
- Specifications can still be given in the continuous-time domain, but they are mapped to the discrete-time domain

## **Digital Current Control System**

- Current is sampled at the discrete time instants, in synchronism with the PWM
- PWM behaves as the zero-order hold (ZOH)
- ► Plant model will be mapped into the discrete-time domain using closed-form expressions and physical parameters (*R* and *L*)
- This makes it easier to include the magnetic saturation effects in the discrete-time algorithm later

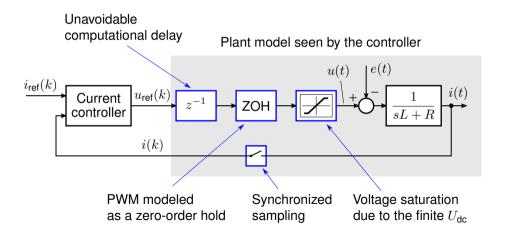


## **Discrete-Time Control Design Approaches**

- Discretized continuous-time design
  - ► Design the controller in the continuous-time domain
  - ► Discretize the controller using the Tustin or Euler method
  - Delays are difficult to handle
  - ► Use high sampling frequency (at least 10...20 times the closed-loop bandwidth)
- 2. Direct discrete-time design
  - Develop the hold-equivalent discrete-time plant model (exact model or series expansion, closed-form or numerical)
  - ► Delays can be easily included in the discrete-time model
  - Design the controller directly in discrete-time domain
  - ► Low sampling frequencies can be used (down to the Nyquist frequency)

The choice depends on the application.

## Nonidealities due to Digital Implementation and Actuator Saturation



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## **Euler and Tustin Methods**

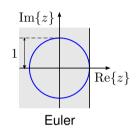
- Common methods to discretize continuous-time algorithms
- ► Euler method is very simple

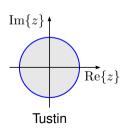
$$\frac{\mathrm{d}x}{\mathrm{d}t} \leftarrow \frac{x(k+1) - x(k)}{T_{\mathrm{S}}} \qquad \text{or} \qquad s \leftarrow \frac{z-1}{T_{\mathrm{S}}}$$

Tustin method is more complicated

$$\frac{\mathrm{d}x}{\mathrm{d}t} \leftarrow \frac{2}{T_\mathrm{S}} \frac{x(k+1) - x(k)}{x(k+1) + x(k)} \qquad \text{or} \qquad s \leftarrow \frac{2}{T_\mathrm{S}} \frac{z-1}{z+1}$$

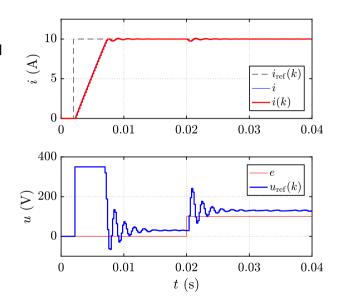
- ► Grey regions in the figure show how stable *s*-plane poles are mapped into the *z*-plane
- ► Tustin method maps the poles well, but time delays are still a remaining problem





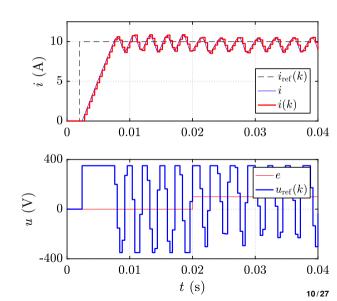
#### **Exercise 3.1**

- Augment your simulation model with the converter model, controlled with the unipolar PWM. Solver time step should be set a few decades shorter than the switching period for good accuracy.
- Discretize the controller using the Euler method and include the computational delay
- Simulate the model with  $T_{\rm S}=200~\mu{\rm s}$  and  $T_{\rm SW}=400~\mu{\rm s}$



#### Exercise 3.2

- ► Reparametrize your model using  $T_{\rm S} = T_{\rm SW} = 400~\mu{\rm s}$  (single-update PWM)
- ► Simulate the model
- Discretization of the continuous-time controller deteriorates the control performance at this long sampling periods
- Note that the results are essentially the same even if you replace the PWM with the ZOH



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#### Sampled-Data System

General Case Example System

**Direct Discrete-Time Control** 

## Sampled-Data System

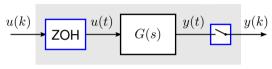
Continuous-time system

$$rac{\mathsf{d}oldsymbol{x}(t)}{\mathsf{d}t} = oldsymbol{A}oldsymbol{x}(t) + oldsymbol{B}u(t) \ y(t) = oldsymbol{C}oldsymbol{x}(t)$$

Integrating the state equation from  $t_0$  to t gives

$$x(t) = e^{A(t-t_0)}x(t_0) + \int_{t_0}^t e^{A(t-\tau)}Bu(\tau)d\tau^{\blacktriangleright}$$
 Assume the ZOH at the control input  $u(\tau) = u(kT_0)$   $kT_0 < \tau < (k+1)$ 

Sampled-data system G(z)



► Let us change the notation

$$t_0 = kT_{\mathrm{S}}$$
 and  $t = (k+1)T_{\mathrm{S}}$ 

$$u(\tau) = u(kT_{\rm S})$$
  $kT_{\rm S} \le \tau \le (k+1)T_{\rm S}$ 

## **Hold-Equivalent Discrete Model**

- ► In digital control systems, assuming the ZOH at the control input typically agrees with the physical reality (also when the actuator is a power converter)
- ▶ Previous expressions lead to the discrete-time system model

$$x(k+1) = \Phi x(k) + \Gamma u(k)$$
  
 $y(k) = Cx(k)$ 

with exact hold-equivalent discrete-time matrices

$$oldsymbol{\Phi} = \mathsf{e}^{oldsymbol{A}T_\mathsf{S}} \qquad oldsymbol{\Gamma} = \int_0^{T_\mathsf{S}} \mathsf{e}^{oldsymbol{A} au} \mathsf{d} au oldsymbol{B}$$

► System can also be expressed in the *z*-domain

$$G(z) = \frac{y(z)}{u(z)} = C(z\mathbf{I} - \mathbf{\Phi})^{-1}\mathbf{\Gamma}$$

## **Series Expansion**

Series expansion

$$\mathbf{\Phi} = \mathbf{I} + T_{\mathsf{S}} \mathbf{\Psi} \mathbf{A}$$
  $\mathbf{\Gamma} = T_{\mathsf{S}} \mathbf{\Psi} \mathbf{B}$ 

where I is the identity matrix and

$$\mathbf{\Psi} = \mathbf{I} + \frac{T_{\mathsf{s}}\mathbf{A}}{2!} + \frac{T_{\mathsf{s}}^2\mathbf{A}^2}{3!} + \dots$$

- ▶ Note that infinite series yields also exact model
- $lackbox{lack}$  Choosing  $oldsymbol{\Psi}=\mathbf{I}$  yields the Euler approximation

#### **Selection of State Variables**

- State variables can be chosen arbitrarily in linear models (and in resulting controllers and observers) and they can be easily changed afterwards
- ▶ Different selections lead to mathematically equivalent control laws
- ► If the system is nonlinear, suitable selection of state variables may reduce the complexity of the model significantly
- ► Later we will consider the effects of magnetic saturation
- lacktriangle Saturation effects are easier to handle if the flux linkage  $\psi$  is the state variable
- For this reason, the flux linkage  $\psi$  will be chosen as a state variable (even if we first consider a magnetically linear system)

## **Hold-Equivalent Discrete Model: Example System**

► Continuous-time system model

$$\frac{\mathrm{d}\psi(t)}{\mathrm{d}t} = -(R/L)\psi(t) + u(t) - e(t)$$
 
$$i(t) = \psi(t)/L$$

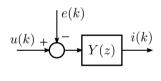
Discrete-time system model

$$\psi(k+1) = \phi\psi(k) + \gamma[u(k) - e(k)]$$
$$i(k) = \psi(k)/L$$

► Hold-equivalent discrete-time coefficients

$$\phi = \mathbf{e}^{-(R/L)T_{\mathbf{S}}} \quad \gamma = \int_0^{T_{\mathbf{S}}} \mathbf{e}^{-(R/L)\tau} \mathrm{d}\tau = \frac{1 - \mathbf{e}^{-(R/L)T_{\mathbf{S}}}}{R/L}$$

ZOH is assumed for disturbance input e(t) as well for simplicity



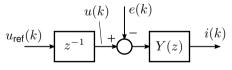
$$Y(z) = \frac{1}{L} \frac{\gamma}{z - \phi}$$

## **Inclusion of the Computational Delay**

Computational delay can be added as an extra state

$$\psi(k+1) = \phi\psi(k) + \gamma[u(k) - e(k)]$$
$$u(k+1) = u_{\mathsf{ref}}(k)$$
$$i(k) = \psi(k)/L$$

► Both states are readily available for feedback



#### **Transfer Function**

► Transfer function from the reference voltage to the measured current

$$\frac{i(z)}{u_{\mathsf{ref}}(z)} = z^{-1}Y(z) = \frac{1}{L} \frac{\gamma}{z(z-\phi)}$$

▶ Open-loop poles at  $z = \phi = e^{-(R/L)T_s}$  and at z = 0

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## **Discrete-Time Controller**

► Full state-feedback control law

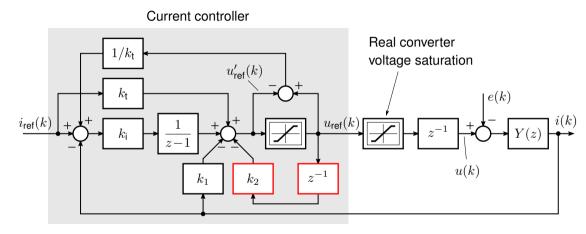
$$\begin{split} u_{\mathrm{i}}(k+1) &= u_{\mathrm{i}}(k) + k_{\mathrm{i}}[i_{\mathrm{ref}}(k) - i(k)] \\ u(k+1) &= u_{\mathrm{ref}}(k) \\ u_{\mathrm{ref}}(k) &= k_{\mathrm{t}}i_{\mathrm{ref}}(k) - k_{1}i(k) - k_{2}u(k) + u_{\mathrm{i}}(k) \end{split}$$

Control law in the z-domain

$$u_{\mathsf{ref}}(z) = k_{\mathsf{t}} i_{\mathsf{ref}}(z) - k_1 i(z) + \frac{k_{\mathsf{i}}}{z - 1} [i_{\mathsf{ref}}(z) - i(z)] - k_2 z^{-1} u_{\mathsf{ref}}(z)$$

- Antiwindup can be added similarly as in the continuous-time case
- ▶ Choosing  $k_t = k_1$  and  $k_2 = 0$  yields the PI controller

## **Discrete-Time Controller**



- Only the red blocks have to be added to the PI controller
- ► Poles can now be arbitrarily placed

## **Closed-Loop System**

Closed-loop transfer function from the reference to the measured current

$$\frac{i(z)}{i_{\text{ref}}(z)} = \frac{b_1 z + b_0}{z^3 + a_2 z^2 + a_1 z + a_0}$$

Coefficients depend on the controller gains

$$a_2 = k_2 - \phi - 1$$
  $a_1 = \phi(1 - k_2) - k_2 + \frac{\gamma k_1}{L}$   $a_0 = k_2 \phi + \frac{\gamma (k_i - k_1)}{L}$   $b_1 = \frac{\gamma k_t}{L}$   $b_0 = \frac{\gamma (k_i - k_t)}{L}$ 

#### Pole and Zero Placement

- ▶ To simplify notation, accurate parameters are assumed
- Coefficients (poles and zero) can be arbitrarily placed

$$k_{\rm t} = \frac{b_1 L}{\gamma}$$
  $k_2 = a_2 + \phi + 1$   $k_1 = \frac{[a_1 - \phi(1 - k_2) + k_2]L}{\gamma}$   $k_{\rm i} = k_1 + \frac{(a_0 - k_2 \phi)L}{\gamma}$ 

- ▶ Let us choose  $a_0 = 0$  due to the computational delay
- ► Example selection for other coefficients

$$a_1 = \beta^2 \qquad a_2 = -2\beta \qquad b_1 = 1 - \beta$$

Resulting closed-loop system

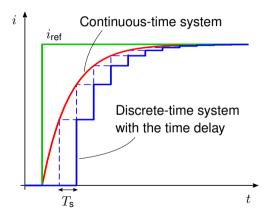
$$\frac{i(z)}{i_{\mathsf{ref}}(z)} = \frac{(1-\beta)(z-\beta)}{z(z-\beta)^2} = \frac{1-\beta}{z(z-\beta)}$$

## **Desired Closed-Loop Reference Tracking**

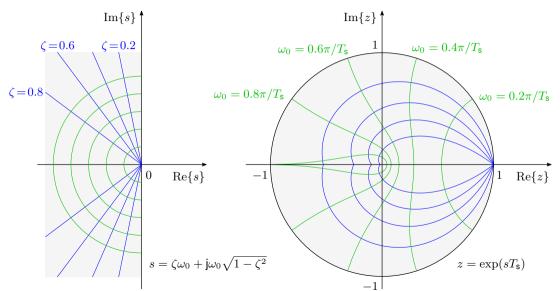
► 1st-order unity-gain LPF + time delay

$$\frac{i(z)}{i_{\mathsf{ref}}(z)} = \frac{1 - \beta}{z(z - \beta)}$$

- $\beta = e^{-\alpha_c T_s}$  and  $\alpha_c$  is the desired continuous-time real pole
- ▶ Delay  $z^{-1}$  cannot be avoided in practice
- ► Same "user input" as in the continuous-time case  $(\alpha_c, R, L)$

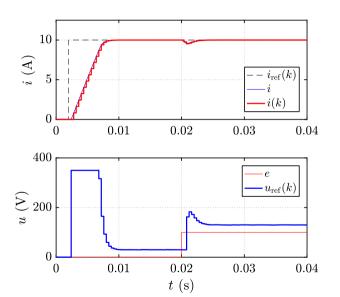


## Mapping from s-Plane to z-Plane



#### Exercise 3.3

- Modify the current controller according to the presented direct discrete-time design approach
- Simulate the model with  $\alpha_{\rm c} = 2\pi \cdot 300$  rad/s and  $T_{\rm s} = T_{\rm sw} = 400~\mu{\rm s}$
- Test also higher bandwidths, model parameter errors, and sensitivity to the measurement noise



## **Further Reading**

- ► G. F. Franklin, J. D. Powell, and M. Workman, *Digital Control of Dynamic Systems*. Menlo Park, CA: Addison-Wesley, 1997.
- ▶ M. Hinkkanen, H. A. A. Awan, Z. Qu, T. Tuovinen, and F. Briz, "Current control for synchronous motor drives: Direct discrete-time pole-place- ment design," *IEEE Trans. Ind. Applicat.*, 2016.