A Virtual Machine for simPL

YSC3208: Programming Design & Language Implementation

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Week 6, Feb 13 - 17, 2017

- A Virtual Machine for simPL
 - simPLa: An Old Hat
 - simPLb: "Adding Division"
 - simPLc: Jumping Up and Down
 - simPLd: Getting Serious
 - simPLe: Getting Recursive
 - Tail Recursion: Getting Efficient

simPLa is ePL Without Division

SVMLa

	<i>S</i>	5	
DONE	LDCI i . s	LDCB b.s	
5	S	S	
PLUS.s	MINUS.s	TIMES.s	•

Compiling simPLa to SVMLa

The translation from simPLa to SVMLa is accomplished by a function

$$: simPLa \rightarrow SVML$$

which appends the instruction DONE to the result of the auxiliary translation function \hookrightarrow .

$$E \hookrightarrow s$$

$$E \rightarrow s.DONE$$

Compiling, continued

$$n \hookrightarrow_{\mathbf{N}} i$$
 $n \hookrightarrow_{\mathbf{LDCI}} i$ true $\hookrightarrow_{\mathbf{LDCB}} true$ false $\hookrightarrow_{\mathbf{LDCB}} false$
 $E_1 \hookrightarrow_{\mathbf{S}_1} E_2 \hookrightarrow_{\mathbf{S}_2}$ $E_1 \hookrightarrow_{\mathbf{S}_1} E_2 \hookrightarrow_{\mathbf{S}_2}$
 $E_1 + E_2 \hookrightarrow_{\mathbf{S}_1} .s_2.$ PLUS $E_1 * E_2 \hookrightarrow_{\mathbf{S}_1} .s_2.$ TIMES

Compiling, continued

$$E_1 \hookrightarrow s_1$$
 $E_2 \hookrightarrow s_2$
 $E_1 \& E_2 \hookrightarrow s_1.s_2. \text{AND}$
 $E \hookrightarrow s$
 $\setminus E \hookrightarrow s. \text{NOT}$
 $E_1 \hookrightarrow s_1$ $E_2 \hookrightarrow s_2$
 $E_1 \gt E_2 \hookrightarrow s_1.s_2. \text{GT}$

$$E_1 \hookrightarrow s_1 \qquad E_2 \hookrightarrow s_2$$

$$E_1 \mid E_2 \hookrightarrow s_1.s_2.OR$$

 $E_1 \hookrightarrow s_1 \qquad E_2 \hookrightarrow s_2$

$$E_1 < E_2 \hookrightarrow s_1.s_2.$$
LT

$$E_1 \hookrightarrow s_1 \qquad E_2 \hookrightarrow s_2$$

$$E_1$$
= $E_2 \hookrightarrow s_1.s_2$.EQ

Executing SVMLa Code

- Given: SVML program s
- To be defined: machine M_s
- M_s keeps two registers
 - Program counter pc
 - Operand stack os

Running the Machine

- Starting configuration: $(\langle \rangle, 0)$
- End configuration: s(pc) = DONE.
- Result:

$$R(\mathit{M}_s) = v, \text{ where } (\langle \rangle, 0)
ightharpoonup ^*_s (\langle v.os \rangle, \mathit{pc}), \text{ and } s(\mathit{pc}) = \mathtt{DONE}$$

simPLb

- simPLb adds division to simPLa
- Add ⊥ as possible stack value
- \bullet Division by zero pushes \bot on the stack, and jumps to DONE

Execution of SVMLb

$$s(pc) = exttt{DIV}$$
 $(0.i_1.os, pc)
ightharpoonup _s (oldsymbol{\perp}.os, |s|-1)$
 $s(pc) = exttt{DIV}, i_2
eq 0$
 $(i_2.i_1.os, pc)
ightharpoonup _s (i_1/i_2.os, pc+1)$

simPLc

- simPLc adds conditionals to simPLb
- Idea: introduce conditional and unconditional jump instructions
- How can we jump from one part of the SVML program to another?
- Answer: by setting the program counter to the address of the jump target

Translation of Conditionals

$$E_1 \hookrightarrow s_1$$
 $E_2 \hookrightarrow s_2$ $E_3 \hookrightarrow s_3$

if E_1 then E_2 else E_3 end \hookrightarrow s_1 .JOFR $|s_2| + 2.s_2$.GOTOR $|s_3| + 1.s_3$

Example

```
if true | false
then 1+2
else 2+3
end
becomes

[LDI 2, LDCB true, LDCB false, OR, JOFR+5, LDCI 1,
LDCI 2, PLUS, GOTOR+4, LDCI 2, LDCI 3, PLUS, MUL,
DONE]
```

Execution of SVMLc

$$s(pc) = GOTOR i$$
 $(os, pc) \Rightarrow_s (os, pc + i)$

$$s(pc) = exttt{JOFR } i$$
 $s(pc) = exttt{JOFR } i$ $(true.os, pc)
ightharpoonup _s (os, pc + i)$

Example

[LDI 2, LDCB true, LDCB false, OR, JOFR 5, LDCI 1, LDCI 2, PLUS, GOTOR 4, LDCI 2, LDCI 3, PLUS, MUL, DONE]

$$\begin{array}{l} (\langle\rangle,0) \rightrightarrows_{s} (\langle2\rangle,1) \rightrightarrows_{s} (\langle \textit{true},2\rangle,2) \rightrightarrows_{s} (\langle \textit{false},\textit{true},2\rangle,3) \rightrightarrows_{s} \\ (\langle \textit{true},2\rangle,4) \rightrightarrows_{s} (\langle2\rangle,5) \rightrightarrows_{s} (\langle1,2\rangle,6) \rightrightarrows_{s} (\langle2,1,2\rangle,7) \rightrightarrows_{s} \\ (\langle3,2\rangle,8) \rightrightarrows_{s} (\langle3,2\rangle,12) \rightrightarrows_{s} (\langle6\rangle,13) \end{array}$$

Implementation of simPLc

- Compiler can generate and use symbolic labels.
- Translate later to use absolute jump addresses
- Corresponding instructions: GOTO and JOF

Using Symbolic Addresses

LABEL is a dummy instruction to capture the symbolic address of the next instruction.

fresh
$$l_1, l_2$$
 $E_1 \hookrightarrow s_1$ $E_2 \hookrightarrow s_2$ $E_3 \hookrightarrow s_3$

if E_1 then E_2 else E_3 end $\hookrightarrow s_1.\text{JOF } l_1.s_2.\text{GOTO } l_2.\text{LABEL } l_1.s_3.\text{LABEL } l_2$

Example

```
2 *
if true | false
then 1+2
else 2+3
end
compiles to:
            [LDCI 2,LDCB true,LDCB false,OR,JOF l_0,
            LDCI 1,LDCI 2,PLUS,GOTO I_1,
LABEL l_0: LDCI 2,LDCI 3,PLUS,
LABEL l_1: TIMES, DONE]
```

Example

transforms (with address linking) to:

```
[LDCI 2,LDCB true,LDCB false,OR,JOF 9,
LDCI 1,LDCI 2,PLUS,GOTO 12,
```

9: LDCI 2, LDCI 3, PLUS,

12: TIMES, DONE]

Rules for New Instructions (with absolute addr)

$$s(pc) = GOTO i$$

$$(os, pc) \Rightarrow_s (os, i)$$

$$s(pc) = exttt{JOF } i$$
 $s(pc) = exttt{JOF } i$ $(true.os, pc)
ightharpoonup _s (os, pc + 1)$ $(false.os, pc)
ightharpoonup _s (os, i)$

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simPLd

$$\begin{array}{ccc}
E & E & E_1 & \cdots & E_n \\
\hline
X & & \text{fun } x_1 \cdots x_n \rightarrow E \text{ end} & (E & E_1 \cdots E_n)
\end{array}$$

Outline

- Compilation of identifiers
- Execution of identifiers
- Compilation of function application
- Compilation of function definition
- Execution of function definition
- Execution of function application
- Returning from a function

Compilation of Identifiers

- add register *e* (environment), mapping identifiers to denotable values.
- translation: \longrightarrow LDS x
- Later, will find a mapping from identifier to integer offsets on environment stack.

Execution of Identifiers

$$s(pc) = LDS x$$

$$(os, pc, e) \Rightarrow_s (e(x).os, pc + 1, e)$$

Compilation of Function Application

$$E \hookrightarrow s \qquad E_1 \hookrightarrow s_1 \cdots E_n \hookrightarrow s_n$$

$$(E \ E_1 \cdots E_n) \hookrightarrow s_n \cdots s_1.s. \text{CALL } n$$

Note that arguments are compiled in reverse order. This allow us to support over-applications, if needed.

Compilation of Function Definition (without Labels)

$$E \hookrightarrow s$$
 $vs = fv(E) - \{x_1 \cdots x_n\}$

$$\texttt{fun } x_1 \dots x_n \text{->} E \texttt{ end} \hookrightarrow \\ \texttt{LDFS } [vs][x_1 \cdots x_n].\texttt{GOTOR } |s| + 1.s.\texttt{RTN}$$

What is *vs*? Answer: Variables from the environment when function was created.

Compilation using Symbolic Labels

$$E \hookrightarrow s$$
 $vs = fv(E) - \{x_1 \cdots x_n\}$

```
fun x_1 \dots x_n \rightarrow E end \hookrightarrow LDFS [vs][x_1 \dots x_n].GOTO l.s.RTN.LABEL l:
```

Execution of Function Definition (Building a Closure)

$$s(pc) = \text{LDFS}[v_1 \cdots v_m][x_1 \cdots x_n]$$

$$(os, pc, e) \Rightarrow_s ((pc+2, x_1 \cdots x_n, e \#[v_1 \cdots v_m]).os, pc+1, e)$$

We introduce triple (address, formals, e) to represent function value on the operand stack. Such a triple is called a closure.

Idea

- Add another machine register that can store the machine state to be re-installed after functions return.
- Since functions call other functions, we need a stack, to support the restoration of caller's state.
- This stack is called the *runtime stack*, denoted by *rs*.
- Stack entries are called stack frames and consist of (r, address, e) where r denotes the number of residual arguments on operand stack.

Execution of Function Application

Over-Application when m < nExact-Application when m = n

$$s(pc) = CALL n \quad 0 < m \le n$$

$$((address, x_1 \cdots x_m, e').v_1 \dots v_m.os, pc, e, rs) \Longrightarrow_s (os, address, e'[x_1 \leftarrow v_1] \cdots [x_m \leftarrow v_m], (n-m, pc+1, e).rs)$$

Examples of Applications

Over-Applied Applications:

Under-Applied Applications:

(fun
$$x y \rightarrow x+y$$
 end 2)

Pre-processing ==> :

fun
$$z \rightarrow$$
 (fun $x y \rightarrow x+y$ end $2 z$) end

Returning from a Function

Pop stack frame if there are no residual argument.

$$s(pc) = RTN \qquad r = 0$$

$$(v.os, pc, e, (r, pc', e').rs) \Rightarrow_s (v.os, pc', e', rs)$$

Returning from a Function

Perform further application if there are some residual arguments.

$$s(pc) = RTN \qquad 0 < m \le r$$

$$((address, x_1 \cdots x_m, e').v_1 \dots v_m.os, pc, e, (r, pc_2, e_2).rs) \Rightarrow_s (os, address, e'[x_1 \leftarrow v_1] \cdots [x_m \leftarrow v_m], (r-m, pc_2, e_2).rs)$$

Representation of Environments

We organize each environment as an array of values for both non-local arguments and local arguments.

Copying from Stack to Environments

During call application, arguments in operation stack are copied to environment and placed after the non-local variables.

Compilation of Application

Applications remember the number of arguments. The application may either by *fully-applied* or *over-applied* but never under-applied due to our pre-processing.

Exact Application Example

Handling Non-Local Variables inside Closures

Why bind non-local variables inside the closure? Reasons: closures may escape the scope of their non-local variables when under-applied.

Inner function of ${\tt f}$ escapes the scope of its non-local variable ${\tt x}$. What is the disadvantage?

Partial -> Full Application

transforms to:

Representation of Closures

Compiled Code:

```
[LDF([],1,11),LDF([],1,4),CALL 1,DONE,
4: LDF([(f,0)],1,6),RTN,
6: LD (pa,1),LDCI 4,LD (f,0),CALL 2,RTN,
10: LDF([(x,0)],1,13),RTN,
```

13: LD (x,0), LD (y,1), PLUS, RTN]

Note partial application removed by pre-processing.

Representing Runtime Stack Frames

Each stack frame comprises of (i) number of residual arguments that remain in the operand stack, (ii) address of method, and (iii) bindings for variables used by the method.

Translation of simPLe

$$E \hookrightarrow s$$
 $v_1 \cdots v_m = fv(E) - \{f, x_1 \cdots x_n\}$

recfun $f(x_1...x_n \rightarrow E \text{ end } \hookrightarrow \text{LDFRS}([v_1 \cdots v_m], f, [x_1 \cdots x_n]).\text{GOTO } \textit{I.s.RTN.LABEL } \textit{I}$

Execution of SVMLe

$$(os, pc, e) \Rightarrow_s (c = (pc + 2, x_1 \cdots x_n, e \# [v_1 \cdots v_m][f \leftarrow c]).os, pc + 1, e)$$

 $s(pc) = LDFRS([v_1 \cdots v_m], f, [x_1 \cdots x_n])$

Idea of Implementation

Use same call. Create a circular data structure. Environment of recursive function value points to function itself.

Tail Recursion: Motivation

- Each function call creates a new stack frame.
- Function calls consume significant amount of memory.
- There are situations, where the creation of a new stack frame can be avoided.

Tail Recursion: Conditions

- last action in the body of a function is another function call
- calling function and the function to be called is the same recursive function

Terminology

- A recursive call, which appears in the body of a recursive function as the last instruction to be executed, is called tail call.
- A recursive function, in which all recursive calls are tail calls, is called *tail-recursive*.

Example

```
let {.}
   facloop = recfun facloop {.} n acc ->
                 if n = 1 then acc
                 else (facloop (n-1) (acc*n)) end
                 end
in {int}
   let {.}
      fac = fun \{.\} n \rightarrow (facloop n 1) end
   in {int}
      (fac 4)
   end
end
```

Compilation for Tail Recursion

Replace
CALL n.RTN
by
TAILCALL n
if operator of the call is the function variable of the immediately surrounding recursive function definition

Example (normal calls)

Example (tail calls)

28:

R.TN1

Tail Call Semantics

$$S(pc) = TAILCALL n \qquad s \leq r + n$$

$$((addr, x_1 \cdots x_s, e_2).v_1...v_s.os, pc, e, (r, npc, ne).rs) \Rightarrow_s (os, addr, e_2[x_1 \leftarrow v_1] \cdots [x_s \leftarrow v_s], (r+n-s, npc, ne).rs)$$