

INPUT BUFFER

The schematic diagram illustrates an input buffer circuit. The input signal, labeled IB-IN, is connected to a network of components: a 100 nF capacitor (IB.C1), a 50 ohm resistor (10%), and a 1206 resistor (IB.R1, 1.0 MOhm, 1%, 1206). This network is connected to the non-inverting input (pin 2) of the NE5532D op-amp. The op-amp is configured with a voltage divider (IB.R2, 22 kOhm, 1%, 1206) and a feedback network (IB.R4, 22 kOhm, 1%, 1206) connected to its output (pin 1, IB-OUT). The inverting input (pin 3) is connected to a voltage divider (IB.J1, IB.J2) and a 1206 resistor (IB.R3, 470 kOhm, 1%, 1206). The op-amp is powered by VRFF and has a ground connection (pin 4).

Figure 1 shows the pin connections for the ADXL345. The diagram includes a 16-pin package with various pins labeled. TP1 is a test point connected to ground. V, VA, and VREF are power supply pins connected to TP2, TP3, and TP4 respectively. TP5 is connected to IN. TP6 is connected to IB-IN. TP7 is connected to LFO-VREF. TP8 is connected to LFO-B-IN. TP9 is connected to LFO-B-OUT. TP10 is connected to LFO-OUT. TP11 is connected to IB-OUT. TP12 is connected to DELAY-IN. TP13 is connected to OC-IN1. TP14 is connected to DELAY-REF. TP15 is connected to AL. TP16 is connected to DELAY-OUT. TP17 is connected to OC-IN2. TP18 is connected to OC-OUT. TP19 is connected to CIR-OUT. TP20 is connected to OUT.

VFM1 1590N1
Hammond

VMH1
Винт DIN 7965 M3x6 Стойка DI5M3x18 Плата
Шайба DIN 125 D3 Корпус

VMH2
Винт DIN 7965 M3x6 Стойка DI5M3x18 Плата
Шайба DIN 125 D3 Корпус

VMH3
Винт DIN 7965 M3x6 Стойка DI5M3x18 Плата
Шайба DIN 125 D3 Корпус

VFM1 1,0
VFM2 1,0
VFM3 1,0

XP1
B4B-XH-A
JST

Цепь	←
V	1
OUT	2
IN	3
COM	4

→ V_{+9.0 В}
OUT
IN