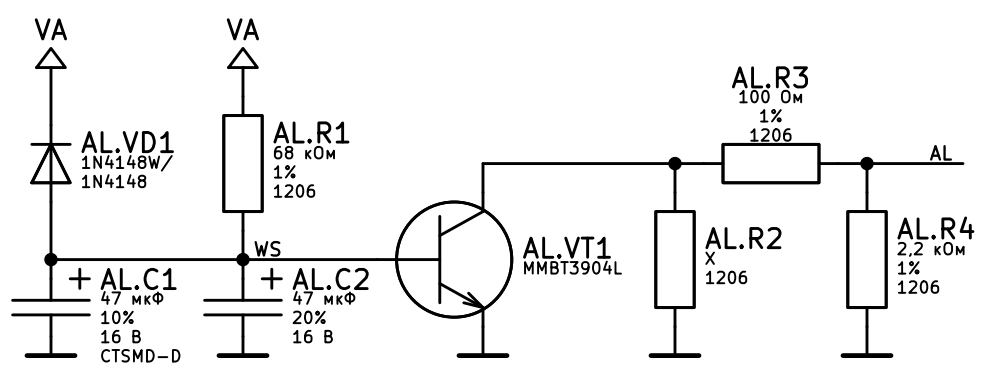
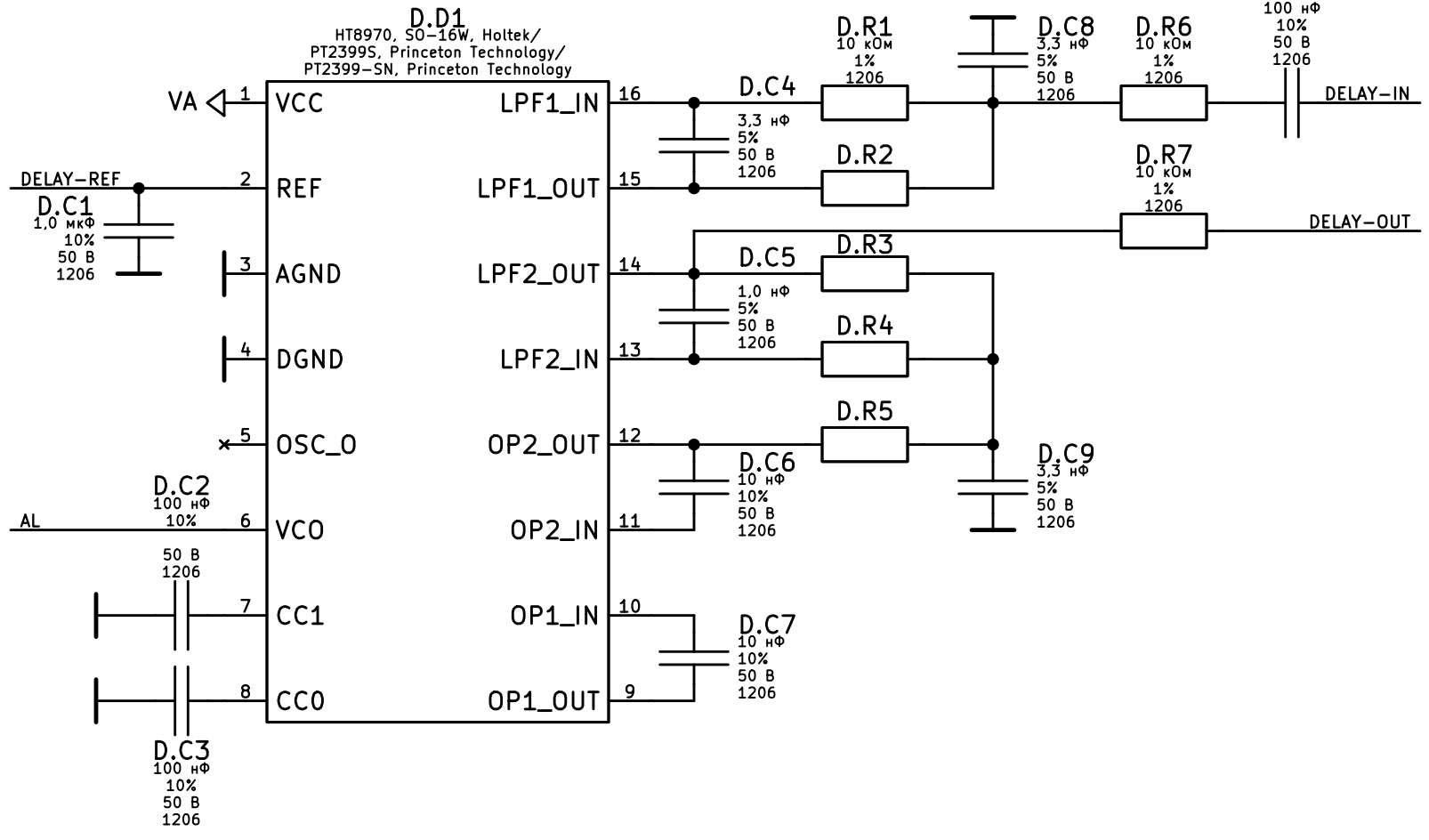


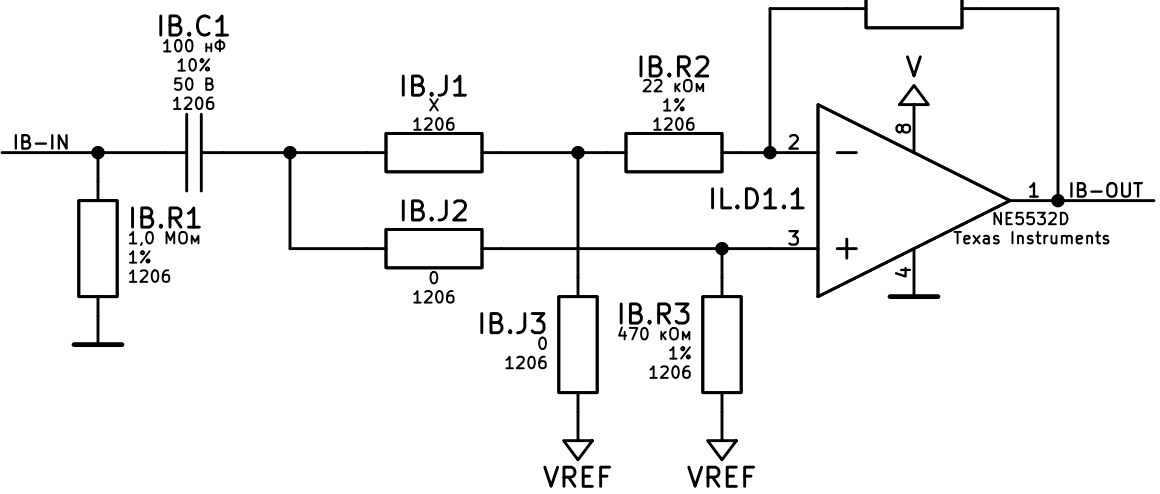
ANTI-LOCKUP



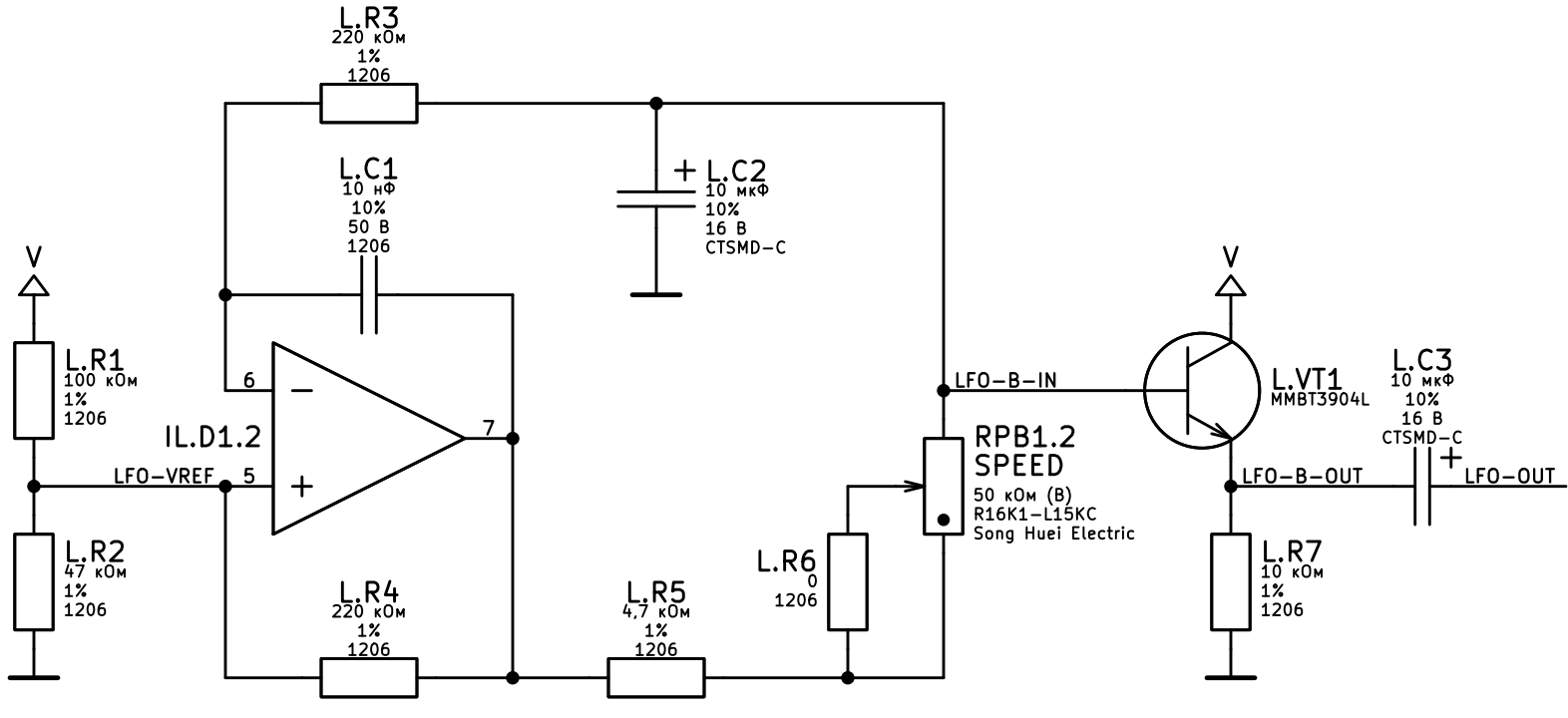
DELAY STAGE



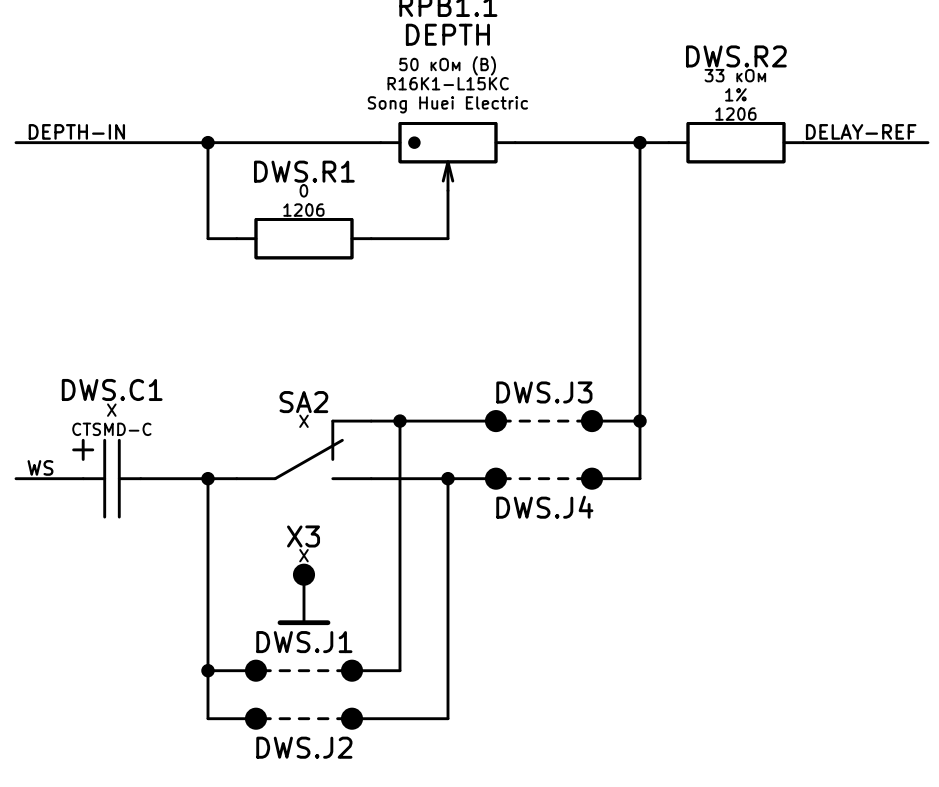
INPUT BUFFER



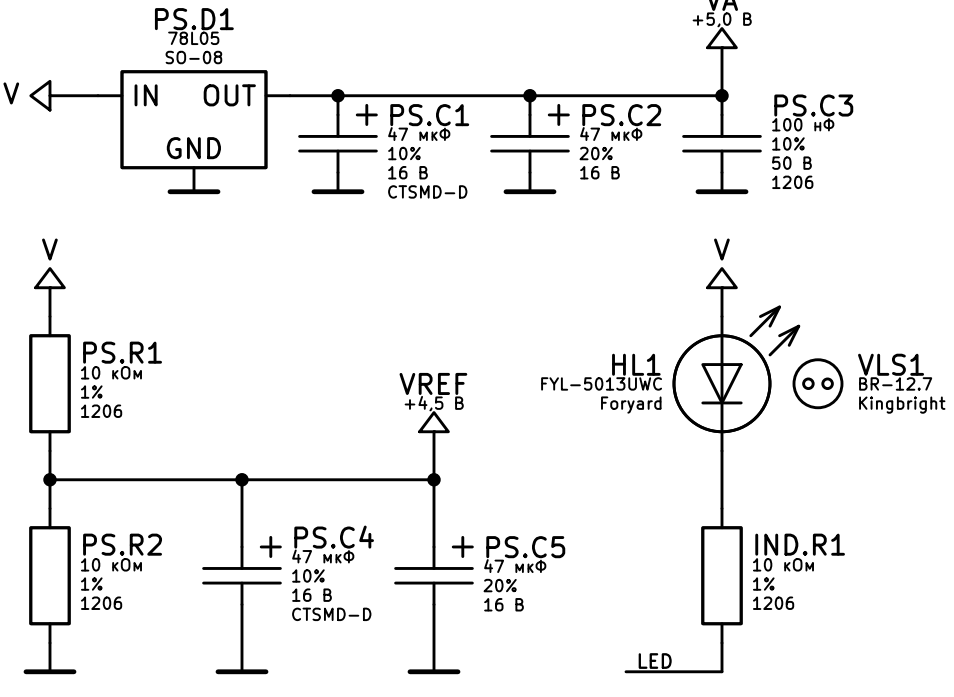
LOW-FREQUENCY OSCILLATOR



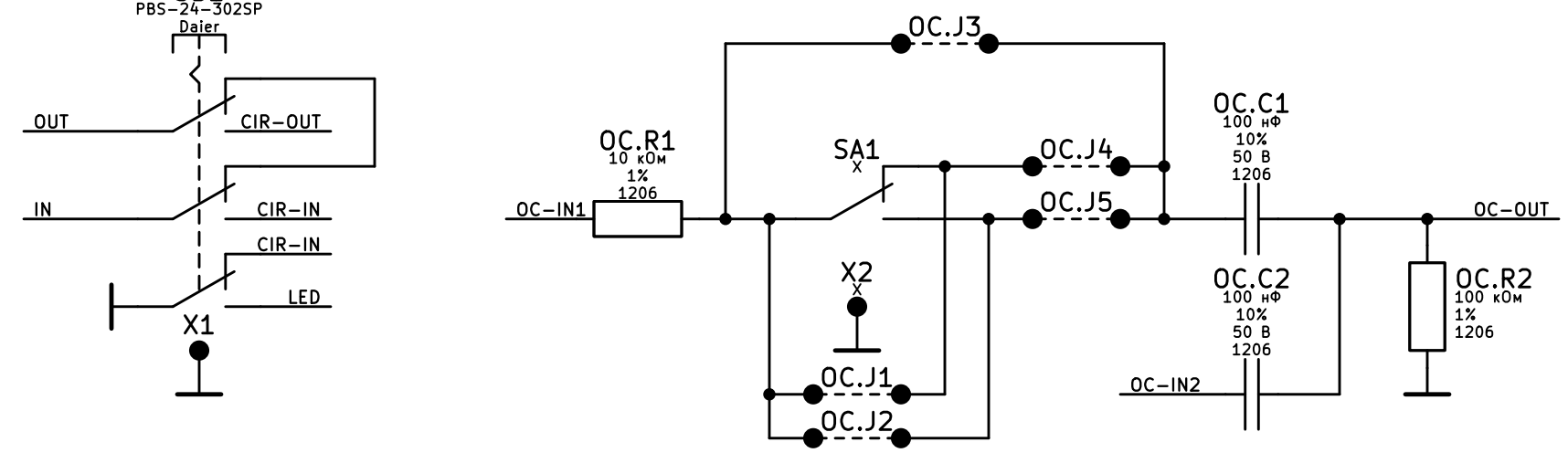
DEPTH & W/S CONTROLS



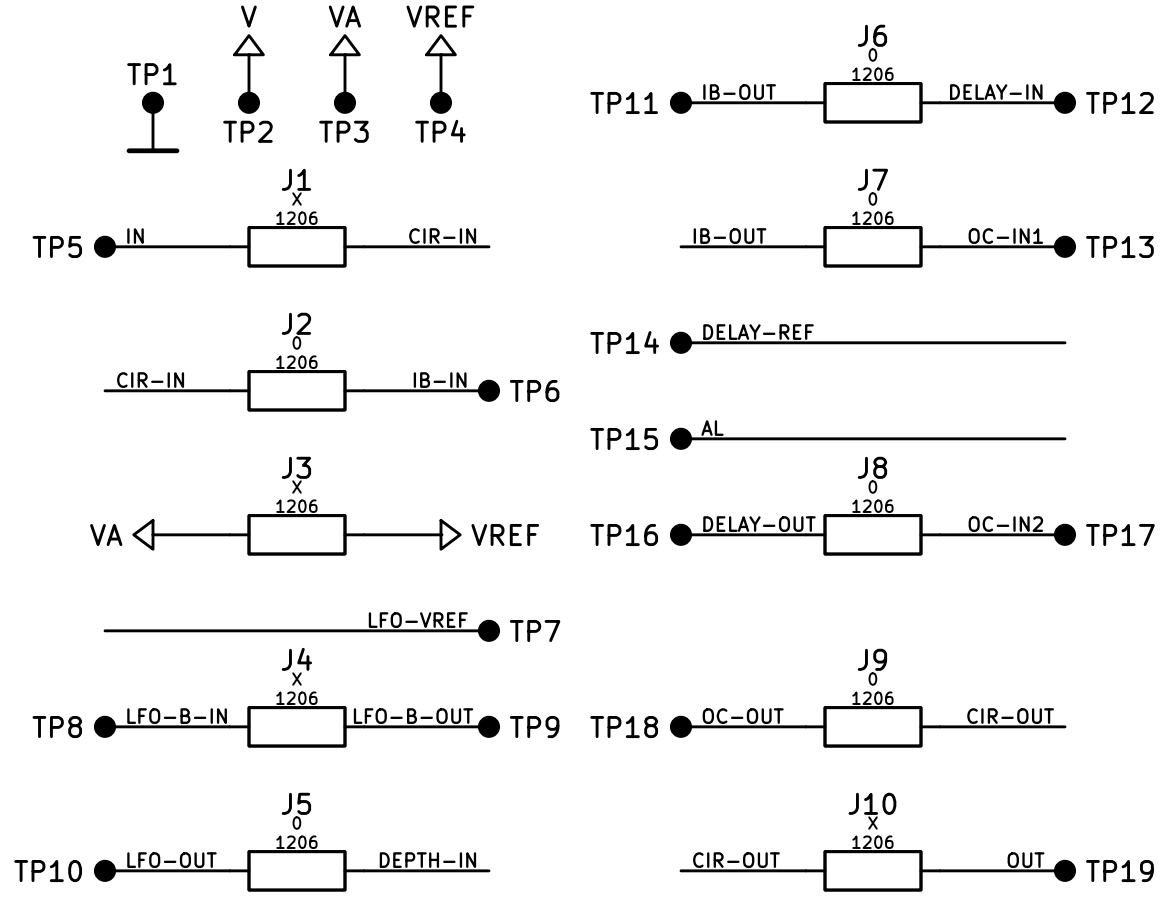
POWER SUPPLY



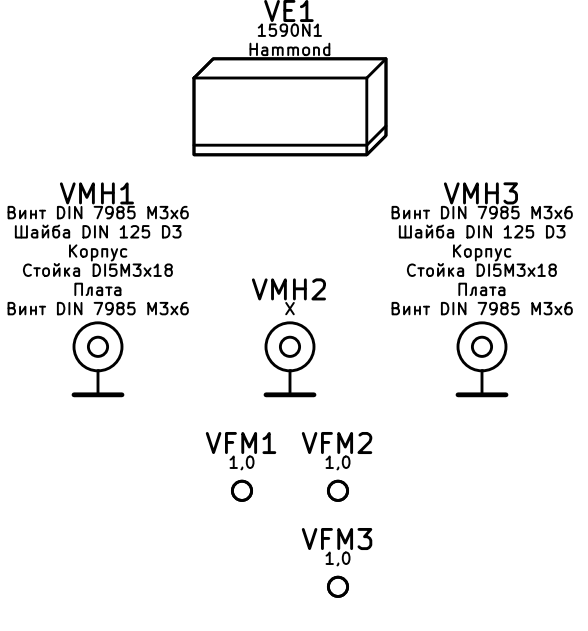
BYPASS CONTROL OUTPUT CIRCUIT & V/C CONTROL



JUMPERS & TEST POINTS



ENCLOSURE



Поз.	Наименование
R1	Не устанавливать
VD1	1N5817
C1	100 мкФ, 20%, 16 В
C2	100 мкФ, 10%, 16 В
C3	100 нФ, 10%, 50 В
C4	100 нФ, 10%, 50 В
GB1	Не устанавливать
J1	Замкнуть
J2	Не замыкать
J3	Замкнуть
J4	Замкнуть

Цепь	←	→
V	1	V _{+9.0 В}
OUT	2	OUT
IN	3	IN
COM	4	—