

INPUT BUFFER

The diagram shows an input buffer circuit. The input signal **IB-IN** is connected to a network of components: a resistor **IB.R1** (1.0 kΩ, 1%, 1206) to ground, a capacitor **IB.C1** (100 nF, 10%, 50 B, 1206) in parallel with the resistor, and a parallel combination of two resistors **IB.J1** and **IB.J2** (both 1206) connected to the non-inverting input (pin 3, labeled '+'). The inverting input (pin 2, labeled '-') is connected to a feedback network consisting of a resistor **IB.R2** (22 kΩ, 1%, 1206) from the output and a resistor **IB.R4** (22 kΩ, 1%, 1206) to ground. A diode **IL.D.1.1** is connected between the inverting input and the output. The output of the op-amp is **IB-OUT**. The op-amp is an **NE5532D** by Texas Instruments. Power pins 4 and 8 are connected to **VRFF**.

The schematic diagram illustrates a power supply circuit for a 12V LED strip. The circuit includes a 78L05 voltage regulator (PS.D1) with input and output capacitors (PS.C1, PS.C2, PS.C3). It features a voltage divider (PS.R1, PS.R2) for a reference voltage (VREF) and a current-limiting resistor (IND.R1) for the LED. A diode (HL1) and a buzzer (VLS1) are also shown.

[illegible][illegible]

VFM1
1590N1
Hammond

VMH1
Винт DIN 7985 M3x6
Шайба DIN 125 D3
Корпус
Стойка D15M3x18
Плата
Винт DIN 7985 M3x6

VMH2
Винт DIN 7985 M3x6
Шайба DIN 125 D3
Корпус
Стойка D15M3x18
Плата
Винт DIN 7985 M3x6

VMH3
Винт DIN 7985 M3x6
Шайба DIN 125 D3
Корпус
Стойка D15M3x18
Плата
Винт DIN 7985 M3x6

XP1
B4B-XH-A
JST

| Цепь | ← |
|------|---|
| V | 1 |
| OUT | 2 |
| IN | 3 |
| COM | 4 |

→ V_{+9,0 В}
OUT
IN

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- TKPN1.37.A-2
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Big Devil Vibrato [REVZA]
- | | |
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