

VA

AL.VD1
1N4148W/
1N4148

AL.C1
47 mF
10%
16 B
CTSM-D

VA

AL.R1
68 kΩ
1%
1206

WS

AL.C2
47 mF
20%
16 B

AL.VT1
MMBT3904L

AL.R2
1206

AL.R3
100 Ω
1%
1206

AL

AL.R4
2.2 kΩ
1%
1206

INPUT BUFFER

The schematic diagram illustrates an input buffer circuit. The input signal, labeled IB-IN, is connected to a 100 nF capacitor (IB.C1) and a 1.0 MΩ resistor (IB.R1) to ground. The signal then splits into two parallel paths, each containing a 1206 resistor (IB.J1 and IB.J2), which are connected to the non-inverting input (pin 3, labeled '+') of the NE5532D op-amp. The inverting input (pin 2, labeled '-') is connected to a 22 kΩ resistor (IB.R2) and a feedback path consisting of a 22 kΩ resistor (IB.R4) and a voltage source (V) in series. The output of the op-amp (pin 1, labeled '1') is labeled IB-OUT. The op-amp is powered by two VREF sources connected to pins 4 and 5. The component values and tolerances are: IB.C1 (100 nF, 10%), IB.R1 (1.0 MΩ, 1%), IB.J1 (1206), IB.J2 (1206), IB.R2 (22 kΩ, 1%), IB.R4 (22 kΩ, 1%), IB.J3 (1206), and IB.R3 (470 kΩ, 1%). The op-amp is identified as NE5532D from Texas Instruments.

VFM3
1590N1
Hammond

Винт DIN 7985 M3x6
Шайба DIN 125 D3
Корпус
Стойка DI5M3x18
Плата

VFM3
Винт DIN 7985 M3x6
Шайба DIN 125 D3
Корпус
Стойка DI5M3x18
Плата

VFM1
1,0
○

VFM2
1,0
○

VFM3
1,0
○

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- TKPN1dF -
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<http://github.com/Adept666>
Little Angel Chorus [REV2A]
- Sheet: /
File: TKP-1.37A-1.sch
- Title: TKP-1.37A-1**
- | | | |
|---|------------------|---------|
| Size: A2 | Date: 2023-11-05 | Rev: 2A |
| KiCad E.D.A. kicad_5.1.12-B4ad8e8a8692ubuntu20.04.1 | | Id: 1/1 |