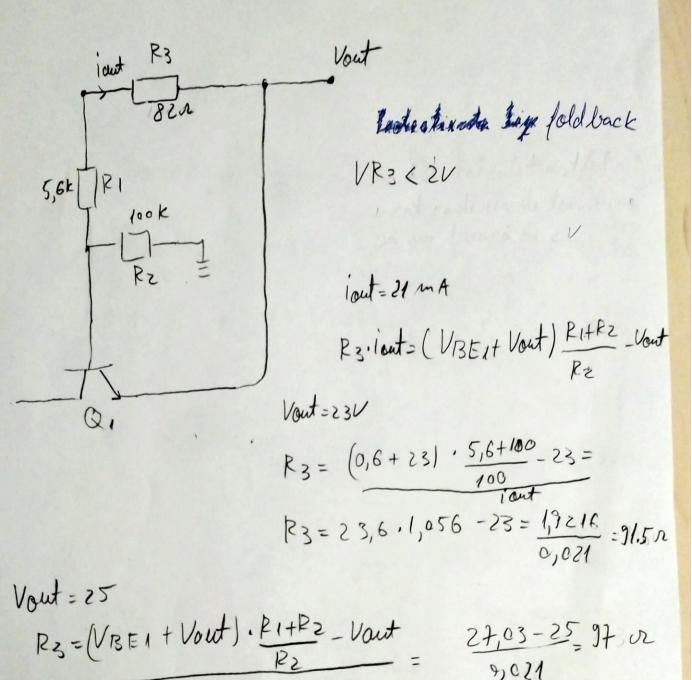
Over current Protection



9,021

ichox R3= 82 2 SMD 0805 ±1% 125 mW

iout=21ma aleg R1=5,6 to

Vout= 23V

Vout = 25V =)
$$18 = 10 \left[\left(1 + \frac{5.6}{Rz} \right) \cdot 0.6 + \frac{5.6}{Rz} \cdot 25 \right] =)$$

$$|27(0,6+\frac{3,36}{R_2}+\frac{140}{R_2})=)21=7,32+\frac{1749}{R_2}=13,68=\frac{1749}{R_2}=$$

Voltage Referance

4,5KCIP RUL ZBZX84-C5VG

From the data sheet 12 min = 5 m A

i choosed iz = 6 mA

iz = Vin- VZ =) RRef = ViN- VZ = 30-5,6 _ 4,06 kr

33-5,6 -4,56KD

ichoose RRel=4,5k2

RRY = 2 x 5, 102 SMD 0805 ± 140 0,125W 2 x 1 k R SMD 0805 ± 140 0,125W 1 x 1,5 k R SMD 0805 ± 140 0,125W to limit the jawer to an ok value and respect.

12>5 mA i chosed PRel=4,5 kr

PRel= 2x 510 2 SMD 0805 ± 140 0,125W 2x 1k2 SMD 0805 ± 10/00,125W 1x 1,5 k2 SMD 0805 ± 140 0,125W

12 min = Vinnin-Vt = 30-5,6 = 5,42 mA > 5 mA.

Rel 4500

12 max = VINMax-VZ = 33-5,6 = 6 m A>5 m A RRed 4500

PMax R17 = 12 max · R17 = 6 m A · 1,5 k &= 54 m W & 125 m W

Series Pass Element PD = Vout max " i out max = 25, 0,018 = 450 m W < 15W The series element és a transister MJD31C JMD D-PACE NAN 1000 34 15W RL = rout Max = 25 = 138852 VCE > Vin max = 33V M1,2 k R 2 1=) Mary 1 1/1 1/1 W Iransistas current source

$$V_{BEI} + R_{i} = 2 V_{D1,2} = R = \frac{V_{BE}}{i} = \frac{0.65}{0.004} = 1645 D$$

i chose $i = 4 \text{ m A}$

R = 1502 SMD 0805 ±140 0,125 W D1, D2 an aleg de Eyes 1 N4148 SMD, 0805, 30 em A LocV Current Mirror For improved characteristics of the error amplifier ichosed az az transistors PNP BC807 SMD SOTES 45V 800 m A Biasing Resistors VE > 0.7V VR=R·IR=> R= 11 = 0,002 = 500 se ix = 2mt = 1. icurrent source Aleg 510 2 SMD 0805 ± 1% 0,125 W

R IV (510 R R) 510 R

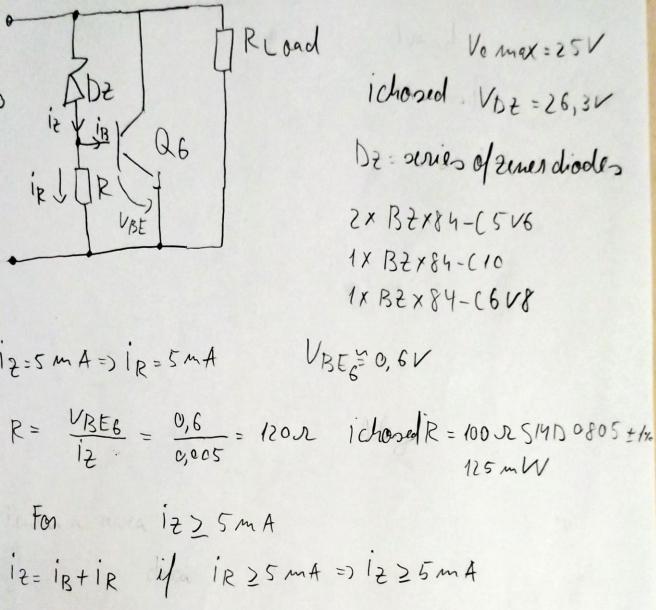
Q2 R Q3

13

Negative Feed Back Network

I chose RIZ=loka: Re = 31kl -> Vo min] Re = 33kl Re = 54,6kl -> Vo max] Re = 33kl but Re = R15+R16 So ichose R15=lok R and R16=22k R So R1 = 32 k 1 Worst Case Scenario for Vo min/max Pot = 4KN] => Vo max = (1+32+4).5,2 = (1+36).5,2 VRef = 5,2V] Vo max = 4,6.5,2 = 23,92 V Vo min = (1+ 32), 5,2 = (1+ 52),5,2 Vo min = 17,08 V $\Delta V = 23,92 - 17,08 = 6,84 V$ Essos for vo max $\xi = \frac{24,38-25}{75},100 = -4,32 %$ For oftim conditions Vicel=5,6]=> Ve max = 26,32 V Pot=5kx] => Ve min = 17,54 V For V Rel: 5,6V] => Ve max = 25,76V Vo min = 18, 4V POT=4Kr J

Over Voltage Protection



The overvoltage protection clamp the Voltage of VBT+VDZ=)

Q6 is a youter transistor.

DPACK 100V 15 W NPN M & D31CSMD

in worst case the protection activate at =(5,2×2)+3,4+6,4

-)26,2+VBEG = 26,8V

DC POWER and CHRRENT CALCULATIONS

=)
$$i_1 = \frac{Vi - 2Vi}{Pitrz + PistProtR3}$$
 => $i_1 = \frac{30 - 1,2}{5000} = 5,72 mA$

* In practice icz dicz because a part of icz's current isomplied to the Series pass transistor

ic3 = ic3-iB1

ic2 = ic8

Vi = VcE1+Vo+VBE1+VBE3+VBE3

VcE1=Vi-Vo-VBE1-VBE8-VBE3'=30-25-2,1=3V

Q5 and Q6 are blocked => VBE<0,3V

in • (R15+R16+ Pot+R1+) = Vout => in = Vout

R15+R16+R17+Pot

=> 25/17.000 = 531 MH

Practice VEC+ CVECS again because of the base current of

Vi= R9 ic++VEC++VCEz+VRef=>VCEz=Vi-VEC+-R9.1C7 =>VCEZ=22,8V

VCEZ = VCE3 = 22, PV

Vi = VECz+ VCE z + icz. Rs + VBE4 + VCE4 = > VCE4 = 30-25,1 VCE4 = 4,9 V

Power dissipation

PQ1 = VCE1 · IC1 = 66 m W

PQz = VCEZ icz = 52,44 mW

PQ3 = VCE3 · ic3 = 52,44mW

PQ4 = VCE4 · 1C4 = 23 mW

PQ5 = VCE5 · ic5 = 36 mW] - Protections are off PQ6 = VCE6 · ic6 = 35 mW]

PRI=PRZ=PRIS=PRZO=PR3=112.PZ=32,7 mW

PR4=13. R4=15mW

PR5=12, R5=44MW

PR6=123, R6=15MW

PRI= PRO = 123 RT=29,37MW

PRS = PR10 = 1c2 · R10 = Z,7 mW

PR11=124, P11=3,25 mW

PR14=10ut. R14=36 MW

PRIZ= 1212 . RIZ= 365 HW

PR13=1213. R13=6,5 mW

PR15=14, R15=2, &1 mW

PR16 = 142, R16 = 6,2 mW

PPOT = 142 . RPOT = 1,41 M W

P17=14 · R17=2,81 mW

P18 = 1 R18 - P18 = 1,3 1 W PRL=iout. RL=529, 2 mW