# The Bipolar Junction Transistor Inverter

Laboratory D1

3C2 DIGITAL CIRCUITS

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## 1 Introduction

The purpose of the D1 laboratory is to examine the properties and functions of a Bipolar Junction Transistor. The experiments will encompass the topics covered in the 3C2 Digital Circuits lectures. The lab also serves as an introduction to designing circuits encompassing Bipolar Junction transistors in Multisim.

# 2 Theory

A bipolar transistor is comprised of three layers of differently doped semiconductor materials Constructing P-N-P or N-P-N transistors.

Bipolar transistors are named as such due to their operation involving holes and electrons. Electrons are majority charge carriers in N-type semiconductors, whereas holes are majority charge carriers in P-type semiconductors.

#### 2.1 A:Bipolar Junction Transistor Characteristics

A bipolar junction transistor is a type of transistor that relies on the contact of two types of semiconductor for its operation. There are two types of BJT transistors, NPN and PNP.

In an NPN transistor, a thin and lightly doped P-type base is sandwiched between a heavily doped N-type emitter and another N-type collector; In a PNP transistor, a thin and lightly doped N-type base is sandwiched between a heavily doped P-type emitter and another P-type collector.(Ref.1)

NPN transistors are much more popular than PNP transistors because electrons move faster in a semiconductor. As a results, a NPN transistor has a faster response time compared to a PNP transistor. BJTs can be found either as individual discrete components, or in large numbers as parts of integrated circuits. BJTs can be used as amplifiers, switches, or in oscillators.(Ref.2)

#### 2.2 B:Resistevely Loaded BJT Inverter Characteristics

The BJT modes of operation are easily found due to the fact that it can only be in three modes, cut-off, forward active and saturated. These two factors of easy modifiability and easy steps to find its mode of operation make the BJT a very useful inverter.

Varying various components and characteristics of the BJT inverter can adversely change the voltage transfer characteristics. The resistor values of Rb and Rc change both the slope of the Vi and the ending Vol. Increasing the value of Rb decreases the slope of the Vil and increases the Vol, whereas decreasing Rb increases the slope of the VTC and decreases Vol. Increasing Rc does the opposite of increasing Rb, it increases the slope and lowers the Vol, decreasing Rc does the opposite. Decreasing the Bf alters the slope of the Vil only by decreasing it, while an increase makes the Vil drop faster.(Ref.6)

The BJT inverter is a very stable inverter and adjusting its Vil is relatively simple. By simply changing the resistor values, one can attain a desired Vil and obtain desired critical voltages.

#### 2.3 C:BJT Inverter Switching Performance

In an electronic circuit there are no mechanical switches. The switching action itself is performed by a transistor with an input voltage (Pic. 8). In a switching operation a transistor is usually controlled in two condition states, which can be referred to as loosly as the "on" state and the "off" state. A switch should appear as a short circuit when turned on and an open circuit when turned off. (Ref. 5) Although the BJT can be made to act as a switch it technically isn't one in a mechanical sense but it servers as a useful replacement. Since it is not a mechanical switch it is desirable to always switch from one state to the other as quickly as possible with little loss time in between.

# 3 Results

# 3.1 A:Bipolar Junction Transistor Characteristics

## 3.1.1 A1:Collector Current against Base - Emitter Voltage

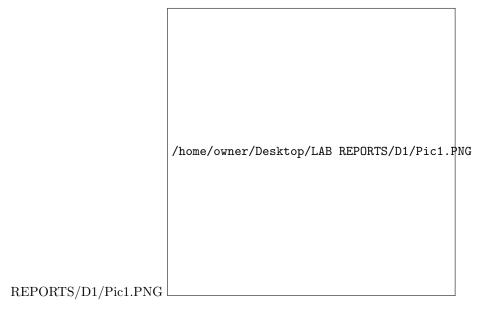


Figure 1: A.1 Part.9

Figure 11 Shows the initial set up  $\frac{1}{2}$ 

Table 1: A.1 Part.4	
BF, the foreward active current gain(Bf)	220
BR, the foreward active current gain(Br)	4
TF, the foreward transit time(Tf)	0.325 E-9s
TR, the reverse transit time(Tf)	100E-9s
CJC,B-C zero-bias depletion capacitance (Cbc)	9.12E-12F
CJE,B-E zero-bias depletion capacitance (Cbe)	27.01E-12F

Table 3.1.1 Completed from the given values

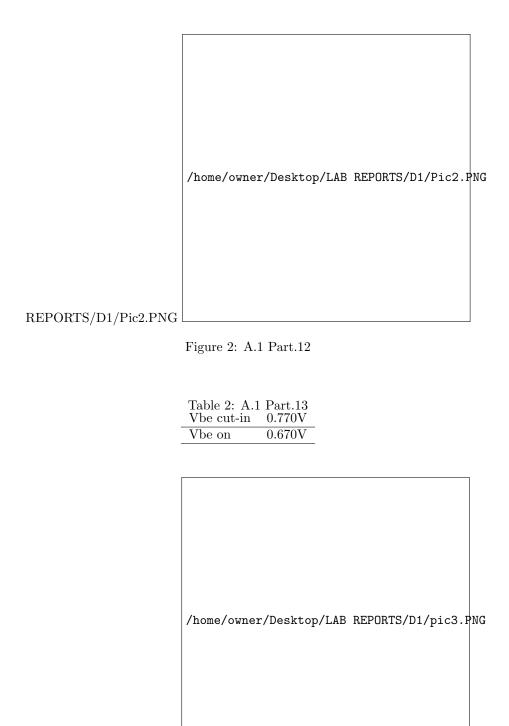


Figure 3: A.1 Part.13

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A.1 Part.13 Q. How do the values of Vbe cut-in and Vbe on compare with those in lectures? Ans. The values in the lectures are mostly ideal. Here we see a gradual increase in the value and a more realistic curve.

## 3.1.2 A2:Collector Current vs. Collector-Emitter Voltage

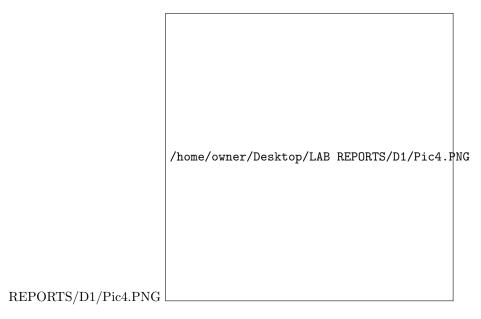


Figure 4: A.2 Part.1

Table 3: A.2 P	art.5
Bf@Ib=10uA	2500
Bf@Ib=100uA	210
Vcesat@Ic=5mA	0.25V

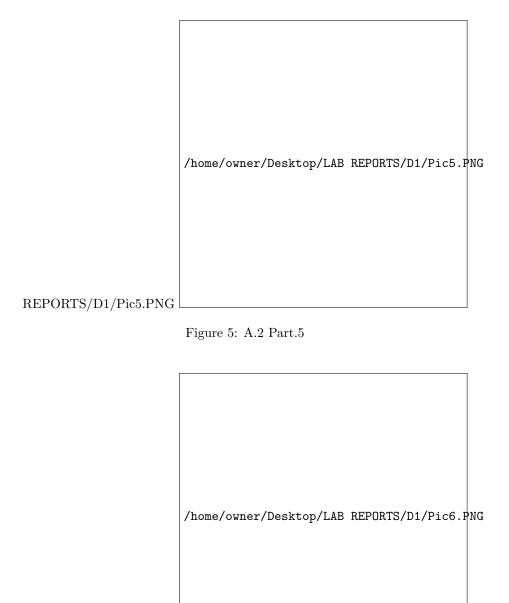


Figure 6: A.2 Part.6

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Table 4: A.2 Part.6		
Bf@Ib=10uA	140	
Bf@Ib=100uA	160	
Vcesat@Ic=5mA	0.22V	

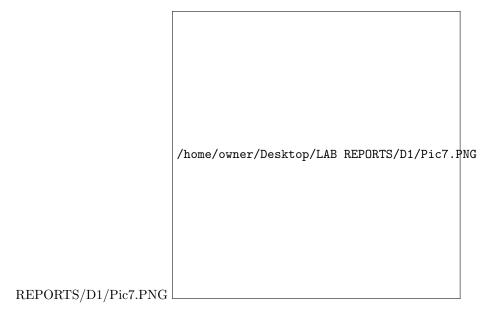


Figure 7: A.2 Part.7

# 3.2 B:Resistevely Loaded BJT Inverter Characteristics

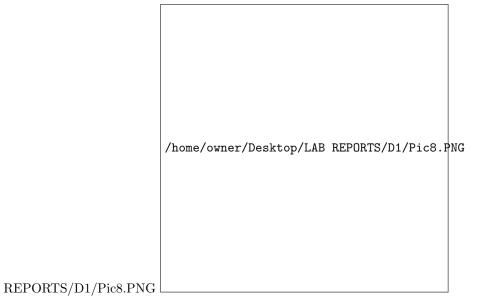


Figure 8: B Part.3

Table 5: B Voh	Part.6 5V
Vil max	0.53V
Vol	0.3V
Vih min	1.1V

$$Voh = Vcc = 5V$$
 
$$Vol = Vcesat = 0.3V$$
 
$$Vilmax = Vbecut - in = 0.53V$$
 
$$Vihmin = Vbeon + \frac{Rb}{Bf * Rc}[Vcc - Vcesat] = 1.1V$$

B Part.8

Q. Predict what will happen if Rc is increased from 1kOhm to 4kOhm. Ans. The wave form will decay quicker and a steeper decline will be noticed in the wave.

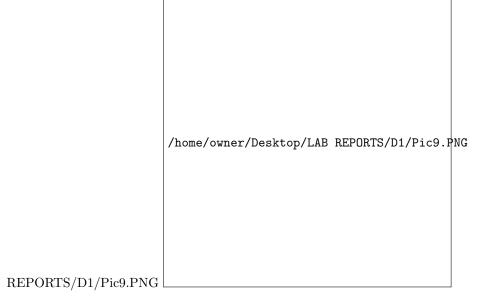
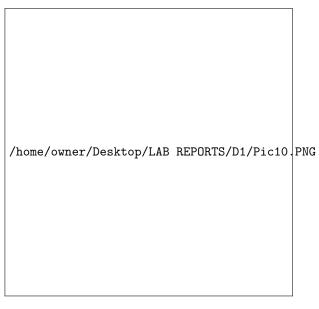


Figure 9: B Part.9

# 3.3 C:BJT Inverter Switching Performance



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Figure 10: C Part.2

Table 6: C Part.2  
td 
$$1.5 * 10^{-7}s$$
  
tf  $1.3 * 10^{-7}s$   
ts  $1.3 * 10^{-7}s$   
tr  $6 * 10^{-7}s$ 

$$td = Rb(Cje + Cjc)ln \frac{Vcc}{Vcc - Vbecutin} =$$

$$td = 20000(27*10^{-12} + 9.12*10^{-12})ln\frac{5}{5 - 0.53} = 8*10^{-8}s$$

$$tf = 0.8Bf(Tf + Rc*Cjc)ln\frac{Bf*Ibf}{Bf*Ibf - Icmax} =$$

$$tf = 0.8 * 220 (0.325 * 10^{-9} + 1000 * 9.12 * 10^{-12}) ln \frac{220 * 2.165 * 10^{-4}}{220 * 2.165 * 10^{-4} - 4.7 * 10^{-3}} = 1.73 * 10^{-7} s$$

$$Ibf = \frac{Vi - Vbeon}{Rb}$$

$$Ibf = \frac{5 - 0.67}{20000} = 2.165 * 10^{-4} A$$

$$Icmax = \frac{Vcc - Vcesat}{Rc}$$

$$Icmax = \frac{5 - 0.3}{1000} = 4.7 * 10^{-3} A$$

$$ts = tsln \frac{Ibf - Ibr}{\frac{Icmax}{Bf} - Ibr}$$

$$ts = 3.927 * 10^{-7} ln \frac{2.165 * 10^{-4} - (-3.35 * 10^{-5})}{\frac{4.7 * 10^{-3}}{220} - (-3.35 * 10^{-5})} = 5.96 * 10^{-7} s$$

$$Ibr = -\frac{Vbeon}{Rb}$$

$$Ibr = -\frac{0.67}{20000} = -3.35 * 10^{-5} A$$

$$Ts = \frac{(1+Br)Tbf + Bf*Tbr}{1+Bf+Br} = \frac{(1+Br)Bf*Tf + Br*Bf*Tr}{1+Bf+Br}$$

$$Ts = \frac{(1+Br)Tbf + Bf * Tbr}{1+Bf + Br} = \frac{(1+4)220 * 0.325 * 10^{-9} + 4 * 220 * 100 * 10^{-9}}{1+220+4} = 3.927 * 10^{-7}$$

$$tr = 0.8 * Bf * (Tf + Rc * Cjc)ln \frac{\frac{Icmax}{Bf} - Ibr}{-Ibr}$$

$$tr = 0.8 * 220 * (0.325 * 10^{-9} + 1000 * 9.12 * 10^{-12}) ln \frac{\frac{4.7 * 10 - 3}{220} - 3.35 * 10 - 5}{-3.35 * 10 - 5} = 8.2 * 10^{-7} s$$

Table 7: C Part.3  
td 
$$8*10^{-8}s$$
  
tf  $1.73*10^{-7}s$   
ts  $5.96*10^{-7}s$   
tr  $8.2*10^{-7}s$ 

#### C Part.4 Q. Predict what will happen if Rb is reduced to 5kOhm.

Ans. A decrease in Rb will effect all 4 cycle times and thus when a time is divided by Rb it will increase. The increse will make the wave function apear less square therefore the drop off point will look less pronounced and the transition time will thus increase.

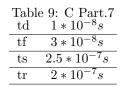


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Figure 11: C Part.5

C Part.6 Q. Predict what will happen if Rc is increased to 4kOhm.

Ans. Since Rc is responsible for the value of ICmax a higher value for Rc will mean a decreased value of ICmax thus giving a smaller value for every time across the board except for td since it is not effected by ICmax or Rc. The drop off point will also become steeper as Vce is raised giving it a square wave look(but not turning it into a square wave).



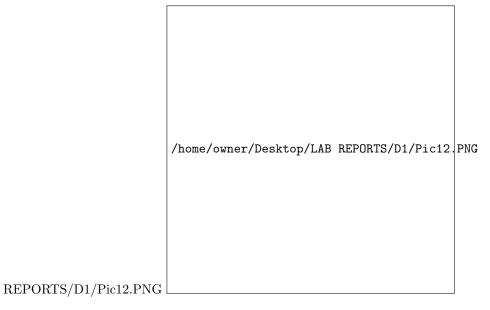


Figure 12: C Part.7

## 4 Discussion

(i) What are the limitations on the accuracy of your results?

Human error played a big part in discerning the results as subjective viewing was used more than a real form of accurate measurement. Rounding the results also could have led to a small discrepancy between the calculated and observed results.

(ii) How do the values of parameters compare with those obtained in lectures?

In our simulation we noticed that the lines in the transient analysis were not perfectly horizontal. The values differed somewhat from those obtained in lectures as the values and diagrams given in lectures represent an ideal state. The Multisim plots tend to lie somewhere in between the ideal state and real world values .

(iii) What are the advantages or disadvantages of circuit simulations such as the one carried out in the experiment?

The advantages of this simulation were that 3 distinct circuits could be simulated and analysed in a 3hour lab. If were to build the circuits manually it would have taken up substantially more time and resources.

The disadvantages of this simulation is that it lacks the real world aspect of dealing with bipolar transistors. In theory someone can know how to calculate and analyse a bipolar transistor without ever having to know what it looks like which for field associated work may be a huge disadvantage.

(iv) What are the benefits or drawbacks of circuit simulation and of MultiSim in general as applied to the design of electronic circuits?

The advantages of simulating an electric circuit are of course that it saves time and effort. Along with the usual advantages a simulation also allows for quicker modifications of circuit designs and instantaneous simulation of that circuit with a graph.

The disadvantages of the MultiSim simulations are that our virtual environment does not factor in environmentally dependent errors such as temperature and humidity. As we all know heat increases resistance. Thus the simulation can only show how a circuit will behave in ideal conditions but not in adverse ones.

(v) What are the essential elements of good circuit simulation and simulators?

A good simulator should be comprised of a large library of components to choose from and be able to experiment and simulate multiple circuits quickly and efficiently. All the models in the simulation should be accurate and up to date with standard industry components.

(vi) What is the role of the Electronic Engineer in this regard?

The role of an Electronic Engineer in this regard is to design and develop the electronic circuit. The engineer must take the pre-requisite demands the circuit must fulfill and the space required for those components to fit in the wider application into consideration. Finally the role of the engineer is to understand and pre-plan for events and circumstances Multisim cannot encompass such as extreme environmental factors.

## 5 Conclusion

BJT's have their limitations, but given their wide potential and current applications in electronics, the conclusion drawn from completing this laboratory is that the BJT is an important piece in the world of semiconductors. Although there were discrepancies between the Multisim values and the real world values, multisim has proven a very fast and relatively accurate way of designing and testing electronic circuits encompassing BJT's.

## A References

This is a list of links used for further information.

- 1. http://fourier.eng.hmc.edu/e84/lectures/ch4/node3.html
- 2. http://aries.ucsd.edu/NAJMABADI/CLASS/ECE65/06-W/NOTES/BJT1.pdf
- 3. The Art of Electronics-Paul Horowitz and Winfield Hill
- 4. Solid State Electronic Devices, Sixth Edition pg.251
- 5. Solid State Electronic Devices, Sixth Edition pg.363
- 6. http://www.allaboutcircuits.com/textbook/semiconductors/chpt-4/transistor-switch-bjt/