

Quiz Microprocessor

Total points **10/10** ?

Solve all Questions

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Class : TEC B DIV

✗ Name of Student *

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✗

Correct answer

Correct Answer

✗ Roll No *

50

✗

Correct answer

Correct Answer



✓ 1. The instruction, MOV AX, 0005H belongs to the address mode

1/1

☐ a) register

☐ b) direct

☒ c) immediate



☐ d) register relative

☐ Other:

✓ 2. The instruction, MOV AX,[BX] is an example of

1/1

☐ a) direct addressing mode

☐ b) register addressing mode

☐ c) register relative addressing mode

☒ d) register indirect addressing mode



✓ 3. Which of the following instruction is not valid?

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☐ a) MOV AX, BX

☒ b) MOV DS, 5000H



☐ c) MOV AX, 5000H

☐ d) PUSH AX



✓ 4.The instruction that pushes the contents of the specified register/memory location on to the stack is *

1/1

- ☐ a) PUSHF
- ☐ b) POPF
- ☒ c) PUSH
- ☐ d) POP



✓ 5.The instruction that subtracts 1 from the contents of the specified register/memory location is

1/1

- ☐ a) INC
- ☐ b) SUBB
- ☐ c) SUB
- ☒ d) DEC



✓ 6. 8086 is ----- bit microprocessor

1/1

- ☐ 8 bit
- ☐ 20 bit
- ☒ 16 bit
- ☐ 32 bit
- ☐ Other:



✓ 7. In general, the source operand of an instruction can be

1/1

- ☐ a) memory location
- ☐ b) register
- ☐ c) immediate data
- ☒ d) memory location and register



✓ 8. The instruction, CMP to compare source and destination operands it performs

1/1

- ☐ a) addition
- ☒ b) subtraction
- ☐ c) division
- ☐ d) multiplication



✓ 9. During comparison operation, the result of comparing or subtraction is stored in

1/1

- ☐ a) memory
- ☐ b) registers
- ☐ c) stack
- ☒ d) nowhere



✓ 10. The advantages of assembly level programming is

1/1

- ☐ a) flexibility of programming is more
- ☐ b) chances of error are less
- ☐ c) debugging is easy
- ☒ d) all of the mentioned



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