

PowerPlay Table Content				
Identifier	Offset	Size	Description	Type
1	0x0	0x2	Table Size	Hex
2	0x2	0x4	Format and Content rev	Hex
3	0xB	0x2	Clock Info offset	Hex
4	Clock Info Offset	0x1	Number of PowerPlay States	Hex
5	Clock Info Offset + 1	0x1	Length per State	Hex
6	Clock Info Offset + 2	0x3	GPU Clock 1	KHz
7	Clock Info Offset + 5	0x3	Memory Clock 1	KHz
8	Clock Info Offset + 8	0x2	GPU Voltage 1	mV
9	0x26	0x2	Unknown Info Offset	Hex
10	0x2A	0x2	Fan Info Offset	Hex
11	Fan Table Offset + 1	0x1	Hysteresis	°C
12	Fan Table Offset + 2	0x2	Temp. Target 1	°C
13	Fan Table Offset. + 4	0x2	Temp. Target 2	°C
14	Fan Table Offset. + 6	0x2	Temp. Target 3	°C
15	Fan Table Offset. + 8	0x2	Fan Speed 1	%
16	Fan Table Offset. + 10	0x2	Fan Speed 2	%
17	Fan Table Offset. + 12	0x2	Fan Speed 3	%
18	Fan Table Offset. + 14	0x2	Max Temp.	°C
19	Fan Table Offset. + 17	0x1	Max PWM Fan Speed	%
20	0x2C	0x2	Limits Info Offset	Hex
21	limits Offset +2	0x3	Max GPU Clock	KHz
22	limits Offset +6	0x3	Max Mem Clock	KHz
23	limits Offset +10	0x2	VCE Info Offset	Hex
24	0x36	0x2	GPU Frequency Info Offset	Hex
25	GPU Freq. T. Off.	0x1	Number of GPU frequencies	Int
26	GPU Freq. T. Off. + 1	0x3	GPU Freq 1	KHz
27	GPU Freq. T. Off. + 4	0x2	GPU Voltage 1	mV
35	0x38	0x2	Mem Frequency Info Offset	Hex
36	Mem Freq. T. Off.	0x1	Number of Memory frequencies	Int
37	Mem Freq. T. Off. + 1	0x3	Mem Freq 1	KHz
35	Mem Freq. T. Off. + 6	0x3	Mem Freq 2	KHz
36	0x42	0x2	Max TDP	Int
37	0x46	0x2	Min TDP	Int