

Tabelle1

02_IMXRT1060CEC_rev0_3_ProcessorDataSheet.pdf 01_IMXRT1060RM_rev3_ProcessorReferenceManual.pdf
Page 90 Page 315

Teensyl4.x Prozessor ARM Cortex M7 600MHz
NXP Semiconductors
IMXRT1062DVL6B

10x10mm Ball

Teensyl4.0 / 4.1 Pin	Processor Pin IMXRT1062DVL6B	Pin 10x10 Ball	XBAR1	Register PIN_MUX_MODE	Value MUX_MODE	Register PIN_DAISYCHAIN	Value DaisyChain
0	GPIO_AD_B0_03	G11	XBAR_INOUT17	401F_80C8 IOMUXC_SW_MUX_CTL_PAD_GPIO_AD_B0_03	001 ALT1	401F_862C IOMUXC_XBAR1_IN17_SELECT_INPUT	01
1	GPIO_AD_B0_02	M11	XBAR_INOUT16	401F_80C4 IOMUXC_SW_MUX_CTL_PAD_GPIO_AD_B0_02	001 ALT1	401F_864C IOMUXC_XBAR1_IN16_SELECT_INPUT	0
2	GPIO_EMC_04	F2	XBAR_INOUT06	401F_8024 IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_04	011 ALT3	401F_861C IOMUXC_XBAR1_IN06_SELECT_INPUT	0
3	GPIO_EMC_05	G5	XBAR_INOUT07	401F_8028 IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_05	011 ALT3	401F_8620 IOMUXC_XBAR1_IN07_SELECT_INPUT	0
4	GPIO_EMC_06	H5	XBAR_INOUT08	401F_802C IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_06	011 ALT3	401F_8624 IOMUXC_XBAR1_IN08_SELECT_INPUT	0
5	GPIO_EMC_08	H3	XBAR_INOUT17	Already used for Teensy Pin 0			
6	GPIO_B0_10	D9	NA				
7	GPIO_B1_01	B11	XBAR_INOUT15	401F_8180 IOMUXC_SW_MUX_CTL_PAD_GPIO_B1_01	001 ALT1	401F_8648 IOMUXC_XBAR1_IN15_SELECT_INPUT	1
8	GPIO_B1_00	A11	XBAR_INOUT14	401F_817C IOMUXC_SW_MUX_CTL_PAD_GPIO_B1_00	001 ALT1	401F_8644 IOMUXC_XBAR1_IN14_SELECT_INPUT	1
9	GPIO_B0_11	A10	NA				
10	GPIO_B0_00	D7	NA				
11	GPIO_B0_02	E8	NA				
12	GPIO_B0_01	E7	NA				
13	GPIO_B0_03	D8	NA				
14	GPIO_AD_B1_02	L11	NA				
15	GPIO_AD_B1_03	M12	NA				
16	GPIO_AD_B1_07	K10	NA				
17	GPIO_AD_B1_06	J12	NA				
18	GPIO_AD_B1_01	K11	NA				
19	GPIO_AD_B1_00	J11	NA				
20	GPIO_AD_B1_10	L13	NA				
21	GPIO_AD_B1_11	J13	NA				
22	GPIO_AD_B1_08	H13	NA				
23	GPIO_AD_B1_09	M13	NA				

Teensyl4.0 Pin Backside	Processor Pin IMXRT1062DVL6B	Pin 10x10 Ball	XBAR1	Register PIN_MUX_MODE	Value MUX_MODE	Register PIN_DAISYCHAIN	Value DaisyChain
24	GPIO_AD_B0_12	K14	NA				
25	GPIO_AD_B0_13	L14	NA				
26	GPIO_AD_B1_14	G12	NA				
27	GPIO_AD_B1_15	J14	NA				
28	GPIO_EMC_32	D5	NA				
29	GPIO_EMC_31	C5	NA				
30	GPIO_EMC_37	E4	XBAR_INOUT23	401F_80A8 IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_37	001 ALT1	401F_863C IOMUXC_XBAR1_IN23_SELECT_INPUT	0
31	GPIO_EMC_36	C3	XBAR_INOUT22	401F_80A4 IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_36	001 ALT1	401F_8638 IOMUXC_XBAR1_IN22_SELECT_INPUT	0
32	GPIO_B0_12	C10	XBAR_INOUT10	401F_816C IOMUXC_SW_MUX_CTL_PAD_GPIO_B0_12	001 ALT1	NA, it could be only GPIO_B0_12	NA
33	GPIO_EMC_07	H4	XBAR_INOUT09	401F_8030 IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_07	011 ALT3	401F_8628 IOMUXC_XBAR1_IN09_SELECT_INPUT	0
34 (SD Card)	GPIO_SD_B0_03	K1	XBAR_INOUT17	Already used for Teensy Pin 3			
35 (SD Card)	GPIO_SD_B0_02	J1	XBAR_INOUT06	Already used for Teensy Pin 2			
36 (SD Card)	GPIO_SD_B0_01	J3	XBAR_INOUT05	401F_81C0 IOMUXC_SW_MUX_CTL_PAD_GPIO_SD_B0_01	011 ALT3	401F_8618 IOMUXC_XBAR1_IN05_SELECT_INPUT	1
37 (SD Card)	GPIO_SD_B0_00	J4	XBAR_INOUT04	401F_81BC IOMUXC_SW_MUX_CTL_PAD_GPIO_SD_B0_00	011 ALT3	401F_8614 IOMUXC_XBAR1_IN04_SELECT_INPUT	1
38 (SD Card)	GPIO_SD_B0_05	J2	XBAR_INOUT08	Already used for Teensy Pin 33			
39 (SD Card)	GPIO_SD_B0_04	H2	XBAR_INOUT06	Already used for Teensy Pin 4			

Teensyl4.1 Pin 24 – 33 same than Teensyl4.0	Processor Pin IMXRT1062DVL6B	Pin 10x10 Ball	XBAR1	Register PIN_MUX_MODE	Value MUX_MODE	Register PIN_DAISYCHAIN	Value DaisyChain
24	GPIO_AD_B0_12	K14	NA				
25	GPIO_AD_B0_13	L14	NA				
26	GPIO_AD_B1_14	G12	NA				
27	GPIO_AD_B1_15	J14	NA				
28	GPIO_EMC_32	D5	NA				
29	GPIO_EMC_31	C5	NA				
30	GPIO_EMC_37	E4	XBAR_INOUT23	401F_80A8	001 ALT1	401F_863C	0
31	GPIO_EMC_36	C3	XBAR_INOUT22	401F_80A4	001 ALT1	401F_8638	0
32	GPIO_B0_12	C10	XBAR_INOUT10	401F_816C	001 ALT1	NA, it could be only GPIO_B0_12	NA
33	GPIO_EMC_07	H4	XBAR_INOUT09	401F_8030	011 ALT3	401F_8628	0
34	GPIO_B1_13	D14	NA				
35	GPIO_B1_12	D13	NA				
36	GPIO_B1_02	C11	XBAR_INOUT16	Already used for Teensy Pin 1			
37	GPIO_B1_03	D11	XBAR_INOUT17	Already used for Teensy Pin 0 / Pin 5			
38	GPIO_AD_B1_12	H12	NA				
39	GPIO_AD_B1_13	H11	NA				
40	GPIO_AD_B1_04	L12	NA				
41	GPIO_AD_B1_05	K12	NA				

Teensyl4.1 Pin Backside Pin 43 – 47 same than Teensyl4.0 Pin 34 – 38	Processor Pin IMXRT1062DVL6B	Pin 10x10 Ball	XBAR1	Register PIN_MUX_MODE	Value MUX_MODE	Register PIN_DAISYCHAIN	Value DaisyChain
42 (J2 SD Card)	GPIO_SD_B0_03	K1	XBAR_INOUT09	Already used for Teensy Pin 3			
43 (J2 SD Card)	GPIO_SD_B0_02	J1	XBAR_INOUT06	Already used for Teensy Pin 2			
44 (J2 SD Card)	GPIO_SD_B0_01	J3	XBAR_INOUT05	401F_81C0	011 ALT3	401F_8618	1
45 (J2 SD Card)	GPIO_SD_B0_00	J4	XBAR_INOUT04	401F_81BC	011 ALT3	401F_8614	1
46 (J2 SD Card)	GPIO_SD_B0_05	J2	XBAR_INOUT08	Already used for Teensy Pin 33			
47 (J2 SD Card)	GPIO_SD_B0_04	H2	XBAR_INOUT06	Already used for Teensy Pin 4			
48	GPIO_EMC_24	D3	NA				
49	GPIO_EMC_27	A2	NA				
50	GPIO_EMC_28	D1	NA				
51	GPIO_EMC_22	F1	NA				
52	GPIO_EMC_26	B3	NA				
53	GPIO_EMC_25	D2	NA				
54	GPIO_EMC_29	E1	NA				