


Engineer: <u>Levi Balling</u>	Title: Main Controller			U of U Computer Engineering 50 S Central Campus Dr. Rm. 3280 MEB SLC, UT 84112		Smart Home 	
Drawn By: <u>Levi Balling</u>	Size: <u>B</u>	Number: <u>1</u>	Revision: <u>2</u>	Project: Smart Home			
Checked By: <u>Dell Hanks</u>	File: MainController_SchDoc						
Date: <u>8/17/2012</u>	Website: <u>www.eng.utah.edu/~lbaling/SmartHome</u>						
Time: <u>11:30:02 PM</u>							
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Bill of Materials

Main Controller

Source Data From: MainControllerProject.PrjPCB
 Project: MainControllerProject.PrjPCB
 Variant: None

Creation Date: 8/17/2012 11:30:06 PM
 Print Date: 17-Aug-12 11:30:09 PM

Footprint	Comment	LibRef	Designator	Description	Quantity
RAD-0.3	Cap	Cap	C1, C2, C3, C4	Capacitor	4
DO-41	Diode 1N4001	Diode 1N4001	D1, D2	1 Amp General Purpose Rectifier	2
DSUB1.385-2H9	D Connector 9	D Connector 9	J1, J2, J3, J4, J5	Receptacle Assembly, 9 Position, Right Angle	5
HDR1X5	Header 5	Header 5	P1, P3, P7, P10, P13, P14, P16, P17	Header, 5-Pin	8
MainPowerC onconnector	Header 4	Header 4	P2	Header, 4-Pin	1
HDR1X7	Header 7	Header 7	P4	Header, 7-Pin	1
HDR1X6	Header 6	Header 6	P5	Header, 6-Pin	1
HDR1X2	Header 2	Header 2	P6, P15	Header, 2-Pin	2
HDR1X6	Garage_HVAC	Header 6	P8	Header, 6-Pin	1
HDR1X9	TempSenseBoar d1	Header 9	P9, P12	Header, 9-Pin	2
HDR1X3	Header 3	Header 3	P11	Header, 3-Pin	1
2N3904	2N3904	2N3904	Q1, Q2, Q3, Q4, Q5	NPN General Purpose Amplifier	5
AXIAL-0.3	Res1	Res1	R1, R2, R3, R4, R5	Resistor	5
Teensy2.0++ N014	Teensy2.0++ SN7416N	Teensy2.0++ SN7416N	Teensy1	The teensy 2.0 from www.PJRC.com	1
			U1, U2, U4	Hex Inverter Buffer / Driver with High-Voltage Outputs	3
NT024D	SN74HC154NT	SN74HC154NT	U3	4-Line to 16-Line Decoder / Demultiplexer	1
ISOWATT220 AB	L7805CP	L7805CP	VR1	Positive Voltage Regulator	1
ISOWATT220 AB	L7812CP	L7805CP	VR2	Positive Voltage Regulator	1

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Approved	Notes