

## ISM BAND FSK RECEIVER MODULE

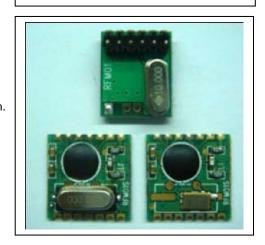
## RFM01

(the purpose of this spec covers mainly for the physical characteristic of the module, for register configure and its related command info please refer to RF01 data sheets)

### **General Introduction**

RFM01 is a low costing ISM band receiver module implemented with unique PLL and zero IF design approach. It works with FSK modulated signal ranges from 315/433/868/915MHZ bands, comply with FCC, ETSI regulation. The SPI interface is used to communicate with microcontroller for parameter setting. RFM01 works with RFM02 transmitter module. At 433MHZ band, the pair of module can work up to 300m in the free open air.

### RFM01



### Features:

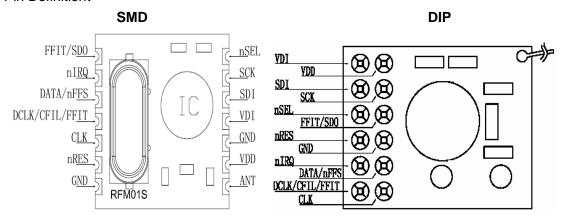
- · Low costing, high performance and price ratio
- Tuning free during production
- FSK reception
- PLL and zero IF technology
- Fast PLL lock time
- High resolution PLL with 2.5 KHz step
- High data rate (up to 115.2 kbps with internal demodulator, with external RC filter highest data rate is 256 kbps)
- · Differential antenna input
- Automatic antenna tuning
- Programmable receiver bandwidth (from 67 to 400 kHz)
- Analog and digital signal strength indicator (ARSSI/DRSSI)
- AFC
- DQD
- Internal demodulator
- SPI interface
- Clock and reset signal output for external MCU use
- 16 bits FIFO
- Low power mode (<0.5mA averaged current consumption)
- 10MHz crystal for PLL timing
- · Wakeup timer
- Low battery detection
- Programmable capacitor bank
- 2.2V 5.4V power supply
- Low power consumption
- Stand by current less than 0.3uA



## **Typical Application:**

- Remote control
- Remote sensor
- Wireless data collection
- Home security system
- Toys
- Tire pressure monitoring system

### Pin Definition:



definition	Туре	Function	
VDI	DO	Valid data indicator	
VDD	S	Positive power supply	
SDI	DI	SPI data input	
SCK	DI	SPI clock input	
nSEL	DI	Chip select (active low)	
FFIT/SDO	DO	FIFO fill interrupt(active low) or status read data output	
nRES	DO	Reset output (active low)	
GND	S	Power ground	
nIRQ	DO	Interrupts request output (active low)	
DATA/nFFS	DO/DI	Data input(non FIFO mode)/ FIFO select	
		Clock output (no FIFO )/ external filter capacitor(analog mode)/ FIFO	
DCLK/CFIL/FFIT	DO/AIO/DO	interrupts(active high)when FIFO level set to 1, FIFO empty	
		interruption can be achieved	
CLK	DO	Clock output for external microcontroller	

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## **Electrical Parameter:**

## Maximum (not at working mode)

	<u> </u>			
symbol	parameter	minimum	maximum	Unit
$V_{dd}$	Positive power supply	-0.5	6.0	V
V <sub>in</sub>	All pin input level	-0.5	Vdd+0.5	V
l <sub>in</sub>	Input current except power	-25	25	MA
ESD	Human body model		1000	V
T <sub>st</sub>	Storage temperature	-55	125	$^{\circ}$ C
T <sub>Id</sub>	Soldering temperature(10s)		260	$^{\circ}$ C

Recommended working range

symbol	parameter	minimum	maximum	Unit
$V_{dd}$	Positive power supply	2.2	5.4	V
T <sub>op</sub>	Working temperature	-40	85	$^{\circ}$

### **DC** characteristic

symbol	parameter	Remark	minimum	typical	maximum	Unit
I <sub>dd</sub>	Current consumption	315,433MHz band		9	11	mA
		868,915MHz band		10.5	12.5	
I <sub>x</sub>	Stand by current	Crystal and base band		3. 0	3. 5	mA
		on				
I <sub>pd</sub>	Sleep mode current	All blocks off		0.3		uA
I <sub>lb</sub>	Low battery detection			0.5		uA
V <sub>Ib</sub>	Low battery step	0.1V per step	2.2		5.3	V
V <sub>lba</sub>	Low battery detection			75		mV
	accuracy					
Vil	Low level input				0.3*V <sub>dd</sub>	V
V <sub>ih</sub>	High level input		0.7*V <sub>dd</sub>			V
I <sub>il</sub>	Leakage current	V <sub>il</sub> =0V	-1		1	uA
l <sub>ih</sub>	Leakage current	V <sub>ih</sub> =V <sub>dd</sub> ,V <sub>dd</sub> =5.4V	-1		1	uA
Vol	Low level output	I <sub>ol</sub> =2mA			0.4	V
V <sub>oh</sub>	High level output	I <sub>oh</sub> =-2mA	V <sub>dd</sub> -0.4			V

### **AC** characteristic

symbol	parameter	remark	min	typical	max	Unit
f <sub>ref</sub>	PLL frequency	Parallel fundamental	8	10	12	MHz
	frequency	315 MHz band,2.5KHz step	310.24		319.75	
f <sub>LO</sub>	(10MHz crystal used)	433 MHz band,2.5KHz step	430.24		439.75	MHz
		868 MHz band,5KHz step	860.48		879.51	
		915 MHz band,7.5KHz step	900.72		929.27	

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## RFM01

	frequency	315 MHz band,2.5KHz step	248.19		255.80	
$f_{LO}$	(8MHZ crystal used)	433 MHz band,2.5KHz step	344.19		351.80	MHz
		868 MHz band,5KHz step	688.38		703.61	
		915 MHz band,7.5KHz step	720.57		743.41	
	frequency	315 MHz band,2.5KHz step	372.28		383.71	
$f_{LO}$	(12MHZ crystal used)	433 MHz band,2.5KHz step	516.28		527.71	MHz
		868 MHz band,5KHz step	1032.5		1055.4	
		915 MHz band,7.5KHz step	1080.8		1115.1	
BW		1	60	67	75	
	Receiver bandwidth	2	120	134	150	
		3	180	200	225	kHz
		4	240	270	300	
		5	300	350	375	
		6	360	400	450	
t <sub>lock</sub>	PLL lock time	After 10MHz step hopping,		20		us
		frequency error <10 kHz				
T <sub>st, p</sub>	PLL start time	After crystal stabilized		250		us
BR	Data rate	With internal digital			115.2	kbps
		demodulator				
BRA	Data rate	With external RC filter			256	kbps
P <sub>min</sub>	sensitivity	BW=134KHz,BR=1.2kbps		-109	-100	dBm
AFC <sub>range</sub>	AFC working range	δ F <sub>fsk</sub> : received signal		0.8* δ F <sub>fsk</sub>		
		modulation depth				
RS <sub>A</sub>	RSSI accuracy			±5		dB
RS <sub>R</sub>	RSSI range			46		dB
C <sub>ARSSI</sub>	ARSSI filter			1		nF
RS <sub>STEP</sub>	RSSI programmable			6		dB
	step					
RS <sub>RESP</sub>	DRSSI response time	RSSI output high after valid,		500		us
		C <sub>ARRSI</sub> =5nF				
C <sub>XL</sub>	Capacitor bank	Programmable step with	8.5		16	pF
	Capacitor barne					
	Supusitor Surin	0.5pF step, +/- 10%				
T <sub>POR</sub>	PWR time			50	100	mS
T <sub>POR</sub>	·	0.5pF step, +/- 10%	0. 96	50	100	mS mS
T <sub>PBT</sub>	PWR time	0.5pF step, +/- 10% V <sub>dd</sub> reach 90%		50		
	PWR time Wakeup timer period Programmable	0.5pF step, +/- 10% V <sub>dd</sub> reach 90%	0. 96	50	1. 08	mS
T <sub>PBT</sub> T <sub>WAKE-UP</sub>	PWR time Wakeup timer period Programmable wakeup time	0.5pF step, +/- 10%  V <sub>dd</sub> reach 90%  Calibrated each 30s	0. 96	50	1. 08	mS
T <sub>PBT</sub>	PWR time Wakeup timer period Programmable	0.5pF step, +/- 10% V <sub>dd</sub> reach 90%	0. 96	50	1. 08 5*10E11	mS mS

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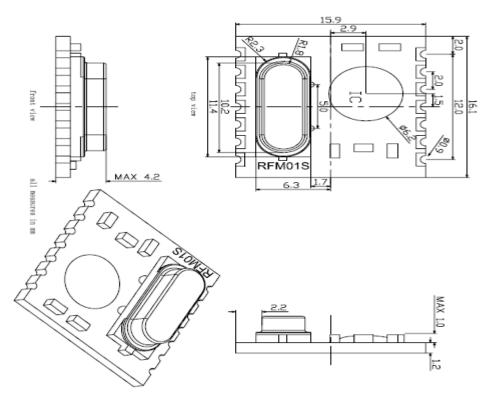
Field testing range

Band	Test condition	Distance
433MHz band	Receiver bandwidth =134KHz, data rate=1.2kbps, transmitter	
	modulation=60KHZ (matches with RF02B)	>300M
	In free open area	
868MHz band	Receiver bandwidth=134KHz,data rate =1.2kbps	
	Transmitter modulation=60KHZ (matches with RFM02B) in free	>200M
	open area	
915MHz band	Receiver bandwidth=134KHz,data rate =1.2kbps	
	Transmitter modulation=60KHZ (matches with RFM02B) in free	>200M
	open area	

## **Mechanical Dimension**

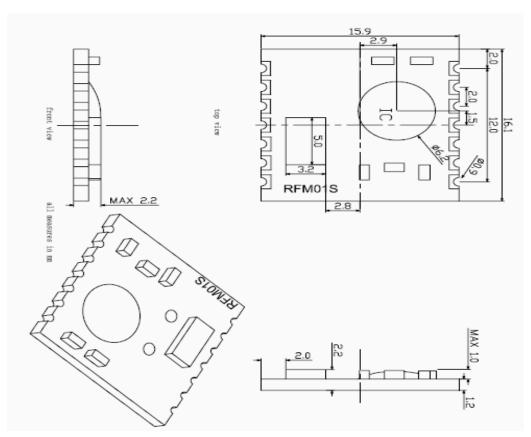
(units in mm)

### **SMD PACKAGE (S1)**

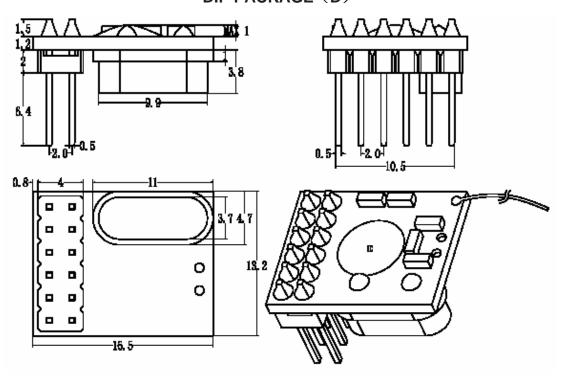


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## SMD PACKAGE (S2)



### **DIP PACKAGE (D)**

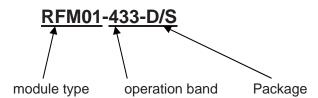


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## **Module Model Definition**

model=module-operation band



Note: SOP packages is divided into two kinds based on thickness: 1. thickness is 4.2mm, 2. thickness is 2.2mm

example: 1, RFM01 module at 433MHz band ,DIP: RFM01-433-D.

2, RFM01 module at 868MHZ band, SMD, thickness at 4.2mm: RFM01-868-S1.

## Marking difference:

(color marks the difference for frequency)

RFM01 receiver band	Color
433MHz band	Black
868MHz band	red
915MHz band	green

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