

HD74LS04 / HD74LS05

Hex Inverters / Hex Inverters (with Open Collector Outputs)

REJ03D0391-0300 Rev.3.00 Jul.13.2005

Features

• Ordering Information

• HD74LS04

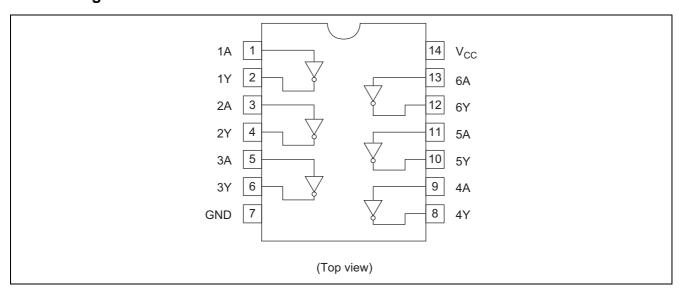
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS04P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS04FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74LS04RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

• HD74LS05

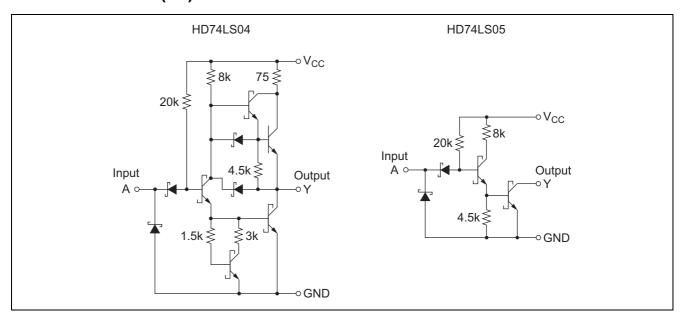
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS05P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS05FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74LS05RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



Circuit Schematic (1/6)



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V_{CC}^{Note}	7	V
Input voltage	V _{IN}	7	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

• HD74LS04

ltem	Symbol	Min	Тур	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output current	I _{OH}	_	_	-400	μΑ
Output current	I _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

• HD74LS05

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	Vcc	4.75	5.00	5.25	V
Output voltage	V _{OH}	_	_	5.5	V
Output current	I _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

• HD74LS04

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V _{IH}	2.0	_	_	V	
Input voltage	V _{IL}	_	_	0.8	V	
	V _{OH}	2.7			V	$V_{CC} = 4.75 \text{ V}, V_{IL} = 0.8 \text{ V}, I_{OH} = -400 \mu\text{A}$
Output voltage	V _{OL}	_		0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}$
	VOL	_		0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.73 \text{ V}, \text{ VIH} = 2 \text{ V}$
	I _{IH}	_		20	μΑ	$V_{CC} = 5.25 \text{ V}, V_I = 2.7 \text{ V}$
Input current	I _{IL}	—		-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	II	_		0.1	mA	$V_{CC} = 5.25 \text{ V}, V_I = 7 \text{ V}$
Short-circuit output current	I _{OS}	-20		-100	mA	V _{CC} = 5.25 V
Supply current	I _{CCH}	_	1.2	2.4	mA	V _{CC} = 5.25 V
Зарріу сапені	I _{CCL}	_	3.6	6.6	mA	V _{CC} = 5.25 V
Input clamp voltage	V_{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Note: $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$

• HD74LS05

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V _{IH}	2.0	_	_	V	
input voitage	V_{IL}	_	_	0.8	V	
Output voltage	V _{OL}	_		0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}$
Output voltage	VOL	_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.73 \text{ V}, \text{ VIH} = 2 \text{ V}$
Output current	l _{OH}	_	_	100	μΑ	$V_{CC} = 4.75 \text{ V}, V_{IL} = 0.8 \text{ V}, V_{OA} = 5.5 \text{ V}$
	I _{IH}	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
Input current	I _{IL}	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 0.4 \text{ V}$
	I _I	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 7 \text{ V}$
Supply current	I _{CCH}	_	1.2	2.4	mA	V _{CC} = 5.25 V
Supply culterit	I _{CCL}	_	3.6	6.6	mA	V _{CC} = 5.25 V
Input clamp voltage	V_{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Note: $^*V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C}$

Switching Characteristics

• HD74LS04

 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$

Item	Symbol	min.	typ.	max.	Unit	Condition
Dropogation dolay time	t _{PLH}	_	9	15	ns	C - 15 pE B - 2 kO
Propagation delay time	t _{PHL}	_	10	15	ns	$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$

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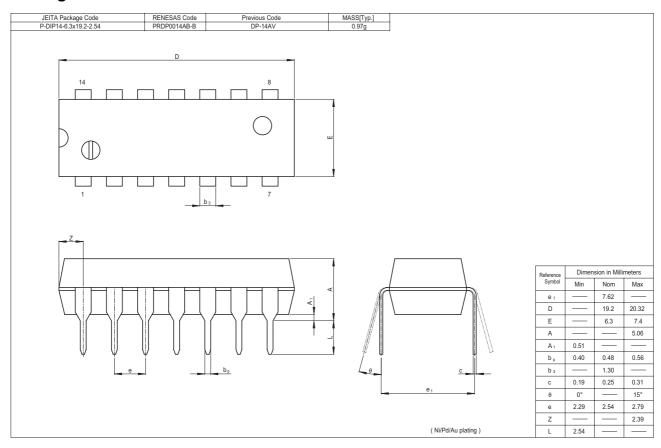
 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$

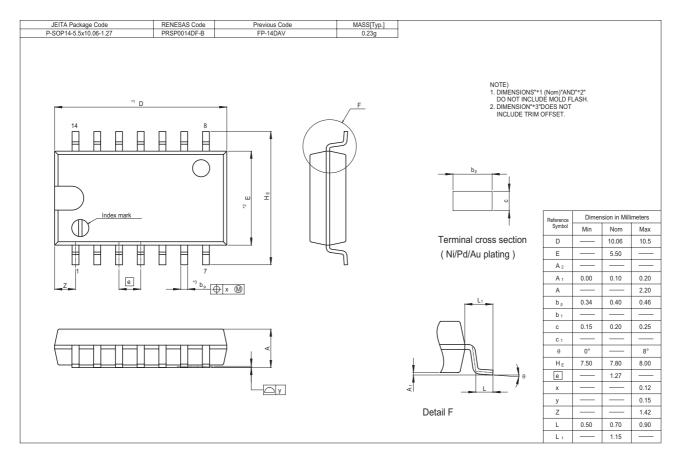
Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}	_	17	32	ns	C 15 pE B 2 kO
	t _{PHL}	_	15	28	ns	$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$

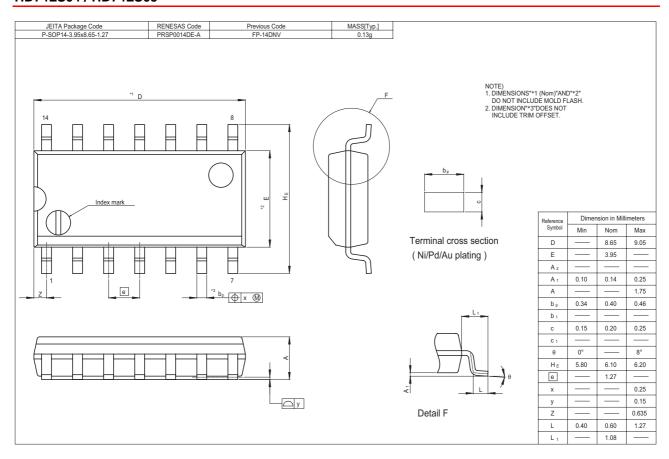
Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".



Package Dimensions







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