



Table 6. I III Delli III oli

Name	No.	Type ¹	Function
3V3	1	Р	Power supply
			High: on, enables the chip.
EN	2	- 1	Low: off, the chip powers off.
			Note: Do not leave the EN pin floating.
IO4	3	I/O/T	GPIO4, ADC1_CH4, FSPIHD, MTMS
IO5	4	I/O/T	GPIO5, ADC2_CH0, FSPIWP, MTDI
IO6	5	I/O/T	GPIO6, FSPICLK, MTCK

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Pin Definitions

Table 3 - cont'd from previous page

Name	No.	Type ¹	Function
107	6	I/O/T	GPIO7, FSPID, MTDO
IO8	7	I/O/T	GPIO8
109	8	I/O/T	GPIO9
GND	9,19	Р	Ground
IO10	10	I/O/T	GPIO10, FSPICS0
RXD	11	I/O/T	GPIO20, U0RXD
TXD	12	I/O/T	GPIO21, U0TXD
IO18	13	I/O/T	GPIO18, USB_D-
IO19	14	I/O/T	GPIO19, USB_D+
IO3	15	I/O/T	GPIO3, ADC1_CH3
IO2	16	I/O/T	GPIO2, ADC1_CH2, FSPIQ
IO1	17	I/O/T	GPIO1, ADC1_CH1, XTAL_32K_N
IO0	18	I/O/T	GPIO0, ADC1_CH0, XTAL_32K_P

P: power supply; I: input; O: output; T: high impedance.