# CQR40平台 核心板接口定义

1   PE10   GPIO   TSO_D7   CSIO_D7		I	I	I	T			
3   PE9   GPIO   TSO_D5   CSIO_D5	1	PE11	GPIO	TS0_D7	CSI0_D7			
4   PE8   GPIO   TSO_D4   CSIO_D4	2	PE10	GPIO	TS0_D6	CSI0_D6			
5   PE7   GPIO   TSO_D3   CSIO_D3	3	PE9	GPIO	TS0_D5	CSI0_D5			
6   PE6   GPIO   TS0_D2   CSI0_D2	4	PE8	GPIO	TS0_D4	CSI0_D4			
7   PE5   GPIO   TSO_D1   CSIO_D1   SMC_VPPN      8   PE4   GPIO   TSO_DO   CSIO_DO       9   PE3   GPIO   TSO_DVLD   CSIO_VSYNC       10   PE2   GPIO   TSO_SYNC   CSIO_HSYNC       11   PE0   GPIO   TSO_CLK   CSIO_HSYNC       12   PE1   GPIO   TSO_CLK   CSIO_HCLK       13   PH4   GPIO   LCD1_D4    UART4_TX   EINT4   CSI1_D4     14   PH3   GPIO   LCD1_D3    UART4_RX   EINT5   CSI1_D5     15   PH5   GPIO   TWI2_SCK   PWM4       17           18   PB21   GPIO   TWI2_SDA   PWM5        19   PB23	5	PE7	GPIO	TS0_D3	CSI0_D3			
8   PE4   GPIO   TS0_D0   CSI0_D0	6	PE6	GPIO	TS0_D2	CSI0_D2			
9   PE3   GPIO   TS0_DVLD   CSI0_VSYNC	7	PE5	GPIO	TS0_D1	CSI0_D1	SMC_VPPN		
10   PE2   GPIO   TS0_SYNC   CSI0_HSYNC      11   PE0   GPIO   TS0_CLK   CSI0-PCLK      12   PE1   GPIO   TS0_ERR   CSI0_MCLK      13   PH4   GPIO   LCD1_D4   UART4_TX   EINT4   CSI1_D4     14   PH3   GPIO   LCD1_D3   UART3_CTS   EINT3   CSI1_D3     15   PH5   GPIO   LCD1_D5   UART4_RX   EINT5   CSI1_D5     16   PB20   GPIO   TWI2_SCK   PWM4       17     PWM5        18   PB21   GPIO   TWI2_SDA   PWM5        19   PB23   GPIO   UART0_RX   CIR1_RX	8	PE4	GPIO	TS0_D0	CSI0_D0			
11   PE0   GPIO   TS0_CLK   CSI0-PCLK  <	9	PE3	GPIO	TS0_DVLD	CSI0_VSYNC			
12   PE1   GPIO   TS0_ERR   CSI0_MCLK   UART4_TX   EINT4   CSI1_D4     13   PH4   GPIO   LCD1_D4   UART4_TX   EINT4   CSI1_D4     14   PH3   GPIO   LCD1_D3   UART3_CTS   EINT3   CSI1_D3     15   PH5   GPIO   LCD1_D5   UART4_RX   EINT5   CSI1_D5     16   PB20   GPIO   TWI2_SCK   PWM4   PWM4   Image: PWM5   Image: PWM5 <td< td=""><td>10</td><td>PE2</td><td>GPIO</td><td>TS0_SYNC</td><td>CSI0_HSYNC</td><td></td><td></td><td></td></td<>	10	PE2	GPIO	TS0_SYNC	CSI0_HSYNC			
13   PH4   GPIO   LCD1_D4   UART4_TX   EINT4   CSI1_D4     14   PH3   GPIO   LCD1_D3   UART3_CTS   EINT3   CSI1_D3     15   PH5   GPIO   LCD1_D5   UART4_RX   EINT5   CSI1_D5     16   PB20   GPIO   TWI2_SCK   PWM4	11	PE0	GPIO	TS0_CLK	CSI0-PCLK			
14   PH3   GPIO   LCD1_D3   UART3_CTS   EINT3   CSI1_D3     15   PH5   GPIO   LCD1_D5   UART4_RX   EINT5   CSI1_D5     16   PB20   GPIO   TWI2_SCK   PWM4	12	PE1	GPIO	TS0_ERR	CSI0_MCLK			
15   PH5   GPIO   LCD1_D5   UART4_RX   EINT5   CSI1_D5     16   PB20   GPIO   TWI2_SCK   PWM4	13	PH4	GPIO	LCD1_D4		UART4_TX	EINT4	CSI1_D4
16 PB20 GPIO TWI2_SCK PWM4   17      18 PB21 GPIO TWI2_SDA PWM5   19 PB23 GPIO UART0_RX CIR1_RX	14	PH3	GPIO	LCD1_D3		UART3_CTS	EINT3	CSI1_D3
17	15	PH5	GPIO	LCD1_D5		UART4_RX	EINT5	CSI1_D5
18   PB21   GPIO   TWI2_SDA   PWM5     19   PB23   GPIO   UART0_RX   CIR1_RX	16	PB20	GPIO	TWI2_SCK		PWM4		
19 PB23 GPIO UARTO_RX CIR1_RX	17							
	18	PB21	GPIO	TWI2_SDA		PWM5		
20	19	PB23	GPIO	UARTO_RX	CIR1_RX			
	20							

21	PG6	GPIO	TS1_D2	CSI1_D2	UART3_TX	CSI0_D10		
22	PB22	GPIO	UART0_TX					
23	PG9	GPIO	TS1_D5	CSI1-D5	UART3_CTS	CSI0_D13	BIST_RESULT0	
24	PG7	GPIO	TS1_D3	CSI1-D3	UART3_RX	CSI0_D11		
25	PG10	GPIO	TS1_D6	CSI1-D6	UART4_TX	CSI0_D14	BIST_RESULT1	
26	PG8	GPIO	TS1_D4	CSI1-D4	UART3_RTS	CSI0_D12		
27	PI10	GPIO	SPI0_CS0	UART5_TX			EINT22	
28	PG11	GPIO	TS1_D7	CSI1-D7	UART4_RX	CSI0_D15		
29	PI20	GPIO	PS2_SCK0	UART7_TX			PWM2	
30	PI11	GPIO	SPI0_CLK	UART5_RX			EINT23	
31	PF4	GPIO	SDC0_D3		UART0_RX			
32	PI21	GPIO	PS2_SDA0	UART7_RX			PWM3	
33	PF0	GPIO	SDC0_D1		JTAG_MS1			
34	PF5	GPIO	SDC0_D2		JTAG_CK1			
35	PF2	GPIO	SDC0_CLK		UART0_TX			
36	PF1	GPIO	SDC0_D0		JTAG_DI1			
37	PH15	GPIO	LCD1_D15	ETXD2	KP_IN5	SMC_VPPPP	EINT15	CSI1_D15
38	PF3	GPIO	SDC0_CMD		JTAG_DO1			
39	GND							
40	GND							

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41	TVOUT1	TVOUT1						
42	TVOUT0	TVOUT0						
43	TVOUT3	TVOUT3						
44	TVOUT2	TVOUT2						
45	LINEINL	LINEINL						
46	LINEINR	LINEINR						
47	PHOUTN							
48	PHOUTP							
49	TVIN1	TVIN1						
50	TVIN0	TVIN0						
51	TVIN3	TVIN3						
52	TVIN2	TVIN2						
53	MICIN2	MICIN2						
54	MICIN1	MICIN1						
55	HPOUTR	HPOUTR						
56	HPOUTL	HPOUTL						
57	HPCOMFB	HPCOMFB						
58	VMIC	VMIC						
59	PH11	GPIO	LCD1_D11	ERXD0	KP_IN3		EINT11	CSI1_D11
60	PB4	GPIO	CIR0_RX					
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61	PB17	GPIO	SPI2_MISO	JIAG_DI0			
62	PB16	GPIO	SPI2_MOSI	JIAG_DO0			
63	PB15	GPIO	SPI2_CLK	JIAG_CK0			
64	PB14	GPIO	SPI2_CS0	JIAG_MS0			
65	PH6	GPIO	LCD1_D6		UART5_TX	EINT6	CSI1_D6
66	PH7	GPIO	LCD1_D7		UART5_RX	EINT7	CSI1_D7

1	HTX2P	HTX2P					
2	HTX2N	HTX2N					
3	HTXIP	HTXIP					
4	HTX1N	HTX1N					
5	HTX0P	HTX0P					
6	HTX0N	HTX0N					
7	НТХСР	НТХСР					
8	HTXCN	HTXCN					
9	HSCL	HSCL					
10	HSDA	HSDA					
11	HCEC	HCEC					
12	HHPD	HHPD					
13	GND						
14	GND						
15	PB8	GPIO	I2S_D00	AC97_DO			
16	PB7	GPIO	I2S_LRCK	AC97_SYNC			
17	PB6	GPIO	I2S_BCLK	AC97_BCLK			
18	PB12	GPIO	<sub>12S_DL</sub> 12S_	AC97_ AC97_DL			
19	PH8	GPIO	LCD1_D8	ERXD3	KP_IN0	EINT8	CSI1_D8
20	PB13	GPIO	SPI2_CS1		OWA_DO		

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21	PI19	GPIO	SPI1_MISO	UART2_RX			EINT31	
22	PH13	GPIO	LCD1_D13		PS2_SDA1	SMC_RST	EINT13	CSI1_D13
23	PI18	GPIO	SPI1_MOSI	UART2_TX			EINT30	
24	PI16	GPIO	SPI1_CS0	UART2_RTS			EINT28	
25	PI17	GPIO	SPI1_CLK	UART2_CTS			EINT29	
26	PG4	GPIO	TS1_D0	CSI1_D0	SDC1_D2	CSI0_D8		
27	PG5	GPIO	TS1_D1	CSI1_D1	SDC1_D3	CSI0_D9		
28	PG2	GPIO	TS1_SYNC	CSI1_HSYNC	SDC1_D0			
29	PG3	GPIO	TS1_DVLD	CSI1_VSYNC	SDC1_D1			
30	PG0	GPIO	TS1_CLK	CSI1_PCLK	SDC1_CMD			
31	PG1	GPIO	TS1_ERR	CSI1_MCLK	SDC1_CLK			
32	PI12	PI12	SPI0_MOSI	UART6_TX	CLK_OUT_A		EINT24	
33	PH17	GPIO	LCD1_D17	ETXD0	KP_IN7	SMC_VCCEN	EINT17	CSI1_D17
34	GND							
35	GND							
36	REXT-SATA	REXT-SATA						
37	SATA-TXP	SATA-TXP						
38	SATA-TXM	SATA-TXM						
39	SATA-RXP	SATA-RXP						
40	SATA-RXM	SATA-RXM						

41	SATA-CLKP	SATA-CLKP			
42	SATA-CLKM	SATA-CLKM			

1	PD0	GPIO	LCD0_D0	LVDS0_VP0		
2	PD1	GPIO	LCD0_D1	LVDS0_VN0		
3	PD2	GPIO	LCD0_D2	LVDS0_VP1		
4	PD3	GPIO	LCD0_D3			
<del>'1</del>	רטא	GPIO	LCD0_D3	LVDS0_VN1		
5	PD4	GPIO	LCD0_D4	LVDS0_VP2		
6	PD5	GPIO	LCD0_D5	LVDS0_VN2		
7	PD6	GPIO	LCD0_D6	LVDS0_VPC		
8	PD7	GPIO	LCD0_D7	LVDS0_VNC		
9	PD8	GPIO	LCD0_D8	LVDS0_VP3		
10	PD9	GPIO	LCD0_D9	LVDS0_VN3		
11	PD10	GPIO	LCD0_D10	LVDS1_VP0		
12	PD11	GPIO	LCD0_D11	LVDS1_VN0		
13	PD12	GPIO	LCD0_D12	LVDS1_VP1		
14	PD13	GPIO	LCD0_D13	LVDS1_VN1		
15	PD14	GPIO	LCD0_D14	LVDS1_VP2		
16	PD15	GPIO	LCD0_D15	LVDS1_VN2		
17	PD16	GPIO	LCD0_D16	LVDS1_VPC		
18	PD17	GPIO	LCD0_D17	LVDS1_VNC		
19	PD18	GPIO	LCD0_D18	LVDS1_VP3		
20	PD19	GPIO	LCD0_D19	LVDS1_VN3		

21	PD20	GPIO	LCD0_D20	CSI1_MCLK			
22	PD21	GPIO	LCD0_D21	SMC_VPPEN			
23	PD22	GPIO	LCD0_D22	SMC_VPPPP			
24	PD23	GPIO	LCD0_D23	SMC_DET			
25	PD27	GPIO	LCD0-VSYNC	SMC_SDA			
26	PD26	GPIO	LCD0-HSYNC	SMC_SLK			
27	PD24	GPIO	LCD0-CLK	SMC_VCCE N			
28	PD25	GPIO	LCD0-DE	SMC_RST			
29	PB10	GPIO	I2S_DO2		PWM7		
30	PB9	GPIO	I2S_DO1		PWM6		
31	GND						
32	GND						
33	PB2	GPIO		PWM0			
34	PB18	GPIO	TWI1_SCK				
35	PD22	GPIO	LCD0_D22	SMC_VPPPP			
36	PB19	GPIO	TWI1_SDA				
37							
38	PB21	GPIO	TWI2_SDA		PWM5		
39	TPX1	TPX1					
40	TPY1	TPY1					

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41	TPX2	TPX2					
42	TPY2	TPY2					
43	KEYADC0	KEYADC 0					
44	KEYADC1	KEYADC 1					
45							
46	FEL	FEL					
47	PI1	GPIO	TWI3_SDA				
48							
49							
50	PI0	GPIO	TWI3_SCK				
51	POWERON						
52	AP-RESET#						
53	VCC-RTC	VCC-RTC					
54	AVCC						
55	PS						
56	PS						
57							
58							
59	GND						
60	GND					_	

61	ACIN				
62	ACIN				
63	ACIN				
64	ACIN				
65	VBAT				
66	VBAT				

1	PA16	GPIO	ECOL		UART1_DCD	GCLKIN/ECOL	I2S1_DO
2	PA13	GPIO	ETXEN	UART6_RX	UART1_CTS	GTXCTL/ETXEN	
3	PA4	GPIO	ETXD3	SPI1_CS1		GTXD3	
4	PA5	GPIO	ETXD2	SPI3_CS0		GTXD2	
5	PA6	GPIO	ETXD1	SPI3_CLK		GTXD1	
6	PA7	GPIO	ETXD0	SPI3_MOSI		GTXD0	
7	PA14	GPIO	ETXCK	UART7_TX	UART1_DTR	GNULL/ETXCK	I2S1_BCLK
8	PA8	GPIO	ERXCK	SPI3_MISO		GRXCK	
9	PA0	GPIO	ERXD3	SPI1_CS0	UART2_RTS	GRXD3	
10	PA1	GPIO	ERXD2	SPI1_CLK	UART2_CTS	GRXD2	
11	PA2	GPIO	ERXD1	SPI1_MOSI	UART2_TX	GRXD1	
12	PA3	GPIO	ERXD0	SPI1_ISO	UART2_RX	GRXD0	
13	PA10	GPIO	ERXDV		UART1_TX	GRXCTL/RXDV	
14	PA15	GPIO	ECRS	UART7_RX	UART1_DSR	GTXCK/ECRS	I2S1_LRCK
15	PA9	GPIO	ERXERR	SPI3_CS1		GNULL/ERXERR	I2S1_MCLK
16	PA17	GPIO	ETXERR		UART1_RING	GNULL/ETXERR	I2S1_DI
17	PA11	GPIO	EMDC		UART1_RX	GMDC	
18	PA12	GPIO	EMDIO	UART6_TX	UART1_RTS	GMDIO	
19	PD21	GPIO	LCD0_D21	SMC_VPPEN			
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21	GND						
22	GND						
23	USB0-DM	USB0-DM					
24	USB-DP	USB-DP					
25	PH9	GPIO	LCD1_D9	ERXD2	KP_IN1	EINT9	CSI1_D9
26	PH10	GPIO	LCD1_D10	ERXD1	KP_IN2	EINT10	CSI1_D10
27	PH0	GPIO	LCD1_D0		UART3_TX	EINT0	CSI1_D0
28	USBVBUS						
29	GND						
30	GND						
31	USB1-DM	USB1-DM					
32	USB1-DP	USB1-DP					
33	PH1	GPIO	LCD1_D1		UART3_RX	EINT1	CSI1_D1
34	GND						
35	GND						
36	PH2	GPIO	LCD1_D2		UART3_RTS	EINT2	CSI1_D2
37	USB2-DM	USB2-DM					
38	USB2-DP	USB2-DP					
39	GND						
40	GND						

41	VCC-3V				
42	VCC-3V				