

Application Note

# Pin Mapping for Murata Type-1VD Wi-Fi Module

For Murata SP-ZZ1HD\_B



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### Revision History

Revision	Date	Author	Change Description
1	2017/Nov/30	Gokul R.	Initial document



# 1 Introduction

This document describes the pin mapping for the Ayla features of the Murata Type-1VD

## 1.1 Intended Audience

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This document is intended for programmers and hardware engineers that need to connect the Murata Type-1VD module to other hardware components. This document contains descriptions of the Ayla signals present on each of the Type-1VD pins.

## 1.2 Related Documentation

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Setup for Murata Type 1VD Module (*AY006UVD*)

## 2 Signal Pin-outs

Ayla Signal Name	Module Pin Name	Module Pin	Module Connector Pin	I/O	Description
SPI_SS	PB12	11	23	I/O	SPI Slave Select
SPI_SCK	PB13	13	25	I/O	SPI CLock
SPI_MOSI	PA1	14	26	I/O	SPI Master Out Slave In
SPI_MISO	PA11	12	24	I/O	SPI Master In Slave Out
MCU_UART_TX	PA15	30	41	O	MCU UART Transmit
MCU_UART_RX	PA10	24	36	I	MCU UART Receive
MCU_UART_CTSn	PA11	12	24	I	MCU UART CTS
MCU_UART_RTSn	PA12	25	37	O	MCU UART RTS
READY_N	PA4	7	19	I	Module Initialized
INTR_N	PC13	33	44	O	Interrupt Output
RESET_N	nRESET	6	18	I	Module Reset
LINK_N		-			
WKUP		-			
I2C_SCL	PB8				
I2C_SDA	PB9				

### 3 Additional Pins

Module Pin Name	Module Pin	Module Connector Pin	Description
VDD_WLAN	46	59	3V DC Supply for Module
VDD_IO_MCU	19	31	3V DC Supply for Module
GND	1, 9, 10, 18, 20, 21, 29, 45, 47, 74-81, 83, 84, 86, 87, 92-94, 99- 147	5, 6, 12, 13, 21, 22, 29, 30, 32, 33, 45, 46, 57, 58, 61-64	

## 4 Programming Pins

The pins required to program, configure and update the firmware are listed below.

Ayla Signal Name	Module Pin Name	Module Pin
CONSOLE_UART_TX	PA2	4
CONSOLE_UART_RX	PA3	3
SWDIO	PA13	26
SWCLK	PA14	31





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