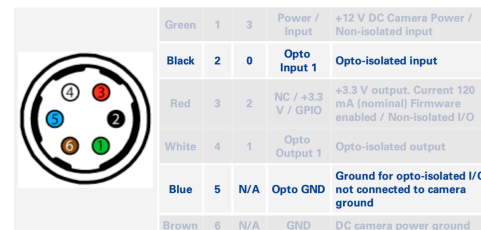
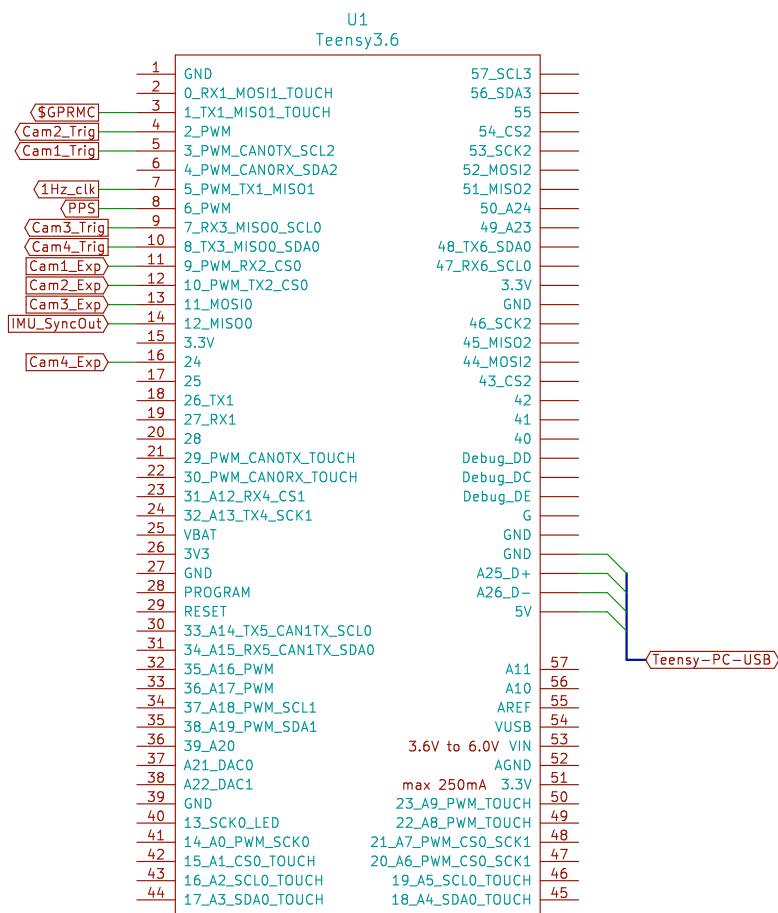


Pins 3 and 4 use PWM Timer FTM1 so changes to that timer affect both pins.

FrequencyTimer2 uses pin 5 and can output a very low jitter signal based on the PWM Timer. Unfortunately it's 50% duty cycle and we need a shorter pulse so we're using pin 6

[illegible]

Sheet: /
File: Handle.sch

Title: Handle Pinout

Size: A4	Date: 5-Aug-21
----------	----------------

KiCad E.D.A.	kicad (5.1.5-0)
--------------	-----------------

Rev: B

Id: 1/1