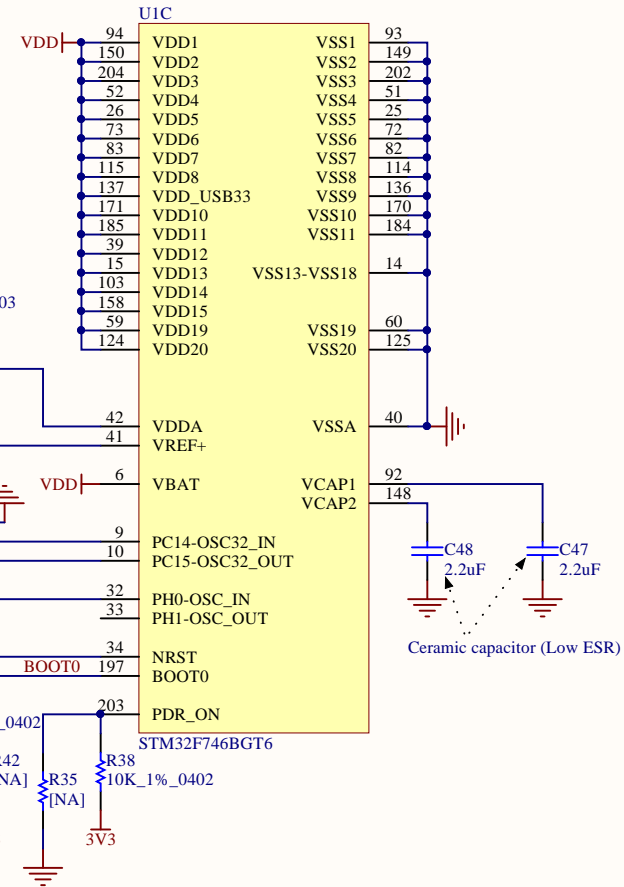
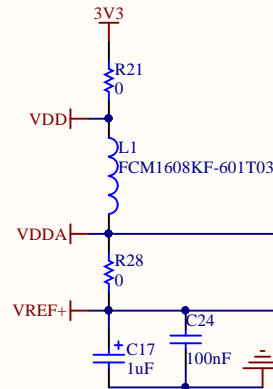
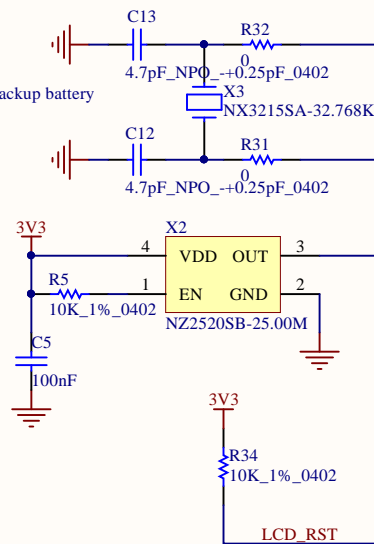


if using RTC, add backup battery

replace with 16Mhz TCXO



Ceramic capacitor (Low ESR)

LCD_RST

mcHF Pro QRP Transceiver Logic board

STM32 power and clock

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File: C:\Projects\mcHF Pro\pcb\logic\Schematics\logic_cpu_power_and_clocks.SchDoc

A

A

B

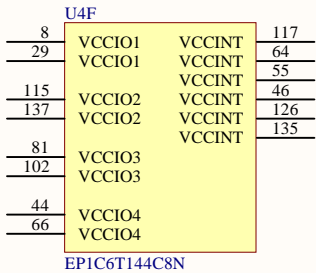
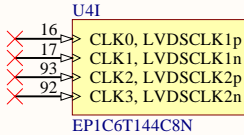
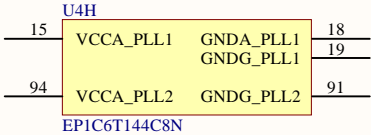
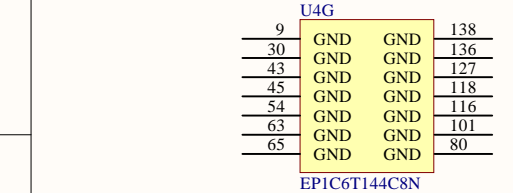
B

C

C

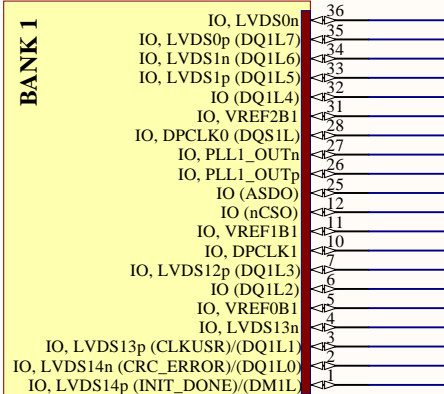
D

D



U4A

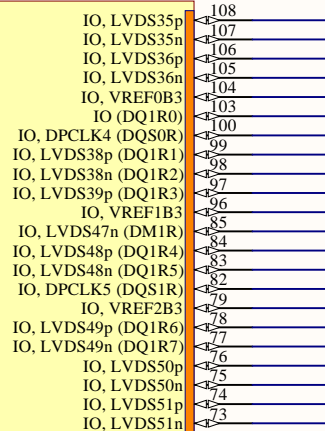
BANK 1



EPIC6T144C8N

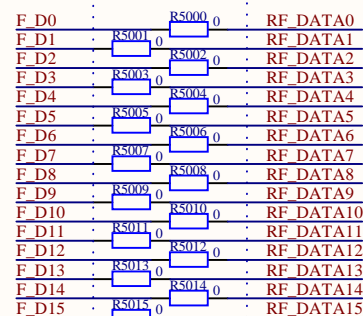
U4C

BANK 3

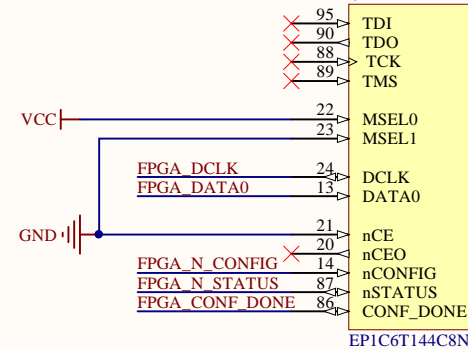


EPIC6T144C8N

Alpha proto only



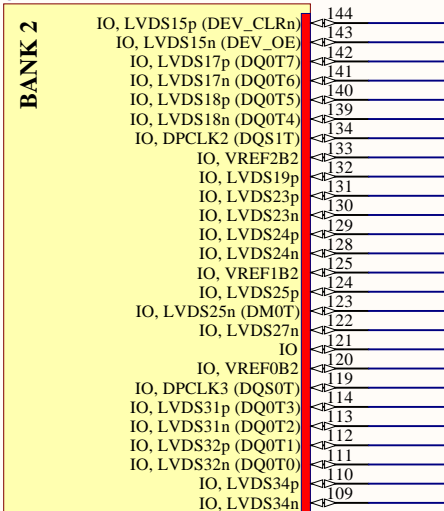
U4E



EPIC6T144C8N

U4B

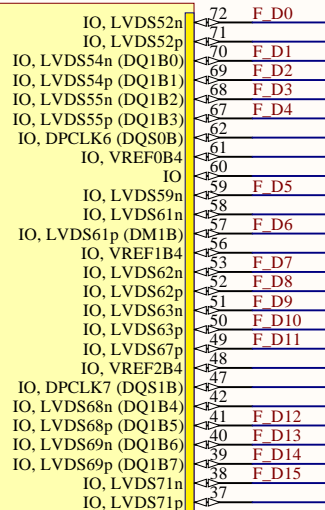
BANK 2



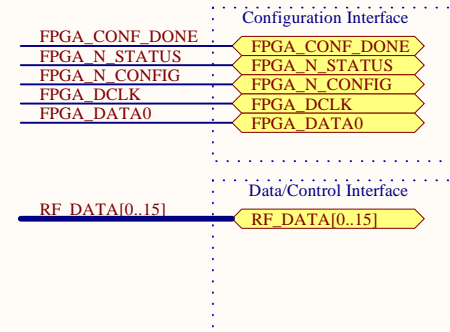
EPIC6T144C8N

U4D

BANK 4



EPIC6T144C8N



mcHF Pro QRP Transceiver Logic board

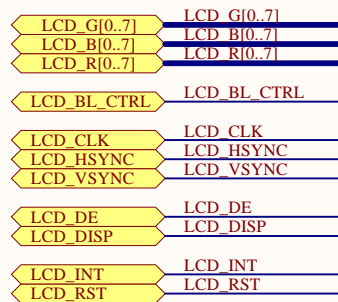
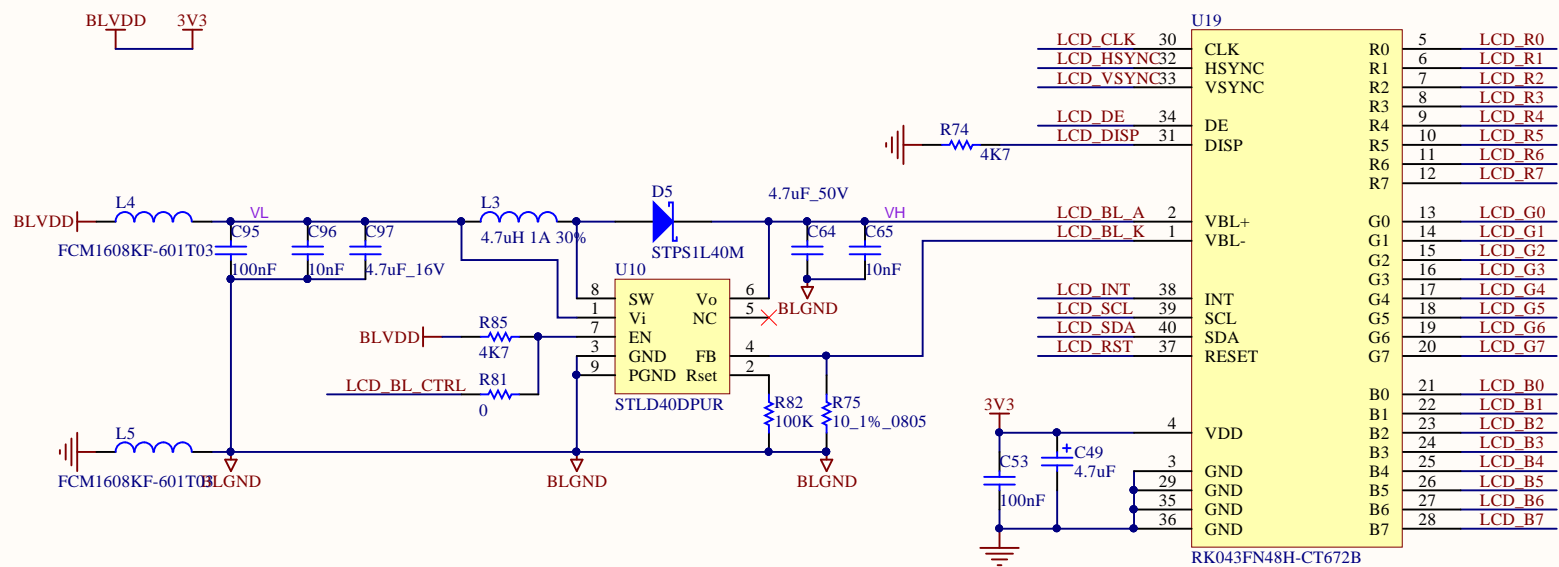
FPGA Data and Control

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File: C:\Projects\mcHF Pro\pcb\logic\Schematics\logic_fpga_signal.SchDoc

need complete rework for 5 inch LCD



ToDo: Draw footprint

mcHF Pro QRP Transceiver Logic board

LCD

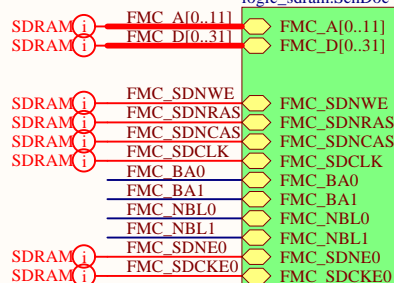
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File: C:\Projects\mcHF Pro\pcb\logic\Schematics\logic_lcd.SchDoc

Matched Net Lengths [Tolerance = 100mil]SDRAM

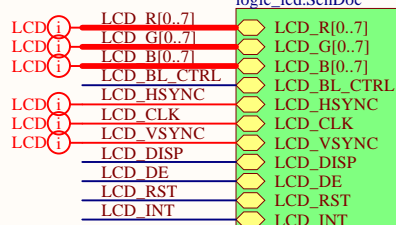
logic_sdram.SchDoc



Matched Net Lengths [Tolerance = 100mil]

U_LCD-43

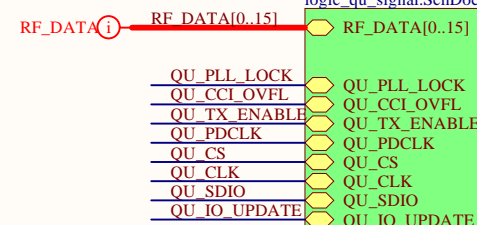
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Matched Net Lengths [Tolerance = 100mil]

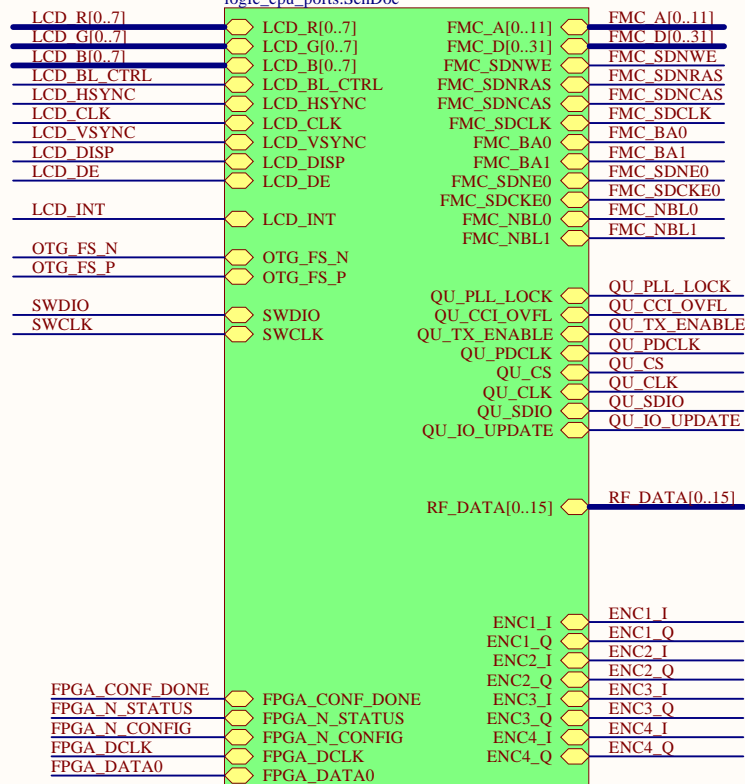
Quadrature upconverter data

logic_qu_signal.SchDoc



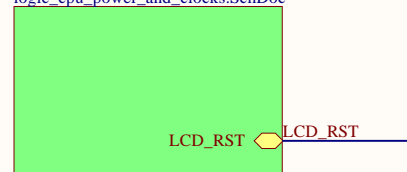
STM32F7 ports

logic_cpu_ports.SchDoc



STM32F7 power

logic_cpu_power_and_clocks.SchDoc



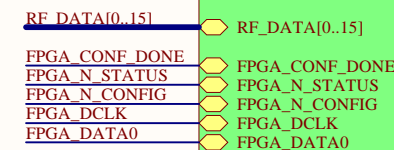
Quadrature upconverter power

logic_qu_power.SchDoc



FPGA

logic_fpga_signal.SchDoc

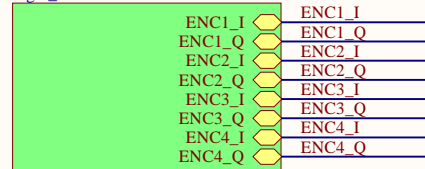


FPGA

logic_fpga_power.SchDoc

User Input

logic_ui.SchDoc

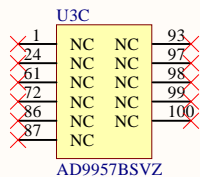
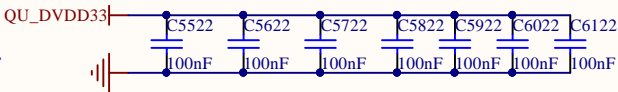
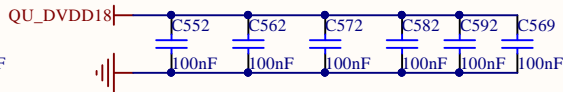
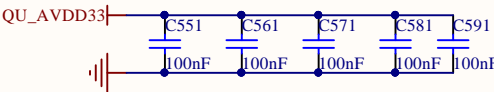
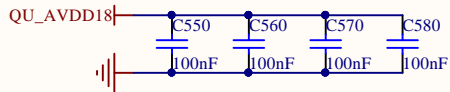
**mcHF Pro QRP Transceiver Logic board**

Modules interconnect

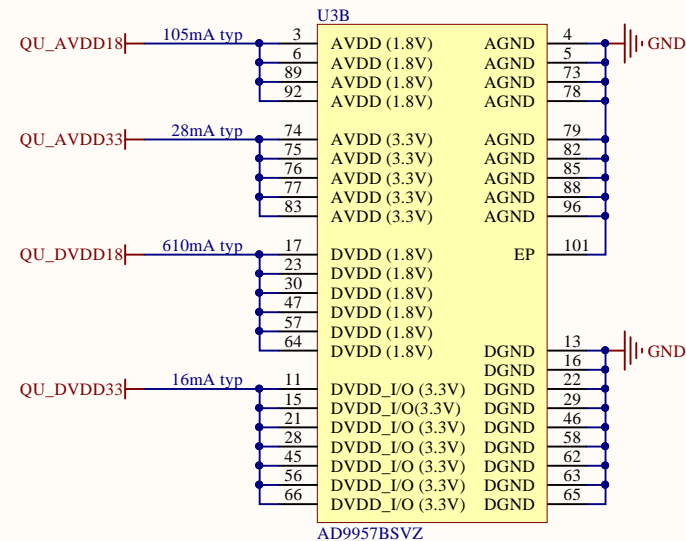
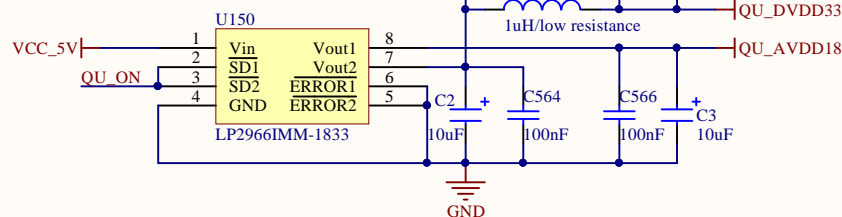
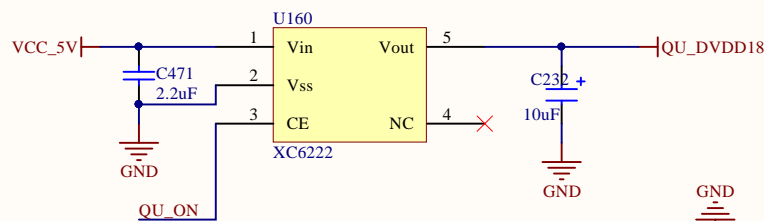
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File: C:\Projects\mcHF Pro\pcb\logic\Schematics\logic_main.SchDoc



maybe use switching regulators ?



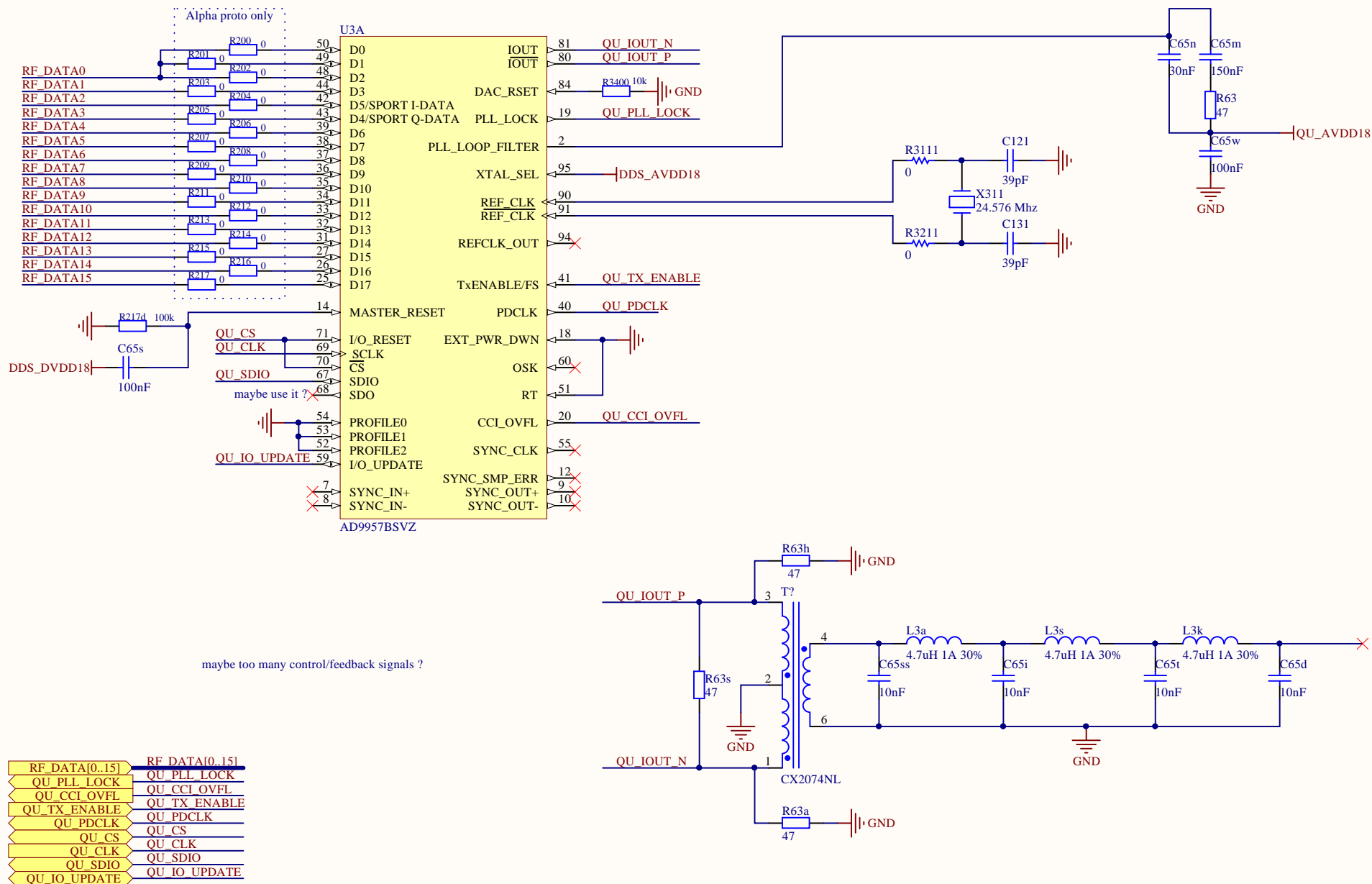
mcHF Pro QRP Transceiver Logic board

**Quadrature Upconverter
Power**

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File: C:\Projects\mcHF Pro\pcb\logic\Schematics\logic_qu_power.SchDoc



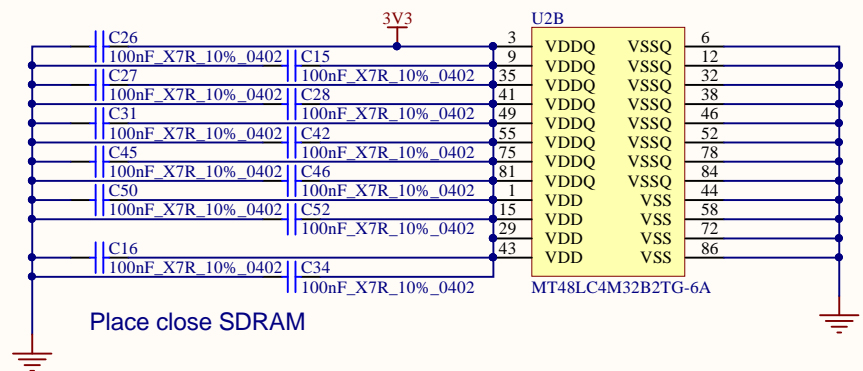
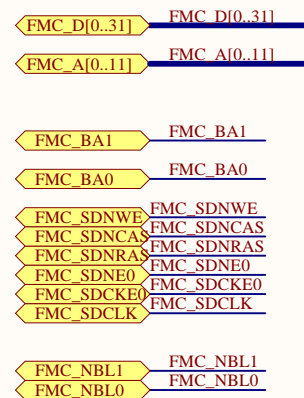
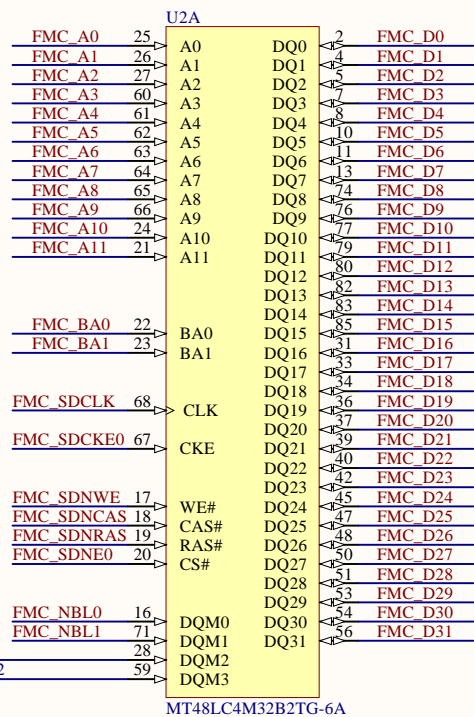
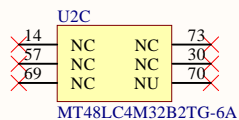
mcHF Pro QRP Transceiver Logic board

Quadrature Upconverter
Data and Output

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File: C:\Projects\mcHF Pro\pcb\logic\Schematics\logic_qu_signal.SchDoc



mcHF Pro QRP Transceiver Logic board

SDRAM

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File: C:\Projects\mcHF Pro\pcb\logic\Schematics\logic_sdram.SchDoc

