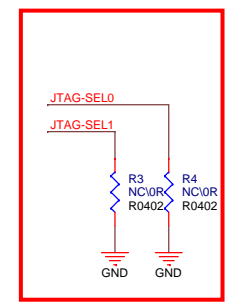
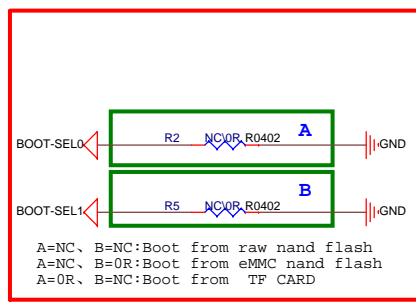
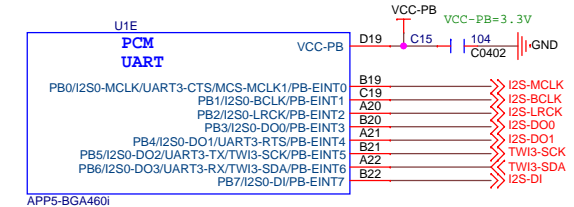
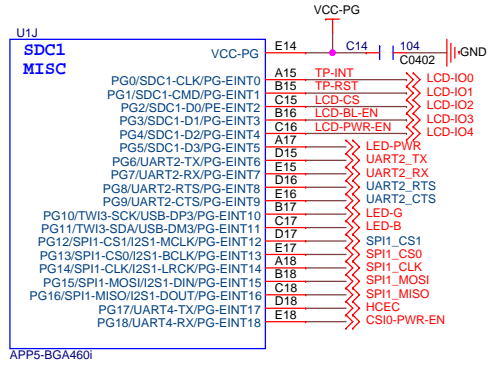
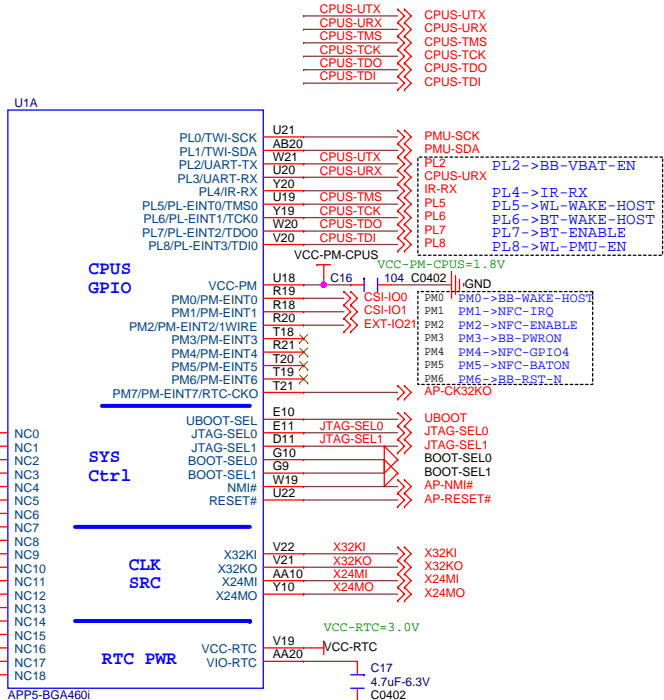
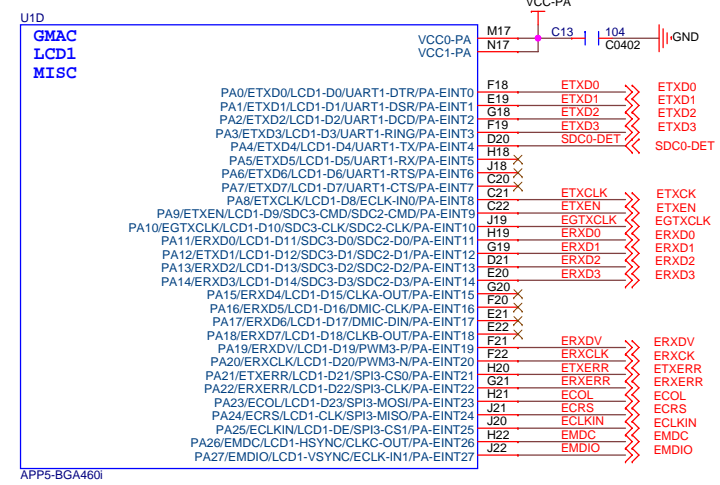
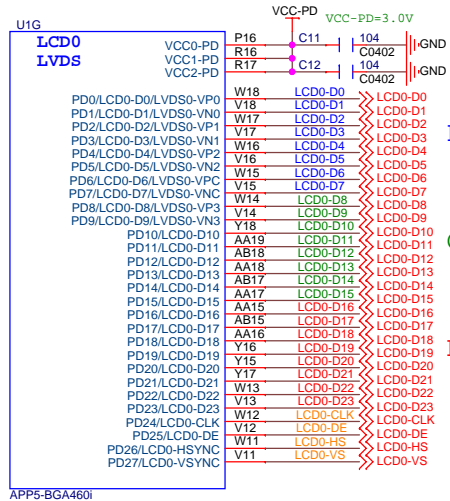
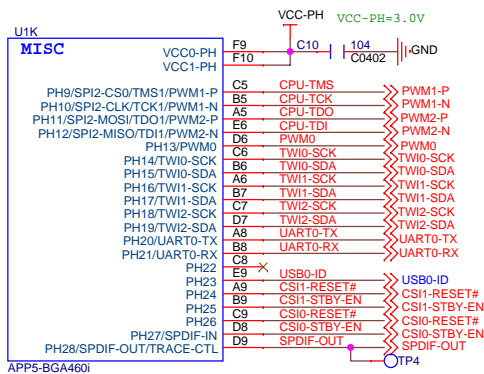
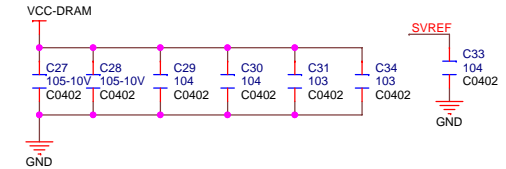
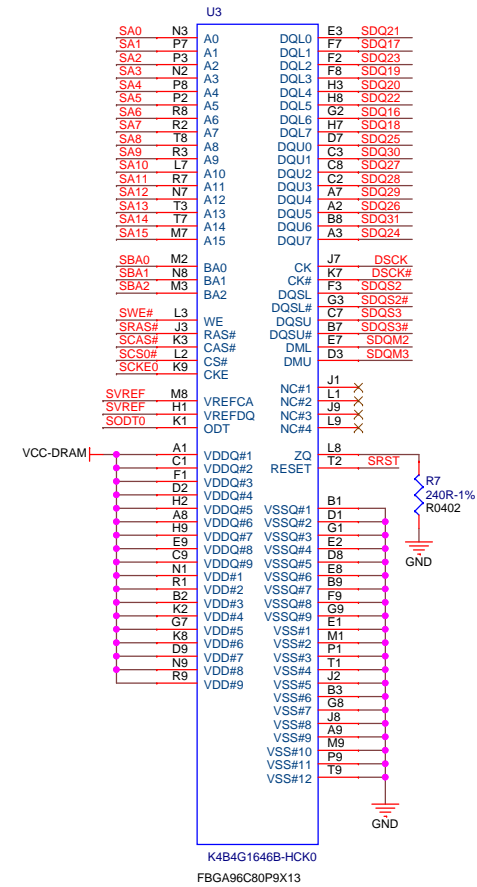
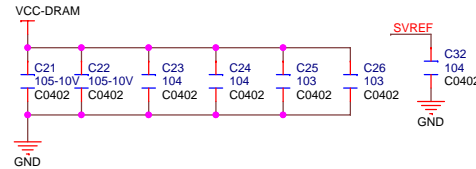
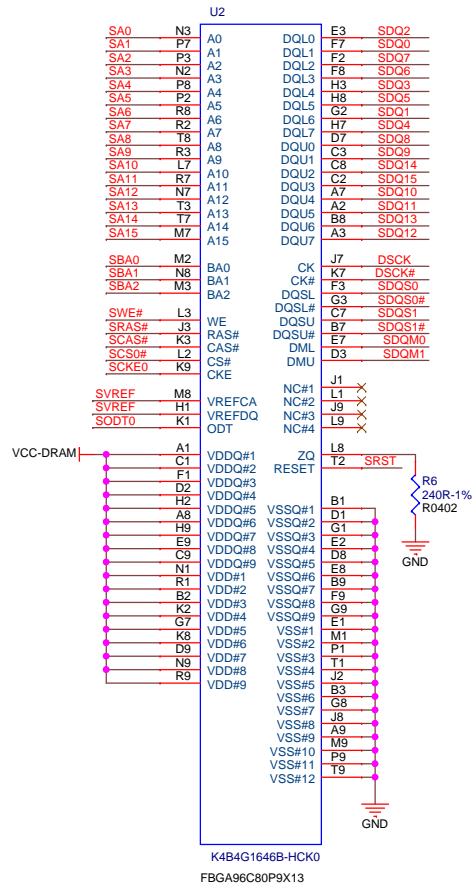
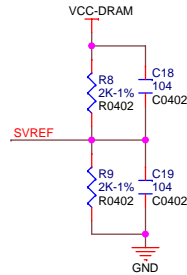
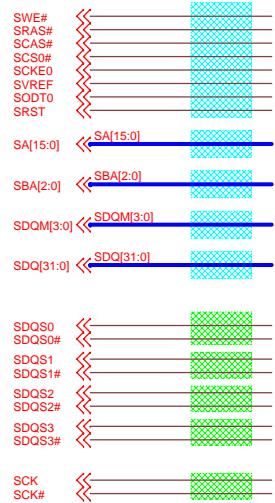


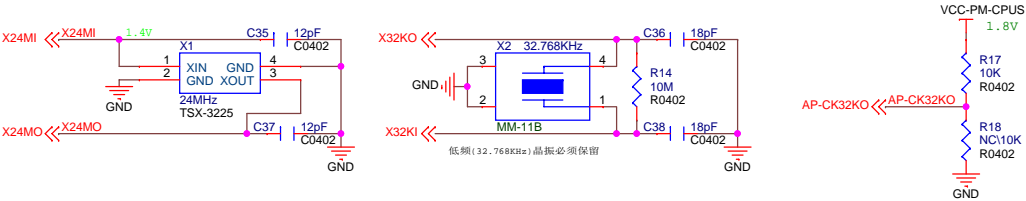
CPU2



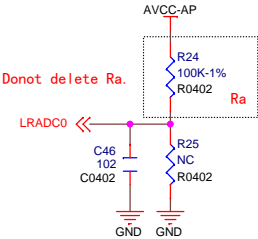
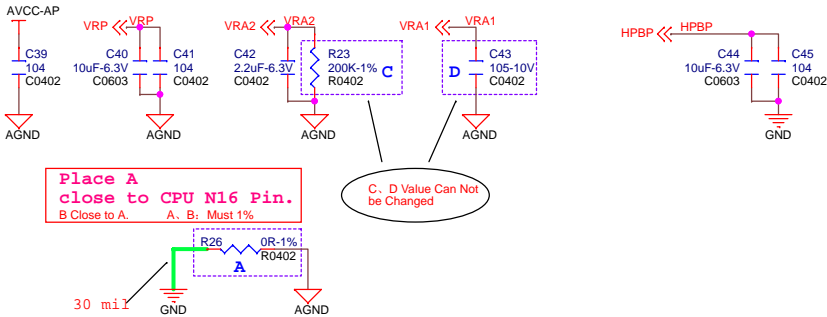
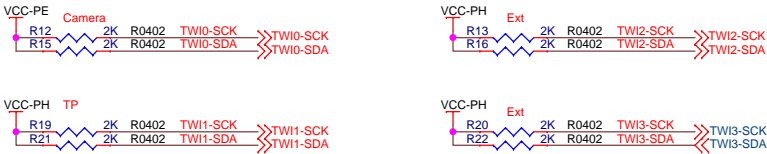
DDR3-16BITx2



Beside CPU

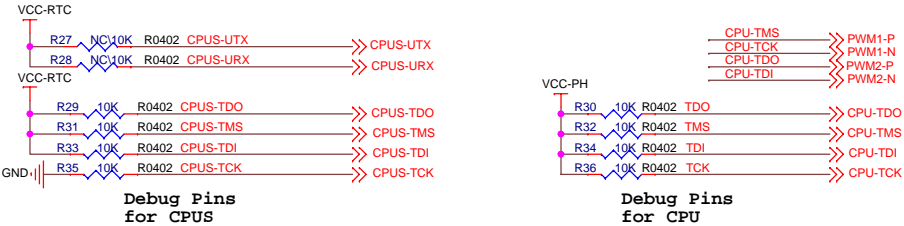


Pull-Up Resistorfor TWIs



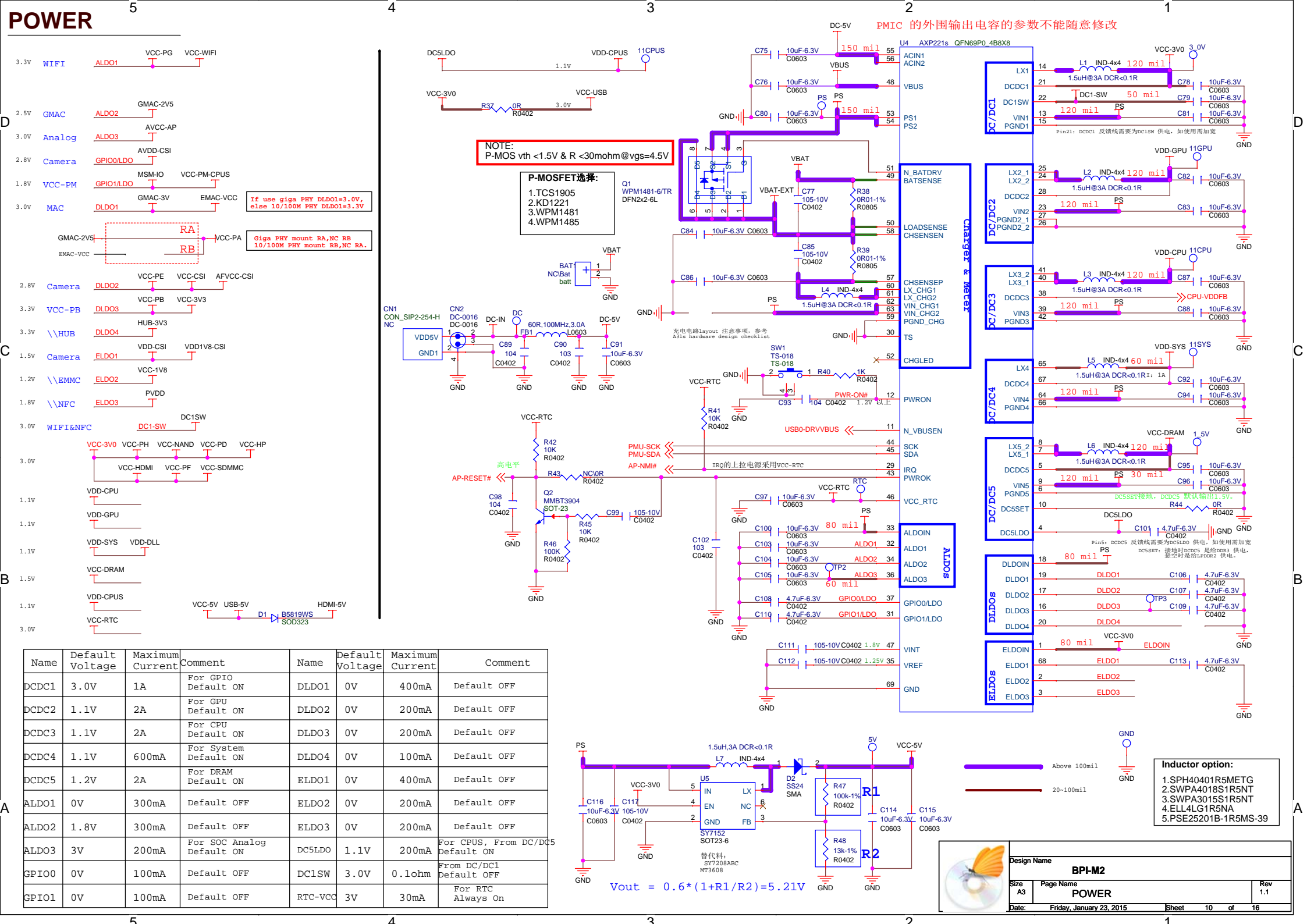
AVCC-AP、VRP、VRA1、VRA2 网络上的到地电阻和电容必须一点接地

Debug ports

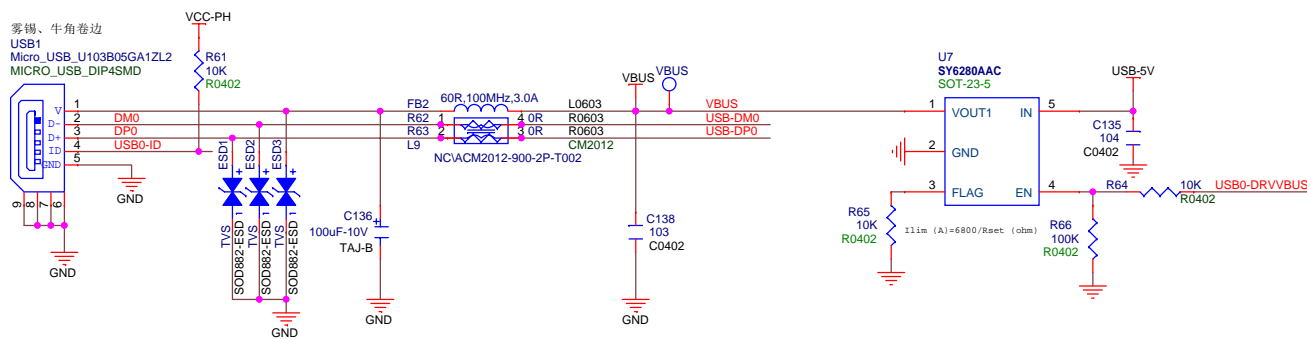


This part used for system debug must be kept.

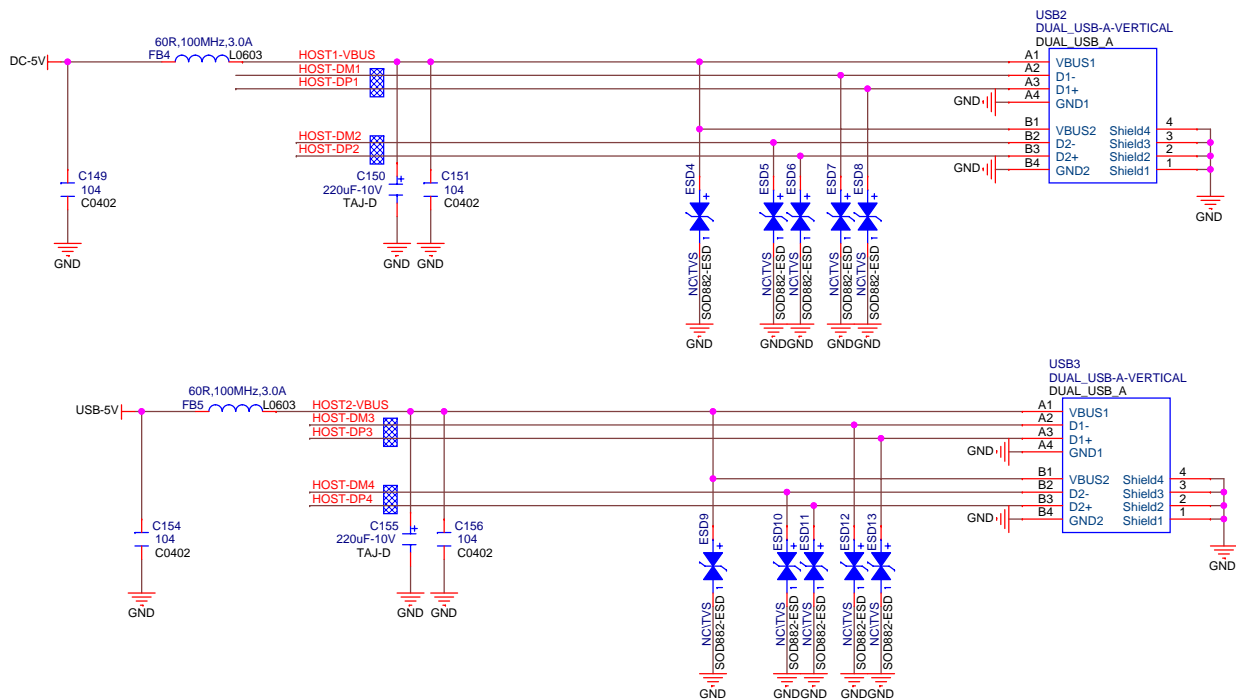
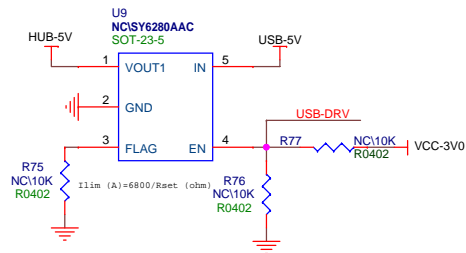
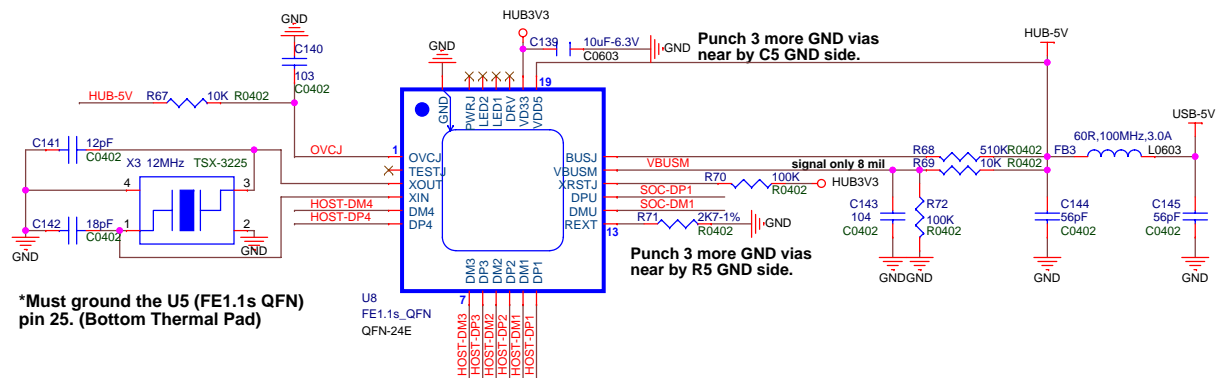
This part used for system debug must be kept.



USB-DM0
USB-DP0
USB-ID
USB0-DRVVBUS

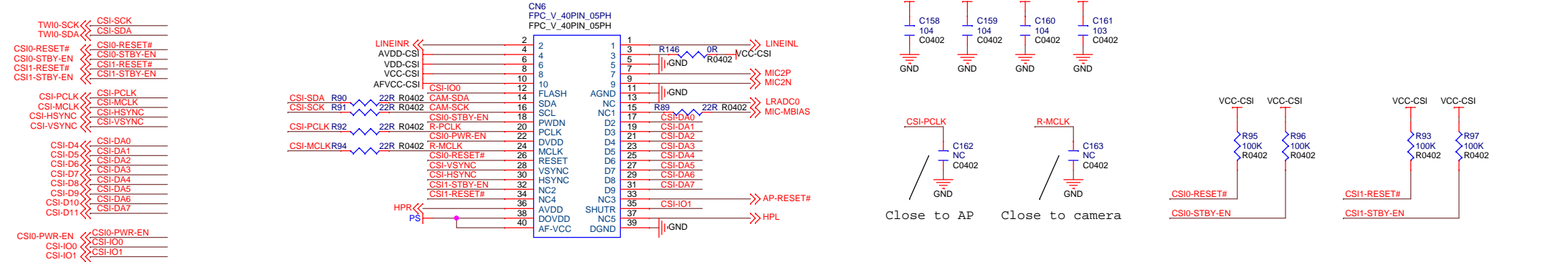


USB-DM1
USB-DP1
USB-DRV

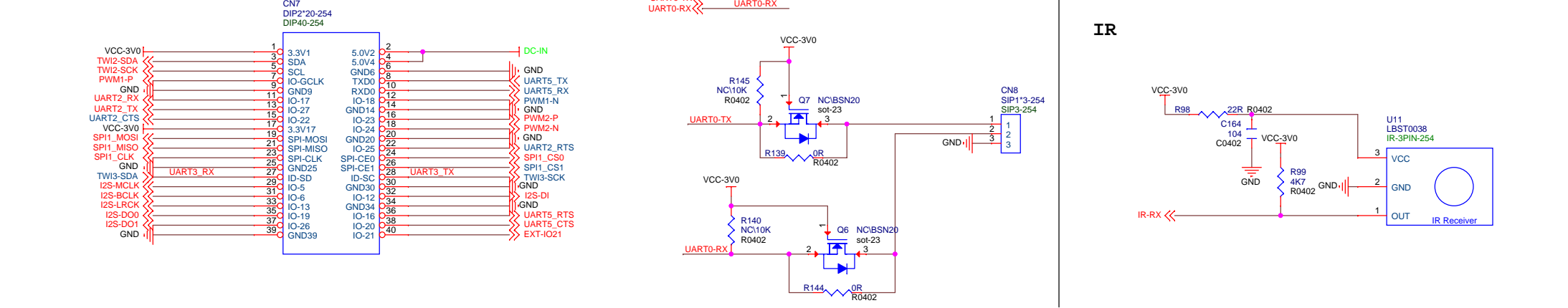


Camera-Ext-Ir-LEDs-Key

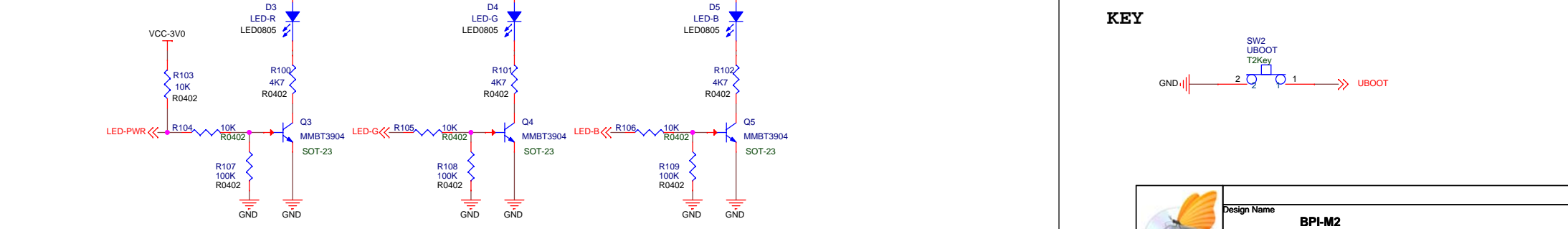
Camera




Ext



LEDs

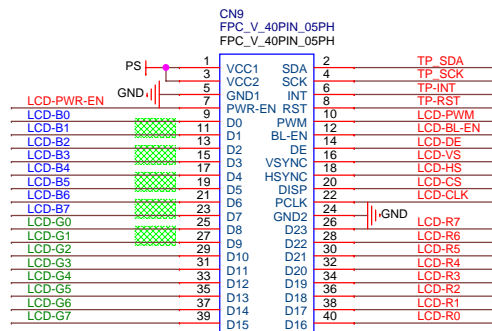
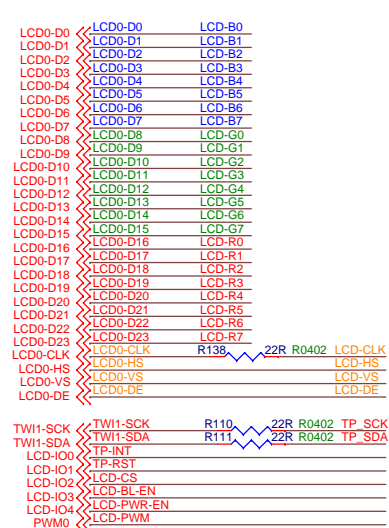




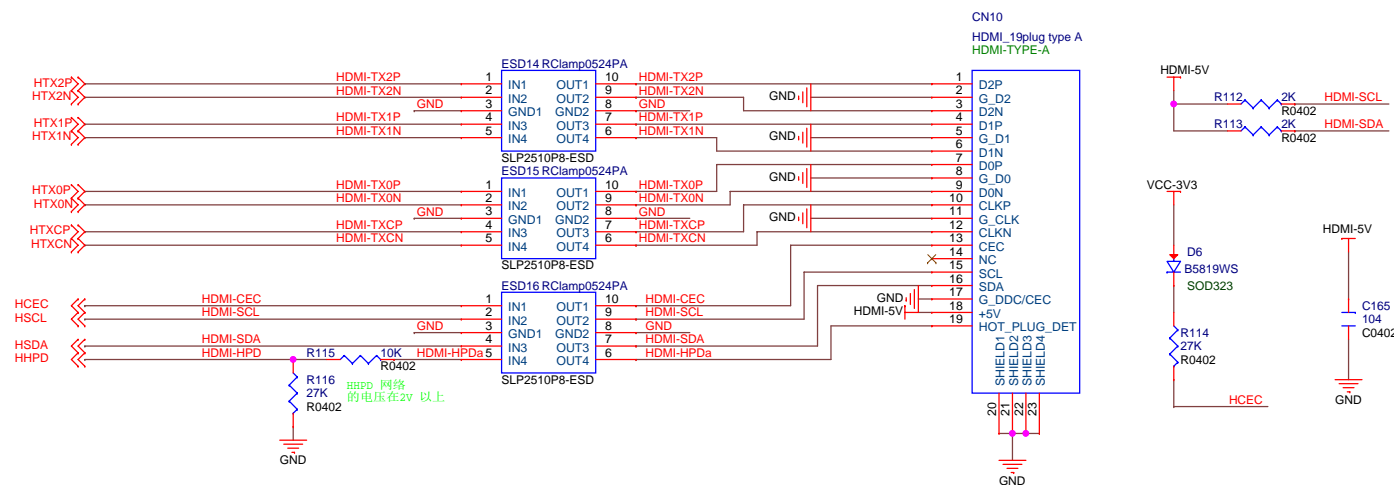
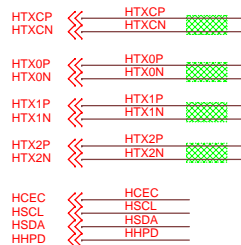
Design Name		
BPI-M2		
Size	Page Name	Rev
A3	Camera-Ext-Ir-LEDs-Key	1.1
Date:	Friday, January 23, 2015	Sheet 13 of 16

LCD-HDMI

LCD

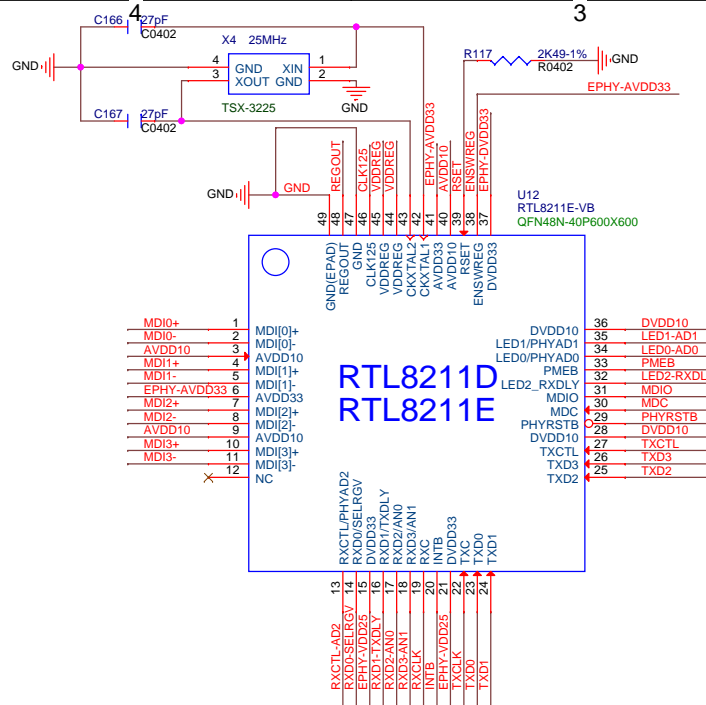
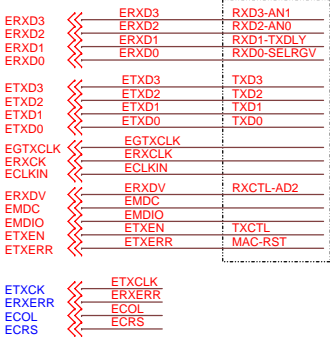


HDMI

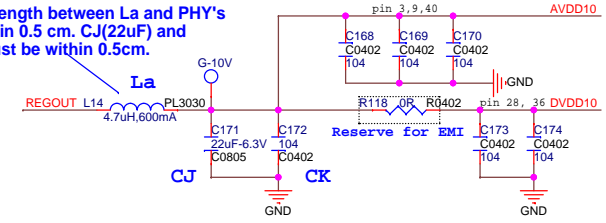


Design Name			
BPI-M2			
Size A3	Page Name		Rev 1.1
	LCD-HDMI		
Date:	Friday, January 23, 2015	Sheet	14 of 16

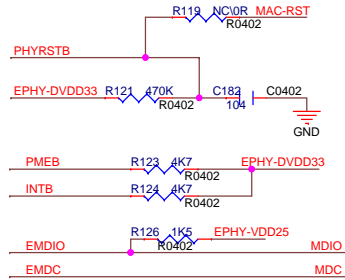
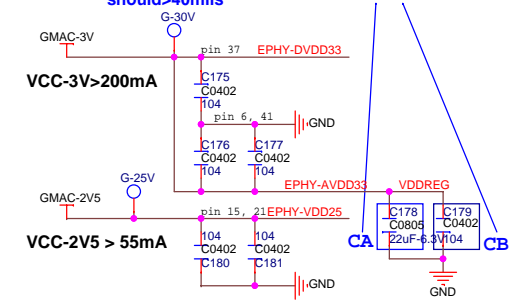
GMAC



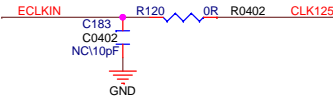
Note 1: The Trace length between La and PHY's Pin48 must be within 0.5 cm. CJ(22uF) and CK(0.1uF) to La must be within 0.5cm.



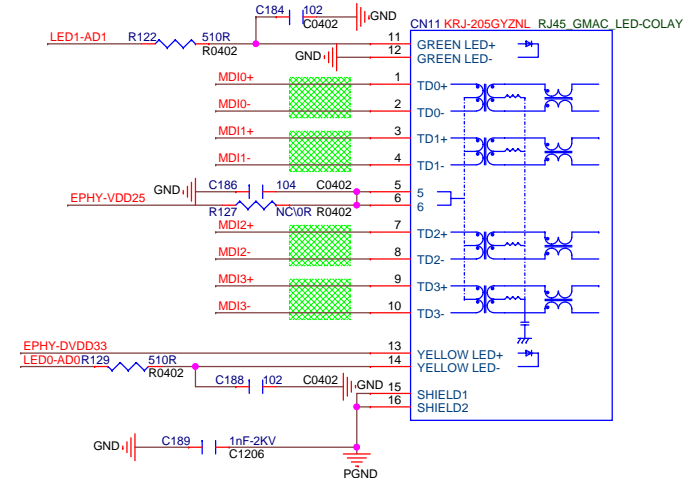
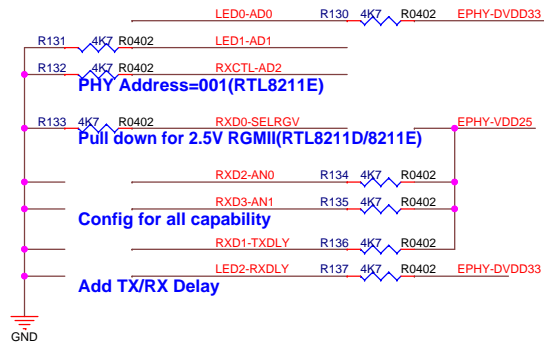
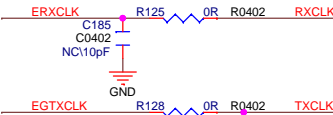
Note 2: The Trace length from CA(22uF), CB(0.1uF) to Pin 44,45(VDDREG) must be within 0.5 cm. The trace width from AVDD33 to Pin 44,45 should be 40mils



Place filter network close to CLK125. Reserved for EMI



Place filter network close to RX_CLK. Reserved for EMI



LED0: Blinking=Transmitting or Receiving.
LED1: Link Up/Down



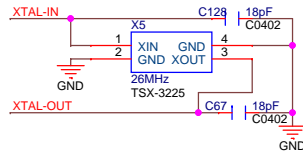
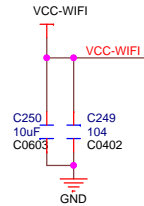
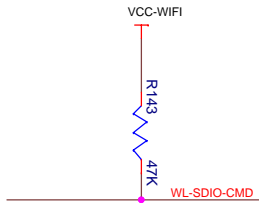
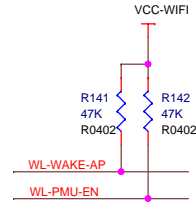
WiFi

WL-SDIO-D2
WL-SDIO-D3
WL-SDIO-CMD
WL-SDIO-CLK
WL-SDIO-D0
WL-SDIO-D1

PL8
PL5
AP-CK32KO

WL-PMU-EN
WL-WAKE-AP

AP-CK32KO



50 Ohm RF trace

