18CSS201J / Analog and Digital Electronics

List of Experiment

S.No	Title	Virtual Lab Platform	Faculty Incharge	Additional Faculty
1	Design and Implementation of Half Wave and Full Wave Rectifiers using simulation package and demonstrate its working	Multi Sim Simulator	A.Sureshkumar	Dr.N.Kalaiarasi Dr.S.Padmini
2	Design and implement a Schmitt trigger using Op-Amp using a simulation package and demonstrate its working	Multi Sim Simulator		Dr.S.Vijayalakshmi
3	Design and implement a rectangular waveform generator (Op-Amp relaxation oscillator) using a simulation package and demonstrate the working of it	Multi Sim Simulator	Mr.V.Pradeep	Ms.Anitha D
4	Design and implementation of transistor as a switch	Multi Sim Simulator	Mr.T.M.Thamizh Thendral	Dr.Joseph Peter Dr. Sivaprasad
5	Design CMOS Inverter and measure its propagation delay for both the rising edge and the falling edge	Multi Sim Simulator		Dr.Y.Jeyashree Dr.C.Naveen
6a	Design and implementation of Binary to gray code converters using logic gates	Multi Sim Simulator	Ds.S.Usha	Dr. R. Ramya
6b	Design and implementation of Gray to Binary code converters using logic gates	Multi Sim Simulator		Ms.Uthra R
6c	Hardware Implementation of Code Converters Using Virtual Lab – IIT Bombay	virtual lab- IIT bombay		Dr.K. Mohanraj
7a	Design and implementation of Magnitude Comparator combinational circuits using simulation package	Multi Sim Simulator	Dr.A. Geetha	Ms.R.C.Ilambirai
7b	Hardware Implementation Using Virtual Laboratory	virtual lab- IIT bombay		Ms.C.Anuradha
8	Design and implementation of Synchronous sequential circuits using Simulation Package	Multi Sim Simulator	Mr.D.Selvabharathi	Dr.K.Selvakumar & Mr.V Kubendran
9	Implementation of SISO, SIPO, PISO and PIPO shift registers using Flip- flops	virtual lab- IIT	Mr.P.Kanakaraj	Mr.Vigneshwaran T Mr. Vinothkumar
10	Design and simulation of 3-bit Synchronous up and down counter using Multi sim	Multi Sim Simulator	Dr. R. Palanisamy	Dr.R,Brindha
11	Digital Application	virtual lab- IIT	Dr.Dominic Savio A	Mr. Balaji C
Remote Access Lab				
VL1 (a)	Design and implementation of Binary to gray code converters using remote access lab	Remote Access Lab	Ms. S. Geethanjali	
VL 1 (b)	Design and implementation of Gray to Binary code converters using remote access lab	Remote Access Lab		
VL 2	Design and simulation of 3-bit Synchronous up and down counter using remote access lab	Remote Access Lab	Ms. Shanmuga priya	