lab4 report

algorithm

the booting program (at x0200)

- Enable the keyboard interrupt by saving x4000 (the 14th bit is 1) to the address of KBSR(xFE00).
- The interrupt service routine is at address x2000, so we should save it to the address x0180, so that PC can load x2000 when an interrupt is initiated.
- Push the PSR and PC onto the supervisor stack.

the code:

```
.ORIG x0200
1
2
3
      LD RO, KBINT_ADD
       STI RO, KBINT_entry; The PC is loaded with the contents of memory
   location x0180
5
      LD RO, KBSR_D
       STI RO, KBSR_addr ; enable the interrupt
6
7
8
       ; PUSH USER_PSR and USER_PC
9
      LD R6, OS_SP
10
       ADD R6, R6, #-2
      LD RO, USER_PSR
11
      STR RO, R6, #1
12
      LD RO, USER_PC
13
       STR RO, R6, #0
14
15
       RTI
16
17
      OS_SP .FILL x3000
18
      USER_PSR .FILL x8002
     USER_PC .FILL x3000
19
20
      KBSR_addr .FILL xFE00
21
      KBSR_D .FILL x4000
22
       KBINT_entry .FILL x0180
23
       KBINT_ADD .FILL x2000
   .END
```

the interrupt service routine(at x2000)

- Save the data of R0, R2 and R3, and load the data of R1(the address of the string to be output)
- Load the input character from KBDR.
- Check the ASCII code of the character. If it is a digit, branch to the MOVE subroutine, if it is a letter, branch to the TRANS subroutine, if neither, do nothing.
- In MOVE subroutine, calculate the new offset, if it is greater than 17, the largest offset, change it to 17, then fill the last position of the bird with . and fill the current position of the bird with the current character.
- In TRANS subroutine, just change the character representing the bird.

• Outpus the new string, restore R0, R2 and R3 and RTI.

```
.ORIG
            x2000
 2
            ST RO, saveRO
 3
            LD R1, saveR1
            ST R2, saveR2
 4
 5
            ST R3, saveR3
            LDI RO, KBDR
 6
 7
          ; check if R0 >= '0' and R0 <= '9'
 8
            ; detail ommitted
 9
10
    ISCHAR ; check if R0 >= 'a' and R0 <= 'z'
11
12
            ; detail ommitted
13
14
15
    MOVE
            LD R2, ASCII_ZERO
            NOT R2, R2
16
17
            ADD R2, R2, #1
            ADD R2, R2, R0; offset
18
19
            ADD R2, R2, R4
            LD RO, N_SEVENTEEN
20
21
            ADD RO, R2, RO
22
            BRnz SKIP; if new offset > 17, change it to 17
23
            AND R2, R2, #0
            ADD R2, R2, #9
24
25
            ADD R2, R2, #8
26
            ADD R2, R2, #0
27
    SKIP
            LD R3, ASCII_DOT
28
29
            ADD RO, R1, R4; previous ofs
            ADD R4, R2, #0; update R4
30
            LDR R5, R0, #0; previous char
31
            STR R3, R0, #0
32
33
            STR R3, R0, #1
34
            STR R3, R0, #2
35
            ADD R3, R5, \#0; R3 = R0
36
            ADD R0, R1, R2
37
            STR R3, R0, #0
38
            STR R3, R0, #1
39
            STR R3, R0, #2
            BRnzp END
40
41
42
    TRANS
43
            ; LEA R1, OUTPUT
44
            ; ADD R2, R1, R2
            ADD R2, R1, R4
45
46
            STR R0, R2, #0
47
            STR R0, R2, #1
            STR R0, R2, #2
48
49
            BRnzp END
50
51
    END
52
            ADD R0, R1, #0
53
            PUTS
```

```
54
           AND R0, R0, #0
55
           ADD R0, R0, #10
56
           OUT
57
           LD RO, saveRO
58
           ; LD R1, saveOUT
59
           LD R2, saveR2
           LD R3, saveR3
60
61
           RTI
62
63
   KBDR
              .FILL xFE02
64
   DSR
              .FILL xFE04
65 DDR
              .FILL xFE06
66 ASCII_ZERO .FILL x0030
67
   ASCII_NINE .FILL x0039
68 ASCII_a .FILL x0061
69 ASCII_Z
              .FILL x007A
   ASCII_DOT .FILL x002E
70
71
   N_SEVENTEEN .FILL xFFEF
72
   saveR0 .FILL x0000
73 saveR1
             .FILL x3022 ; to be done
            .FILL x0000
74
   saveR2
75
   saveR3
             .FILL x0000
   saveOUT .FILL x0000
76
   OUTPUT_adr .BLKW #1
77
78
               .END
```

the main routine (at x3000)

- Loop infinitely to keep outputting. For each loop, decrease the offset by 1 if it is greater than 0. To slow down the outputting, create an inner delay loop, which loops for x8000 times.
- The user of some registers:
 - R1: the address of the string to be output
 - R4: the current offset

```
.ORIG x3000
 2
           ; R4 : ofs R1 : sym
 3
           LEA R1, OUTPUT
4
          LD R3, FOURTY
 5
            ; LD R2, SEVEN
            AND R4, R4, #0
 6
 7
            ADD R4, R4, #9
8
           ADD R2, R4, #0
9
10
    LOOP
           LD R1, OUT_ADDR
11
            ADD R0, R1, #0
12
            PUTS
13
            AND RO, RO, #0
14
            ADD RO, RO, #10
15
            OUT
16
17
    DELAY ADD R3, R3, #-1; loop for x8000 times
18
            BRP DELAY
```

```
19
     ; if R4 > 0, decrese it by 1
20
    ; modify the string
21 SKIP_ LD R3, FOURTY
        BRnzp LOOP
22
23
24
25
   LABEL_ .FILL x0000
26 LABEL___
              .FILL x0000
27 FOURTY .FILL x8000
28 | DELAY_COUNT .FILL #256
29 SAVERO_ .FILL x0000
30 SAVER2_
            .FILL x0000
            .FILL x0000
31 SAVER3_
32 DOT
            .FILL x002E
33 OUT_ADDR .FILL x3022 ; to be done
34 OUTPUT .STRINGZ ".....aaa....."
35
             .END
```

problems

- If the RTI instruction is executed in user mode after a keyboard interrupt, what will happen? How does the priviledge changes?
 - The privilege mode exception is caused.
 - Push the PSR and address of RTI onto the supervisor stack, load PC from x1000
 - Supervisor mode --> User mode --> Supervisor mode.
- How does LC-3 create a snapshot when an interrupt is enabled?
 - o push PC, PSR to the supervisor stack.
 - o change the stack pointer if the mode changes.
 - let the interrupt service save R0-R7
- What are the 3 kinds of exception? What condition can cause them?
 - Pivileged mode exception. It's caused when the program tries to execute the RTI instruction while in User mode.
 - Illegal opcode. It's caused when the program tries to execute an instruction of opcode 13.
 - Access control violation (ACV) exception. It's caused when the program tries to access a privileged memory location(x0000 ~ x2FFF) while in User mode.
- How to input a character through the keyboard by interrupt?
 - 1. INT is asserted, INTV is loaded with the interrupt vector corresponding to that external event.
 - 2. Put itself into Supervisor mode if it isn't in Supervisor mode, push the PSR and PC of the interrupted process onto the supervisor stack, and load the PC with the starting address of the interrupt service routine.
 - 3. Save the address of interrupt service routine to $\times 0180$, at the address, load the character from KBDR.