

## hw06

3200102888 米博宇

### 6.3

---

The asm code can be

```
1  .ORIG x3000
2
3  AND R1, R1, #0
4  ADD R1, R1, #1
5  LDI R0, R0ADR
6  LDI R2, R2ADR
7
8
9  LOOP
10 ADD R0, R0, #0
11 BRZ END
12 ADD R1, R1, R1
13 ADD R0, R0, #-1
14 BRnzp LOOP
15
16 END
17 ADD R3, R1, #0 ; R3 = R1
18 AND R1, R1, R0
19 BRnp SKIP ; if (R1 AND R0) is not 0, the bit has been set
20 ADD R1, R3, R2
21
22 SKIP
23 ADD R3, R1, #0
24 TRAP x25
25
26 R2ADR .FILL x4000
27 R0ADR .FILL x4001
28 .END
29
30 .ORIG x4000
31 RTWO .FILL x3101
32 RZERO .FILL x0005
33 .END
```

The machine language can be

```

1 0011 1111 0000 0000 ; 0x3F00
2 0101 001 001 1 00000
3 0001 001 001 1 00001
4 0010 000 0 1111 1101
5 0010 010 0 1111 1101
6 0001 000 000 1 00000
7 0000 010 0 0000 0011
8 0001 001 001 0 00 001
9 0001 000 000 1 11111
10 0000 111 1 1111 1011
11 0001 001 011 0 00 010
12 1111 0000 0010 0101

```

## 7.4

symbol	address
TEST	x301F
FINISH	x3027
SAVE3	x3029
SAVE2	x302A

## 7.18

- LDR R3, R1, #0
- NOT R3, R3
- ADD R3, R3, #1

## 7.20

In module (a), 0x0015 is stored to 0x4000 when the instruction STI R0, PTR is executed.

In module (b), 0x0015 is stored to 0x4000 at the beginning of program.