MIPS模拟器实验报告

实验要求

以程序模拟MIPS/RISC-V运行,功能包括:

汇编器:将汇编程序转换成机器码。能有较灵活的格式,可以处理格式指令、表达式、有出错信息。

汇编反汇编:汇编指令与机器码的相互转换。

模拟器:根据机器码模拟执行可以运行简单汇编程序。

- 1、模拟器运行界面设计:可以命令行或窗口界面。可以执行指令的汇编、反汇编,可以单步执行指令观察寄存器、内存的变化。(命令行版可参考DEBUG)
- 2、指令伪指令的汇编反汇编:将汇编指令转换成二进制机器码,能够处理标号、变量。
- 3、MMU存储器管理单元:存储器存取模拟。大头小头,对齐不对齐,Cache,虚拟存储。
- 4、指令: 常用5~10条;

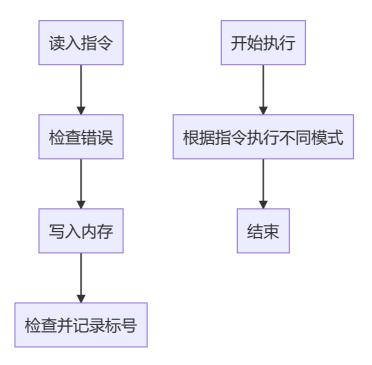
命令:-->R-看寄存器,-->D-数据方式看内存,-->U-指令方式看内存,-->A-写汇编指令到内存,

-->T-单步执行内存中的指令

实现功能

- 1. 读取文件中指令
- 2. 支持label和指令间跳转
- 3. 支持查看寄存器、内存、单步执行、全部执行
- 4. 支持运行中写入指令(仅一条)
- 5. 实现了小头模式读内存
- 6. 支持add, and, or, addi, sub, lw, sw, beq, j指令的实现
- 7. 有报错信息

程序架构



MipsCPU 类

```
1 class MipsCPU {
 2
   public:
 3
       const int MAXSIZE = 4096;
       int PC, IR;
       int *Rgf;
       int *memory;
6
       map<string, int> rgf;
       map<int, const char*> num2rgf;
8
9
       map<string, int> label;
       int size;
10
11
       int reg2num(string s)
12
       int str2num(string s)
13
       int unasm(string in) //反汇编
14
       bool load() //载入指令
       void disp() //显示寄存器
15
       void disp_inst()//显示指令
16
17
       void disp_num(int mode,int num) //小端输出
       void disp_memo() //展示内存
18
19
       void exec(int mode)//执行指令
20 };
```

测试过程

测试代码:

```
1 addi $t1, $zero, 3
2 loop:
 3 add $s0, $t1, $s0
4 addi $t0, $t0, 1
 5 addi $t2, $zero, 3
6 beg $s0, $t1, Corret
7 Incorret:
8 addi $a0, $zero, 1
9 j end
10 Corret:
11 add $a0, $zero, $s0
12 j end
13 end:
14 addi $s2, $zero, 16708
15 sub $s2, $s2, $a0
16 sw $s2, 96($zero)
17 lw $s1, 96($zero)
```

测试过程:

-u查看指令

```
1 -u
 2 13
 3 0000:0x20090003 ADDi
                           $t1, $zero, 3
                           $s0, $t1, $s0
4 0001:0x01308020 ADD
5 0002:0x21080001 ADDi
                           $t0, $t0, 1
6 0003:0x200A0003 ADDi
                           $t2, $zero, 3
                           $t1, $s0, 2
7 0004:0x12090002 BEQ
8 0005:0x20040001 ADDi
                           $a0, $zero, 1
9 0006:0x08000009 J
                           9
10 0007:0x00102020 ADD
                           $a0, $zero, $s0
11 0008:0x08000009 J
                           $s2, $zero, 16708
12 0009:0x20124144 ADDi
                           $s2, $s2, $a0
13 000A:0x02449022 SUB
14 000B:0xAC120060 SW
                           $s2, 96($zero)
15 000C:0x8C110060 LW
                           $s1, 96($zero)
```

```
1 -a
2 0x0000: ADDi $t1, $zero, 2
3 -u
4 13
                          $t1, $zero, 2
5 0000:0x20090002 ADDi
6 0001:0x01308020 ADD
                          $s0, $t1, $s0
7 0002:0x21080001 ADDi
                          $t0, $t0, 1
8 0003:0x200A0003 ADDi
                          $t2, $zero, 3
9 0004:0x12090002 BEQ
                          $t1, $s0, 2
                          $a0, $zero, 1
10 0005:0x20040001 ADDi
11 0006:0x08000009 J
                          9
12 0007:0x00102020 ADD
                          $a0, $zero, $s0
13 0008:0x08000009 J
14 0009:0x20124144 ADDi
                         $s2, $zero, 16708
                          $s2, $s2, $a0
15 000A:0x02449022 SUB
16 000B:0xAC120060 SW
                          $s2, 96($zero)
17 000C:0x8C110060 LW
                          $s1, 96($zero)
```

-t单步执行

```
1 -t
2 0x20090002 ADDi $t1, $zero, 2
$v1 =
  00000000
4  a0 = 00000000  a1 = 00000000  a2 = 00000000 
                                         a3 =
  00000000
   t0 = 00000000 t1 = 00000002 t2 = 00000000
                                         t3 =
  00000000
    $t4 = 00000000 $t5 = 00000000 $t6 = 00000000
                                         t7 =
  00000000
   $s0 = 00000000 $s1 = 00000000 $s2 = 00000000
                                         s3 =
  00000000
   $$4 = 00000000  $$5 = 00000000  $$6 = 00000000
                                         s7 =
  00000000
   $t8 = 00000000 $t9 = 00000000 $k0 = 00000000
                                         k1 =
  00000000
    10
                                         $ra =
  00000000
```

-r查看寄存器

```
1 -r
2 \ensuremath{\$}zero = 00000000 \ensuremath{\$}at = 00000000 \ensuremath{\$}v0 = 00000000 \ensuremath{\$}v1 =
  00000000
   a0 = 00000000 a1 = 00000000 a2 = 00000000
                                          a3 =
  00000000
   t0 = 00000000 t1 = 00000002 t2 = 00000000
                                          t3 =
  00000000
   $t4 = 00000000 $t5 = 00000000 $t6 = 00000000
                                          t7 =
  00000000
  s3 =
  00000000
 s7 =
  00000000
   $t8 = 00000000 $t9 = 00000000 $k0 = 00000000
                                          k1 =
  00000000
   $ra =
  0000000
```

-p全部执行

```
1 -p
                       $s0, $t1, $s0
2 0x01308020
                ADD
                       $t0, $t0, 1
3 0x21080001
                ADDi
4 0x200A0003
               ADDi
                       $t2, $zero, 3
5 0x12090002
               BEQ
                       $t1, $s0, 2
                      $a0, $zero, $s0
6 0x00102020
               ADD
7 0x08000009
                J
                ADDi
8 0x20124144
                       $s2, $zero, 16708
9 0x02449022
                SUB
                       $s2, $s2, $a0
10 0xAC120060
                SW
                       $s2, 96($zero)
11 0x8C110060
                       $s1, 96($zero)
               LW
12 \$zero = 00000000 \$at = 00000000 \$v0 = 00000000
                                               $v1 =
   0000000
13
   a3 =
   0000000
```

```
14
    t0 = 00000001 t1 = 00000002 t2 = 00000003
                                                  t3 =
   00000000
     $t4 = 00000000 $t5 = 00000000 $t6 = 00000000
15
                                                  t7 =
   00000000
     $s0 = 00000002 $s1 = 00004142 $s2 = 00004142
                                                  s3 =
16
   00000000
     $s4 = 00000000 $s5 = 00000000 $s6 = 00000000
17
                                                 s7 =
   00000000
    t8 = 00000000 t9 = 00000000 k0 = 00000000
18
                                                 k1 =
   00000000
$gp = 00000000 $sp = 00000000 $fp = 00000000
                                                 $ra =
   00000000
```

-d查看内存

```
1 -d
2 02 00 09 20 20 80 30 01
3 01 00 08 21 03 00 0A 20
4 02 00 09 12 01 00 04 20
5 09 00 00 08 20 20 10 00
6 09 00 00 08 44 41 12 20
7 22 90 44 02 60 00 12 AC
8 60 00 11 8C 00 00 00
```

报错

```
1 add $s0, $t1
2 ~~~~~~
3 ERROR: a register is missed here
```