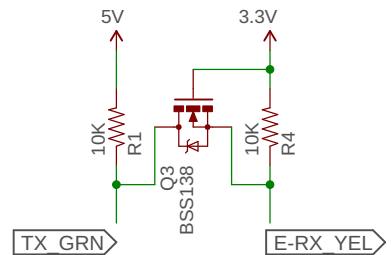
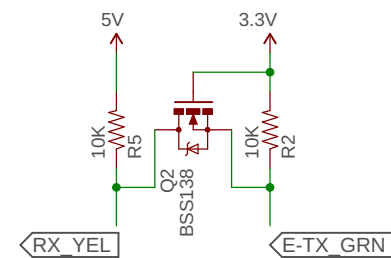


C

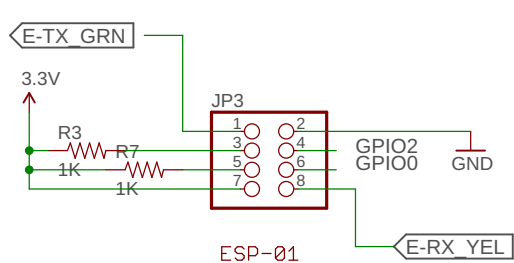
D

E



R 5V
O 3V3 always
Y RX
G TX
B
U sw P pull-up, sw R pull-down
Br Uadj
Gy 3V3 int "3V3"

v0.3.0 hand wired
v0.3.1 added 4-pin GPIO header
PCB order 2018-02-22



- 1 - E-TX
- 2 - GND
- 3 - EN / CH_PD
- 4 - GPIO2
- 5 - ~RST
- 6 - GPIO0 / ~Flash
- 7 - 3V3
- 8 - E-RX

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C

D

E

