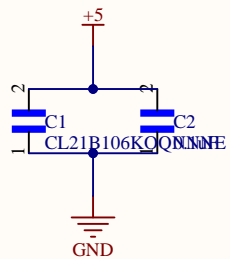
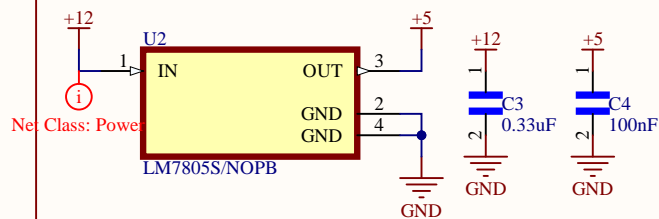


Title		
Size	Number	Revision
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File:	\\..Main.SchDoc	Drawn By:

## Teensy Filter Caps

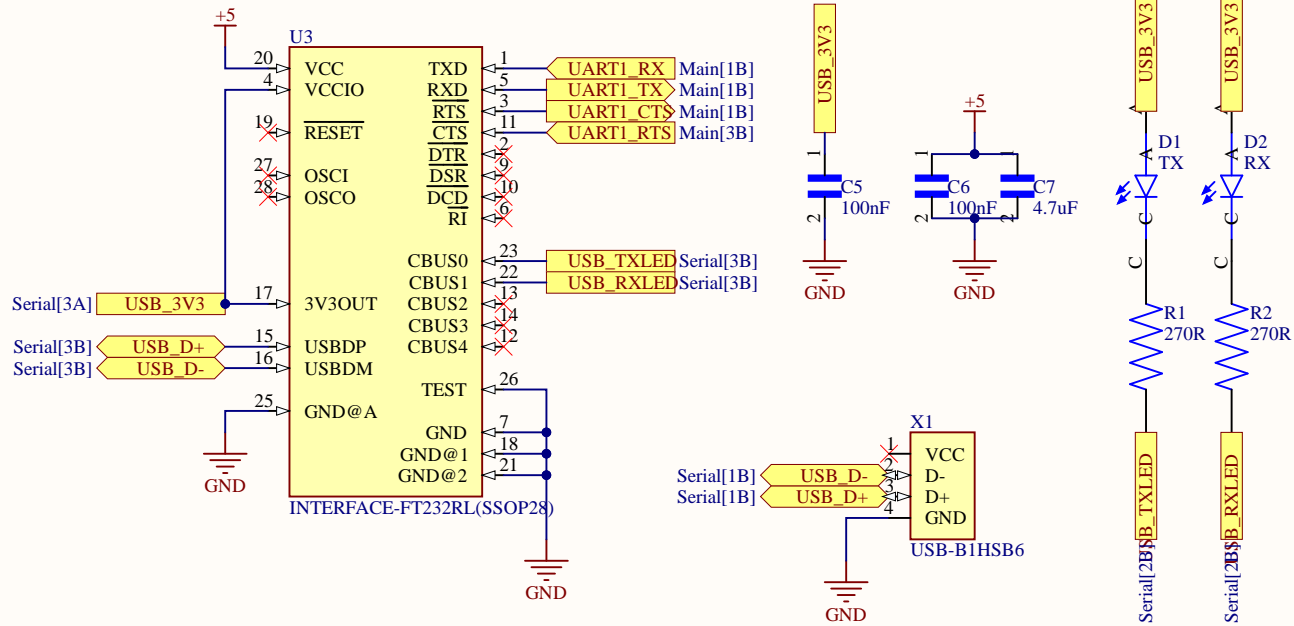


## Input Voltage Regulator

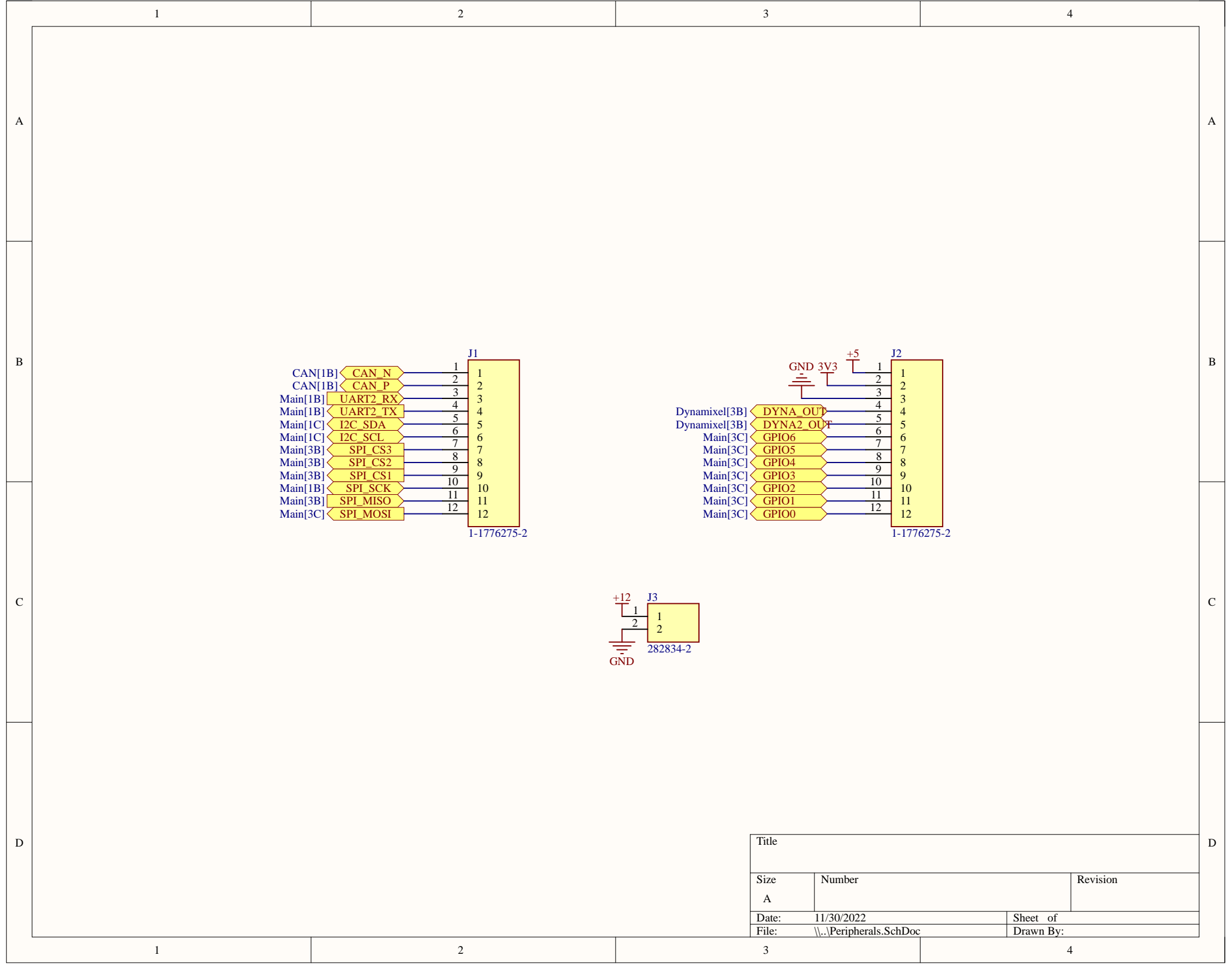


Title		
Size	Number	Revision
A		
Date:	11/30/2022	Sheet of
File:	\\.\Power.SchDoc	Drawn By:

# USB for UART1



Title		
Size	Number	Revision
A		
Date:	11/30/2022	Sheet of
File:	\\..\\Serial.SchDoc	Drawn By:



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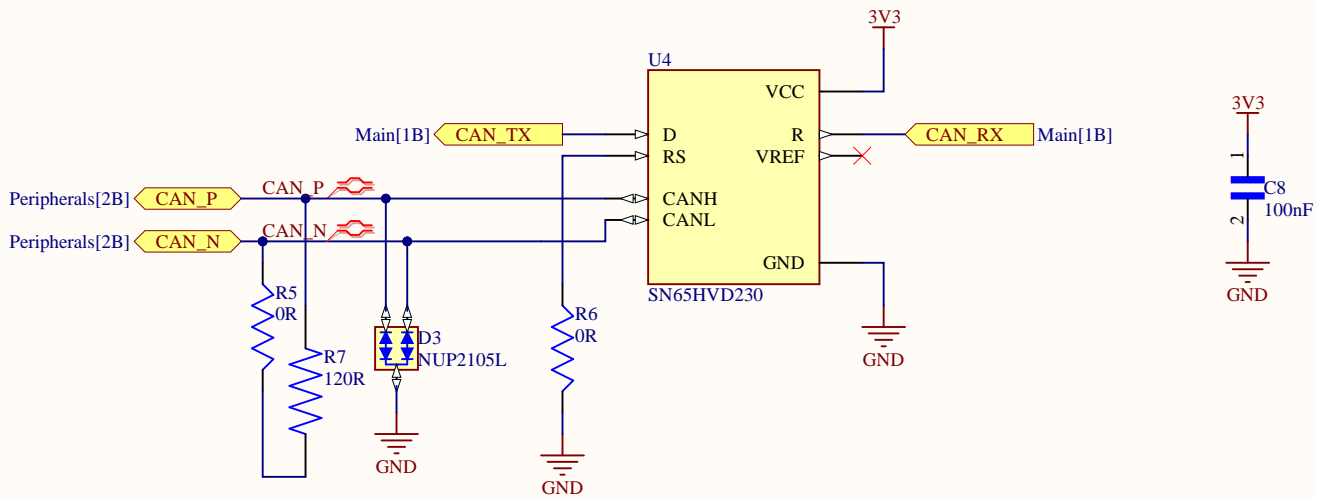
B

C

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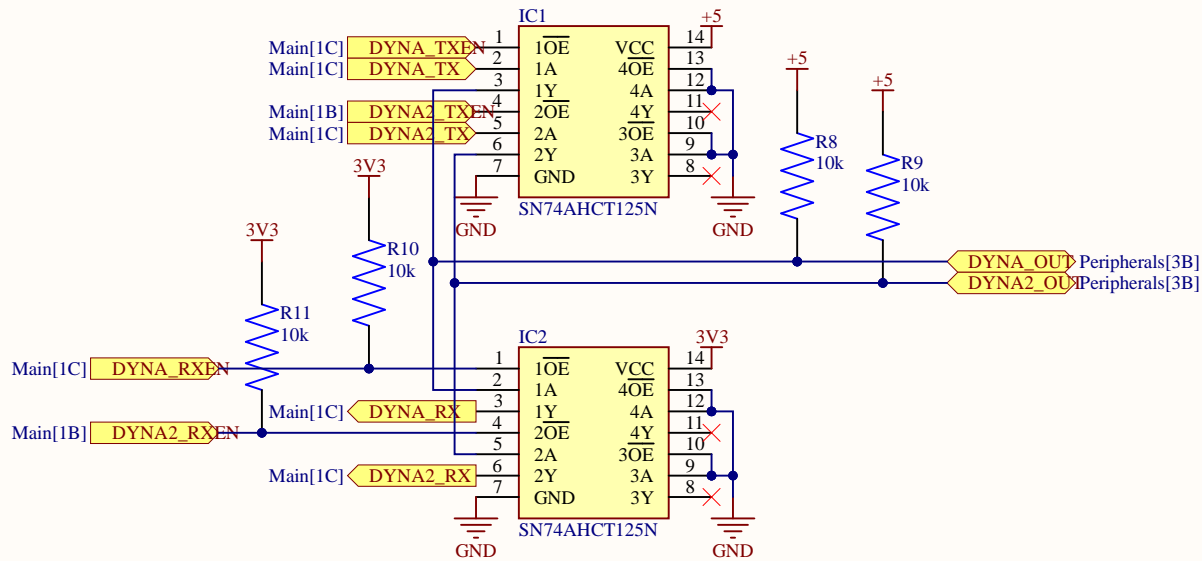
D

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A		
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# UART Level Shifter for Dynamixel



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A		
Date:	11/30/2022	Sheet of
File:	\\..\Dynamixel.SchDoc	Drawn By:

