

Title		
Size	Number	Revision
A		
Date:	11/29/2022	Sheet of
File:	\\..Main.SchDoc	Drawn By:

A

B

C

D

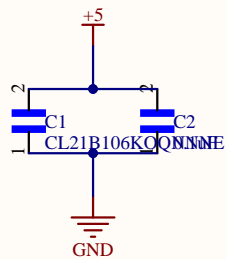
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2

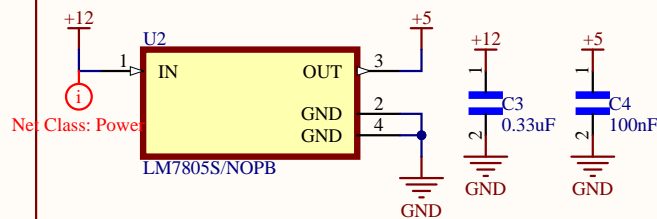
3

4

Teensy Filter Caps



Input Voltage Regulator



A

B

C

D

1

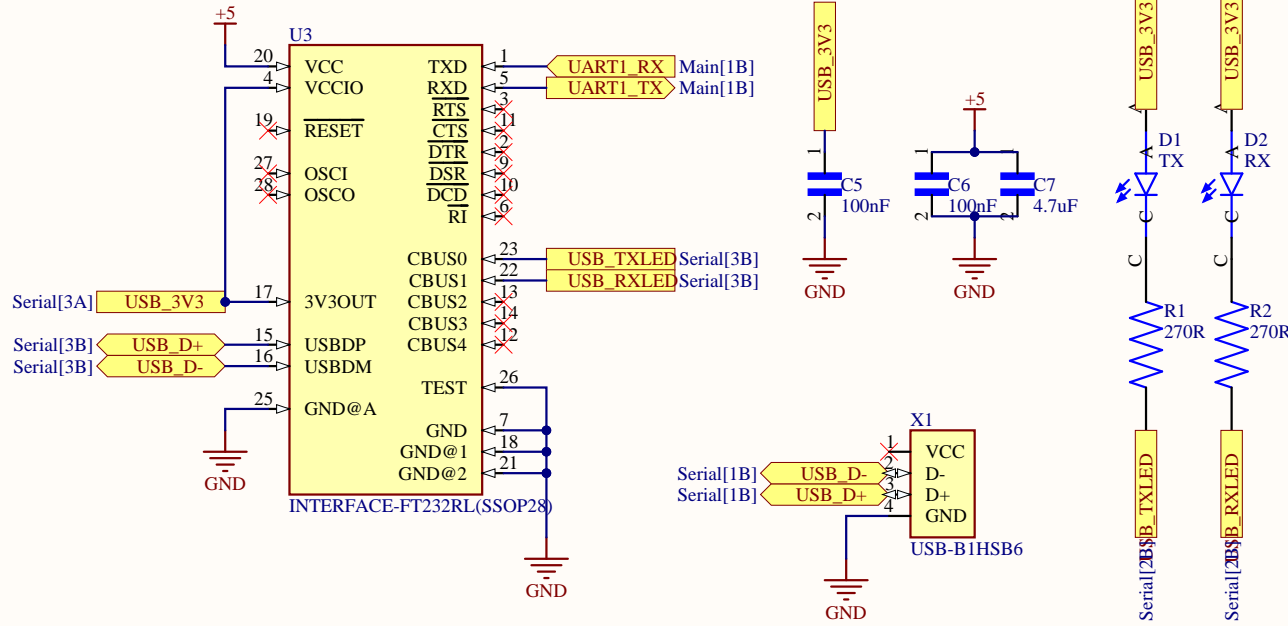
2

3

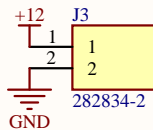
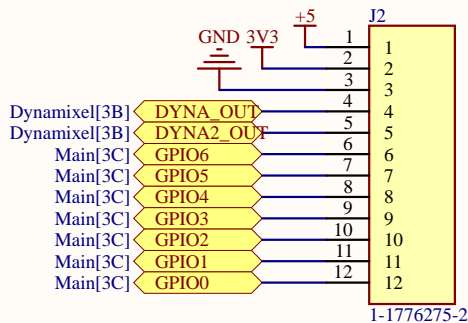
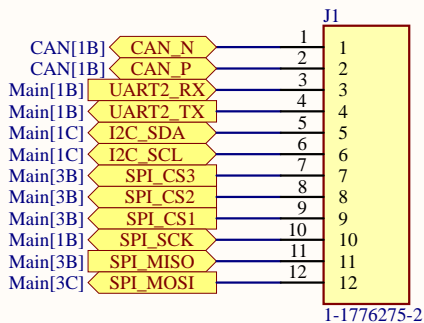
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Title		
Size	Number	Revision
A		
Date:	11/29/2022	Sheet of
File:	\\..\Power.SchDoc	Drawn By:

USB for UART1



Title		
Size	Number	Revision
A		
Date:	11/29/2022	Sheet of
File:	\\.\Serial.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	11/29/2022	Sheet of
File:	\\.\Peripherals.SchDoc	Drawn By:

A

A

B

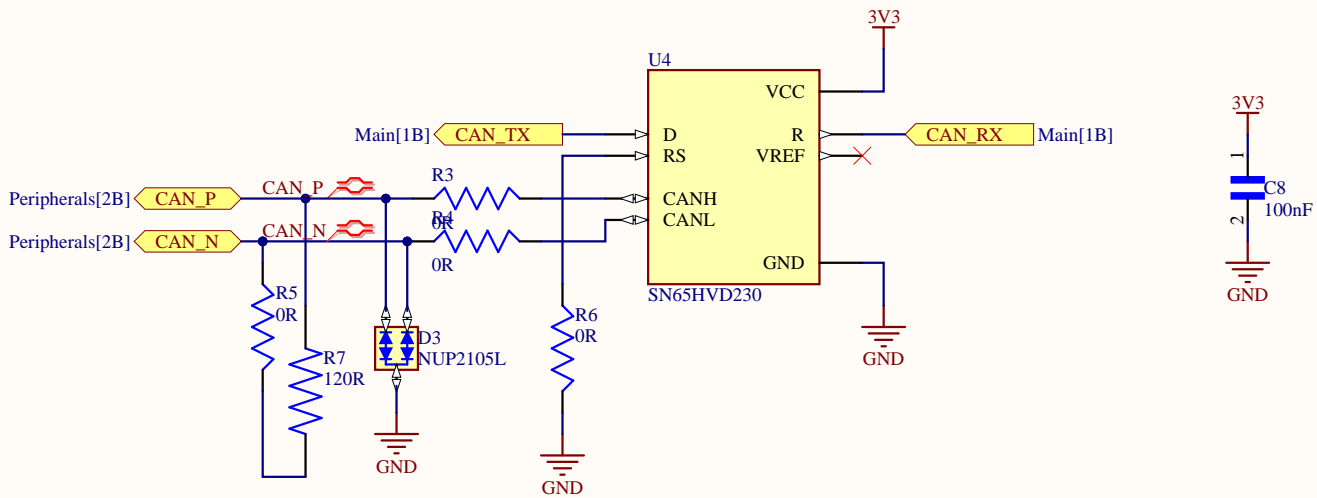
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C

C

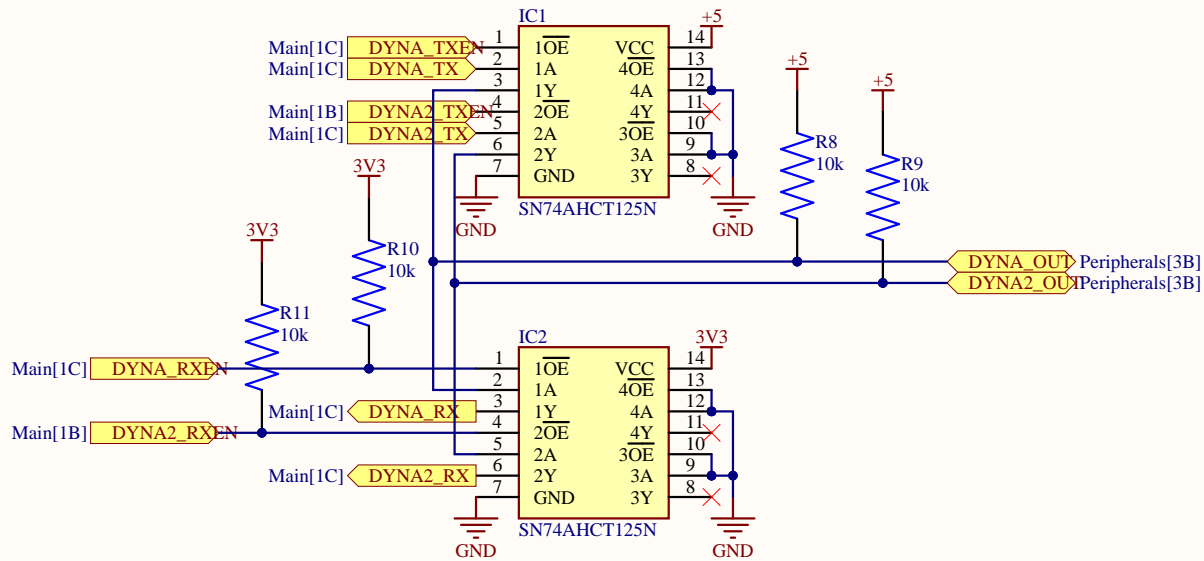
D

D



Title		
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A		
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UART Level Shifter for Dynamixel



Title		
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File:	\\..\Dynamixel.SchDoc	Drawn By:

