SAMPLE

Array Bandwidth

1 Overview

1.1 Location \$(AMDAPPSDKSAMPLESROOT)\samples\C++Amp\examples

1.2 How to Run

See the Getting Started guide for how to build samples. You first must compile the sample.

Use the command line to change to the directory where the executable is located. The default executables are placed in $(AMDAPPSDKSAMPLESROOT) \simeq C++Amp \cdot x86$ for 32-bit builds, and $(AMDAPPSDKSAMPLESROOT) \simeq C++Amp \cdot x86_64$ for 64-bit builds.

Type the following command(s).

1. ArrayBandwidth

This measures the read and write bandwidths of the GPU from the array.

2. ArrayBandwidth -h

This prints the help file.

1.3 Command Line Options

Table 1 lists, and briefly describes, the command line options.

Table 1 Command Line Options

Short Form	Long Form	Description
-h	help	Show all command options and their respective meaning.
-q	quiet	Quiet mode. Suppresses text output.
-e	verify	Verify results against reference implementation.
-t	timing	Print timing.
<u>-i</u>	iterations	Number of iterations for kernel execution.

2 Implementation Details

The sample measures the read and write bandwidths from a C++ AMP array using the following tests.

- 1. Read Bandwidth (single access) All threads are read from a single memory location.
- 2. Read Bandwidth (linear access) All threads perform linear read accesses.
- 3. Read bandwidth (linear access and uncached) All threads perform uncached linear read accesses.
- 4. Read bandwidth (random) All threads perform random read accesses.

Array Bandwidth 1 of 2

- Read bandwidth (stride) Read accesses for adjacent threads are strided so that the memory reads are uncoalesced.
- 6. Write Bandwidth (linear access) All threads perform linear write accesses.

Contact

Advanced Micro Devices, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA, 94088-3453 Phone: +1.408.749.4000

For AMD Accelerated Parallel Processing:

URL: developer.amd.com/appsdk
Developing: developer.amd.com/

Support: developer.amd.com/appsdksupport developer.amd.com/openclforum



The contents of this document are provided in connection with Advanced Micro Devices, Inc. ("AMD") products. AMD makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. The information contained herein may be of a preliminary or advance nature and is subject to change without notice. No license, whether express, implied, arising by estoppel or otherwise, to any intellectual property rights is granted by this publication. Except as set forth in AMD's Standard Terms and Conditions of Sale, AMD assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

AMD's products are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or in any other application in which the failure of AMD's product could create a situation where personal injury, death, or severe property or environmental damage may occur. AMD reserves the right to discontinue or make changes to its products at any time without notice.

Copyright and Trademarks

© 2012 Advanced Micro Devices, Inc. All rights reserved. AMD, the AMD Arrow logo, ATI, the ATI logo, Radeon, FireStream, and combinations thereof are trademarks of Advanced Micro Devices, Inc. OpenCL and the OpenCL logo are trademarks of Apple Inc. used by permission by Khronos. Other names are for informational purposes only and may be trademarks of their respective owners.