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Cyclone V SoC KFB
How to Set Up

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Pin 1 of GPIO_0 and GPIO_1

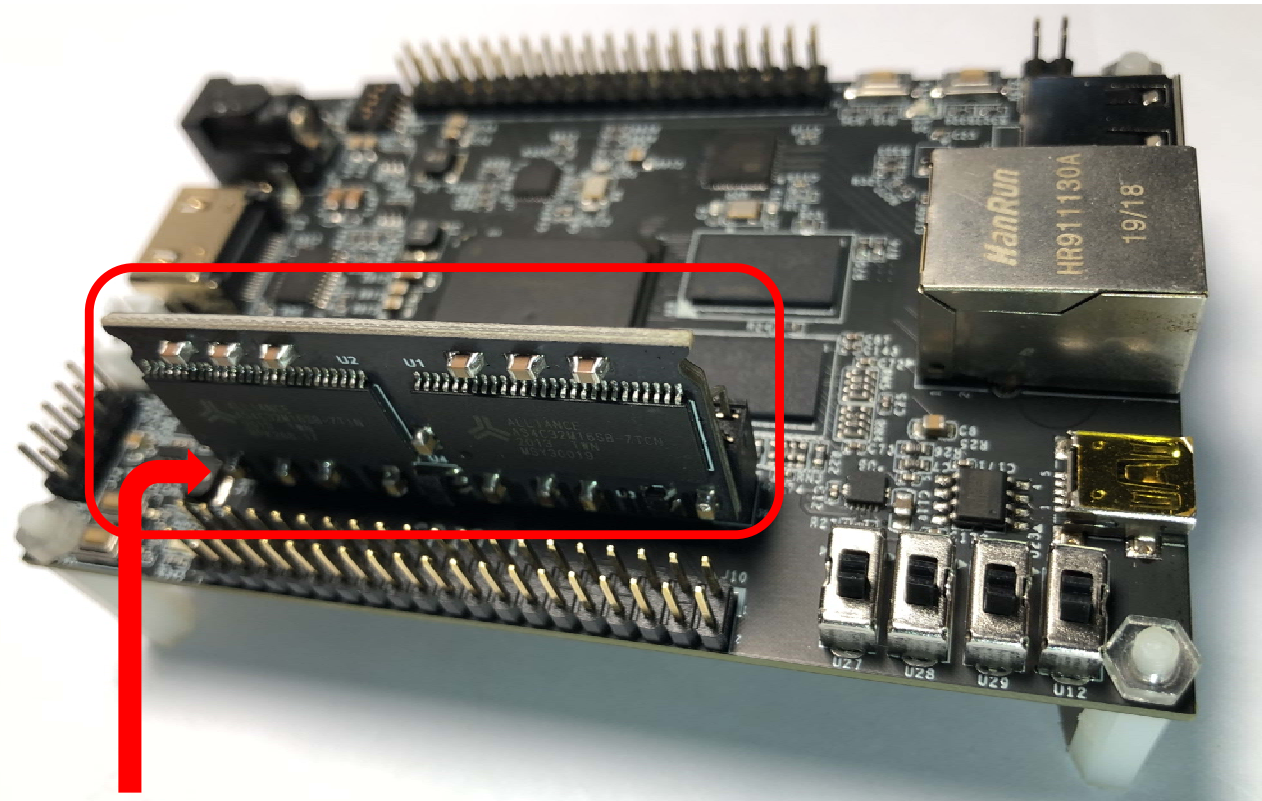GPIO_0 Pin 1

GPIO_1 Pin 1

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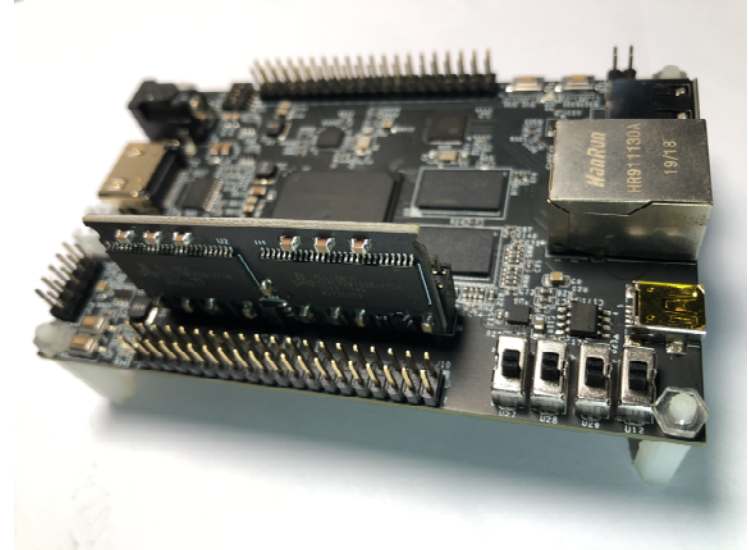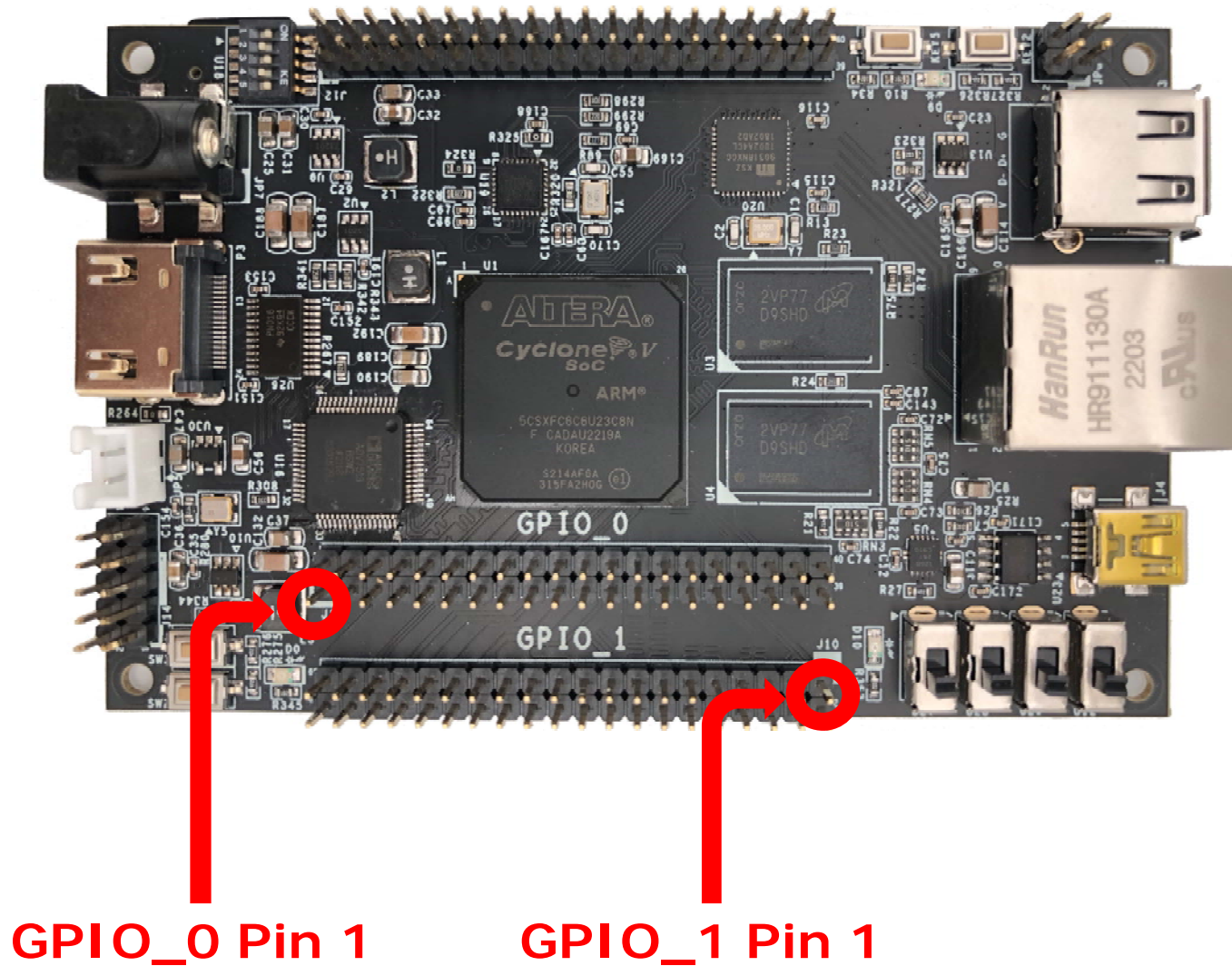
Set Up with Single 128M SDRAM Module

Example Setup:

Use V3.0 128M SDRAM module.

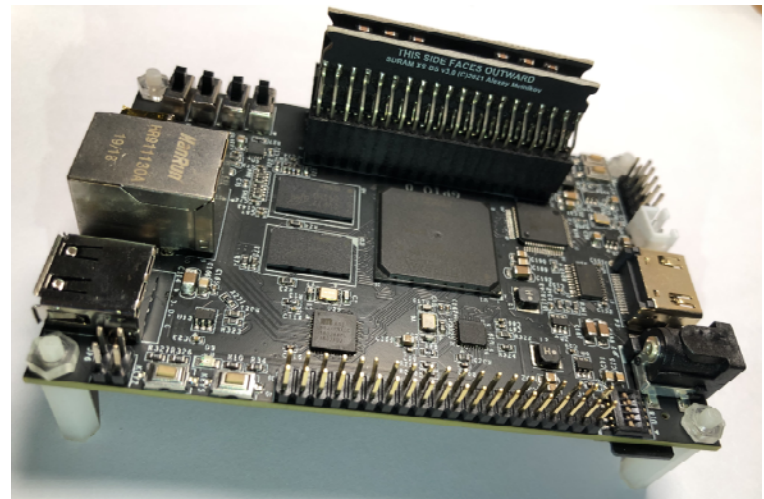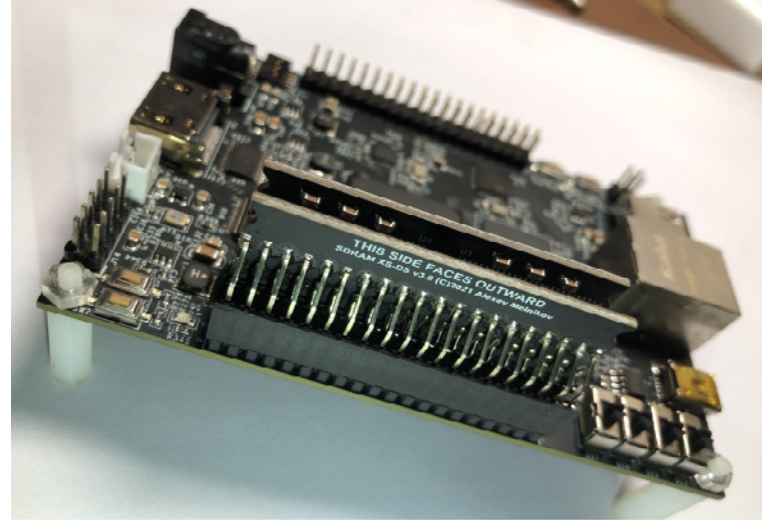**Insert SDRAM module into GPIO_0 header.**

Pin 1

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Set Up with Dual 128M SDRAM Modules

Example Setup:

Use two V3.0 128M SDRAM modules.

Insert SDRAM modules into GPIO_0 and GPIO_1 headers.

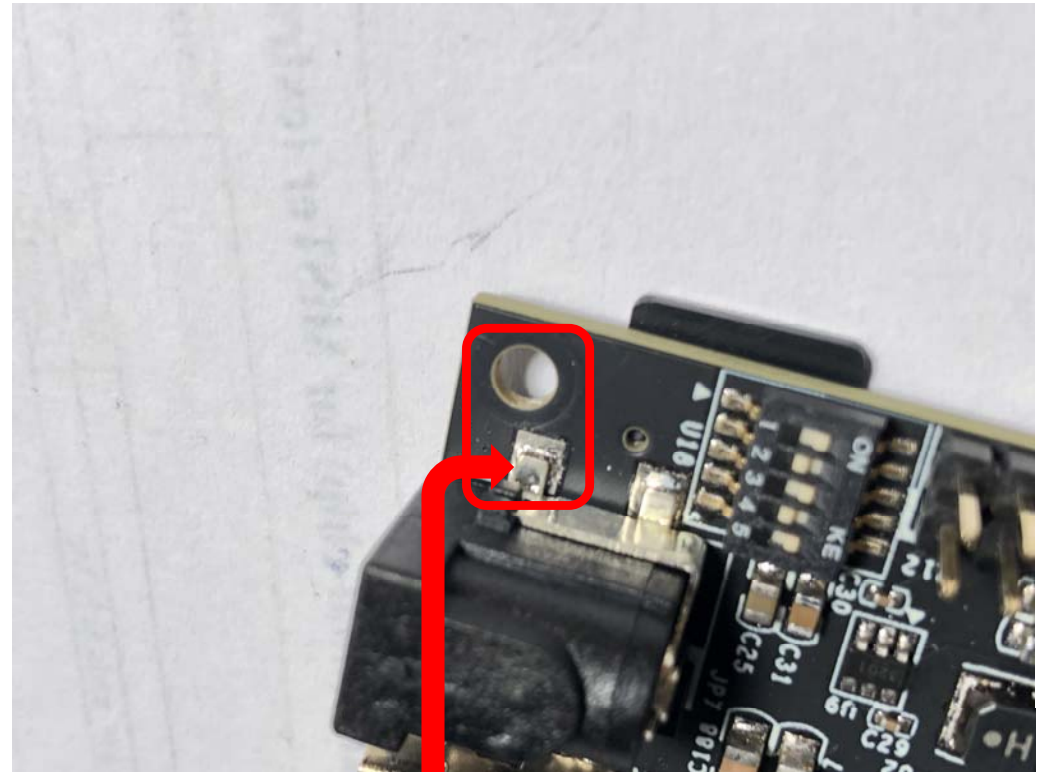**Make sure these two SDRAM modules electrically isolated!**

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!!!!! Limitation !!!!!

The power header +5V pin is too close to the mounting hole!
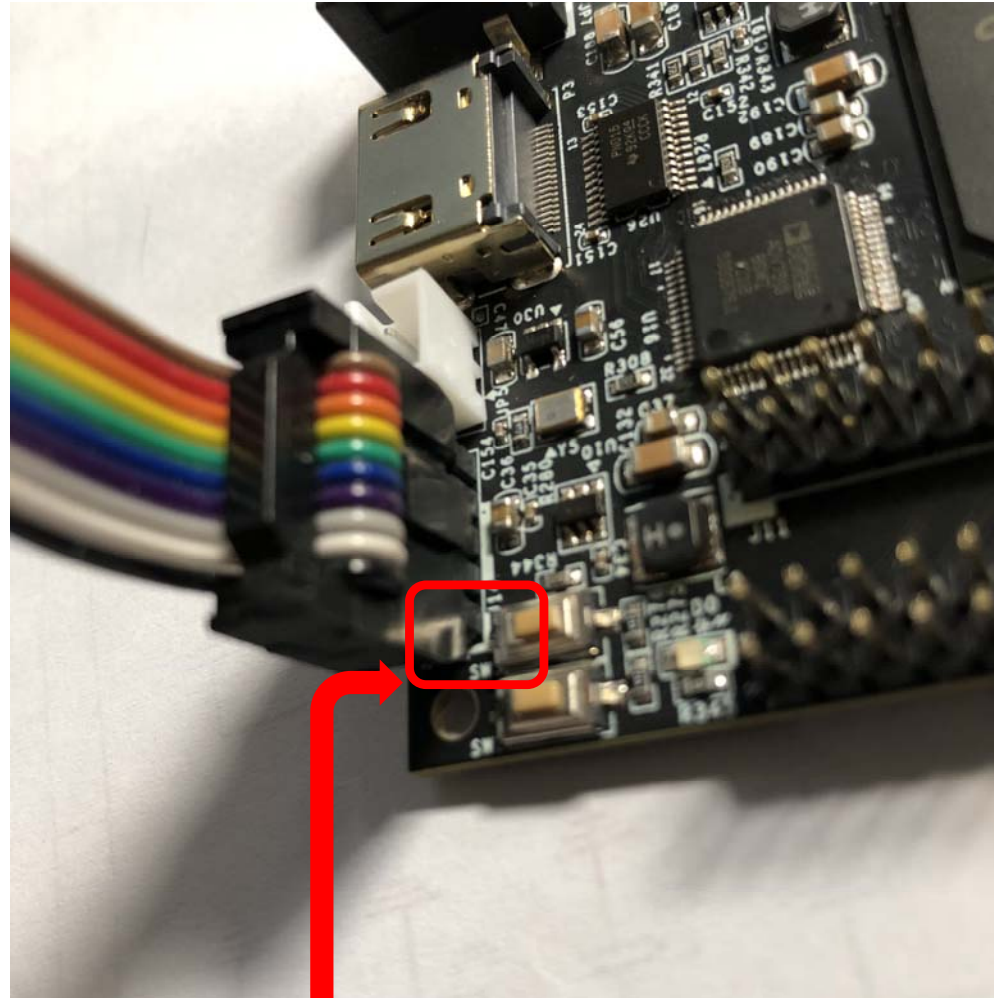
To avoid short circuit, metal screw is not recommended !

！！！注意！！！

电源+5V引脚太靠近固定孔！
为保证电源不被短路，不使用金属固定柱或者金属螺丝！

+5V Pin

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JTAG Connection

Pin 1

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