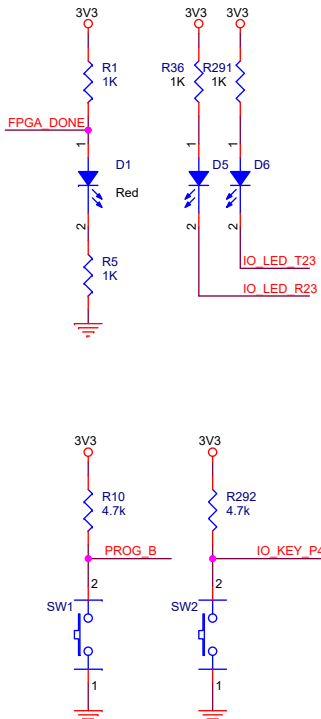
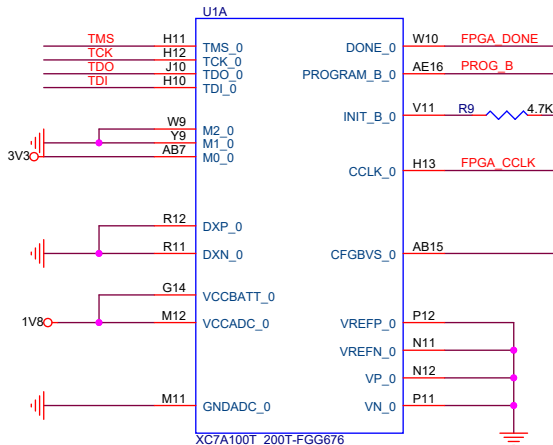
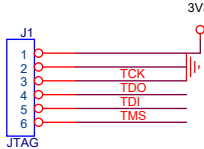
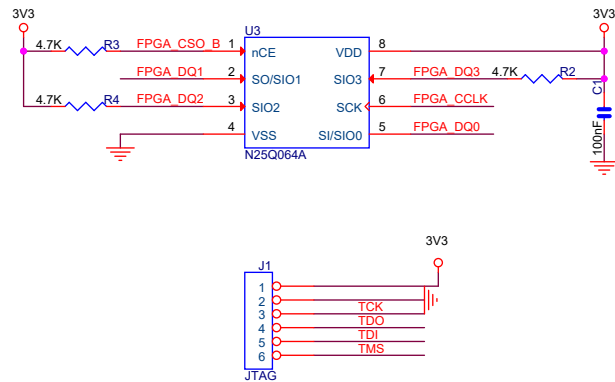
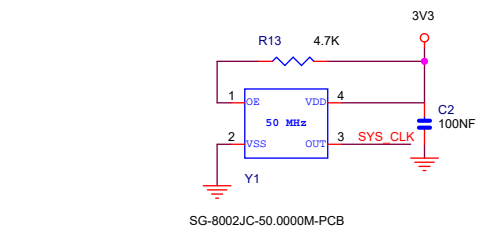
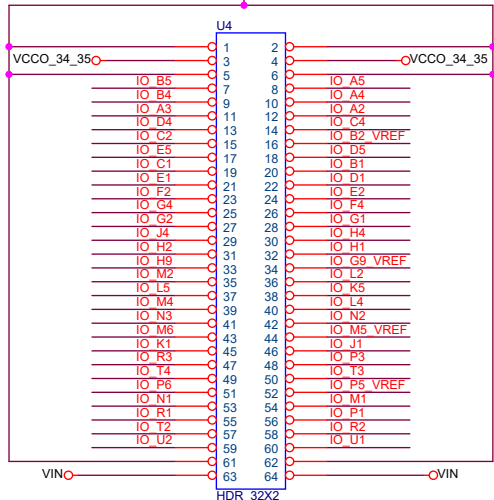
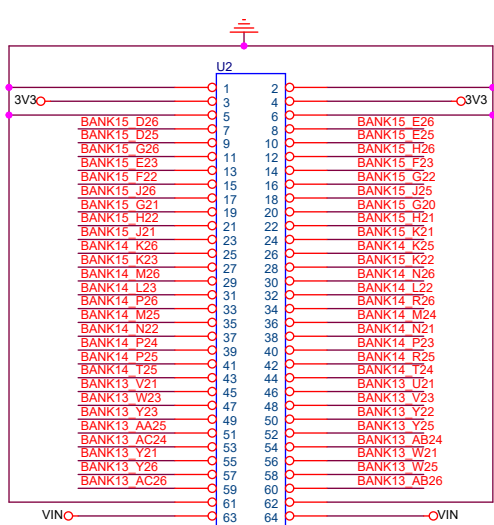
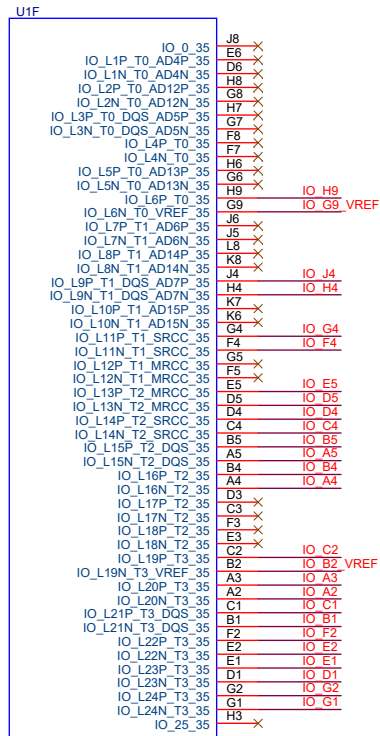
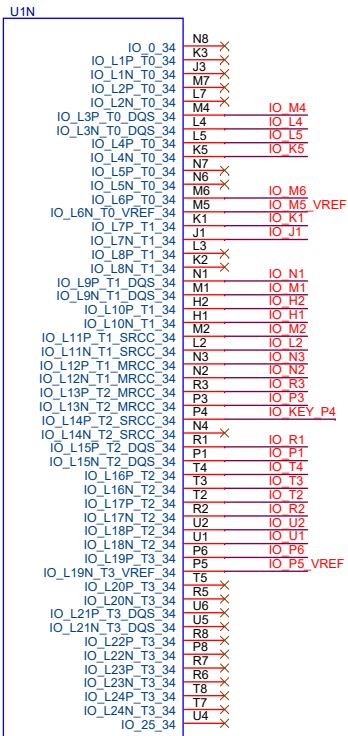
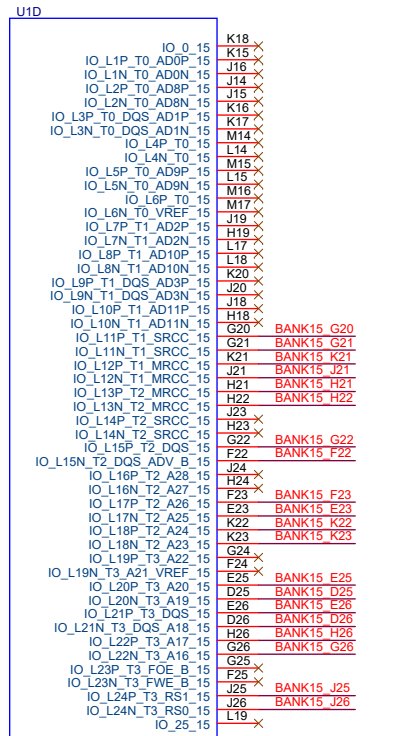
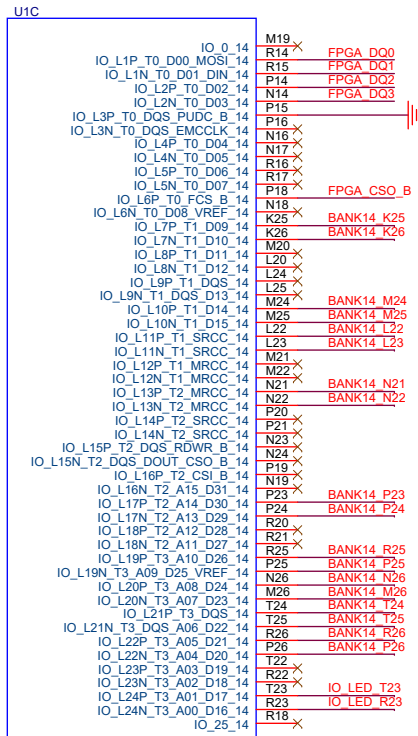
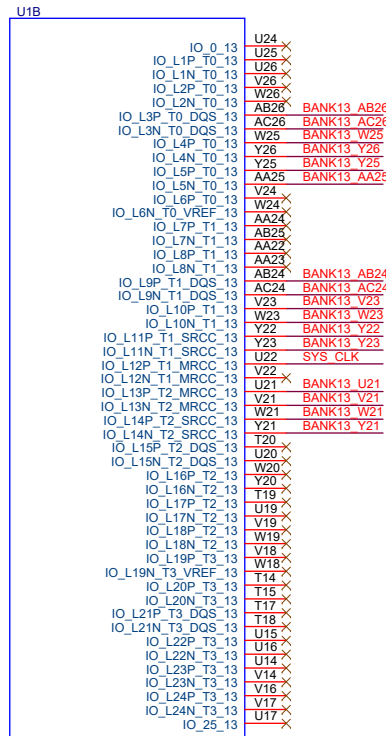


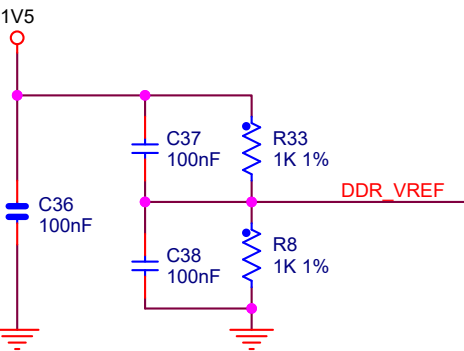
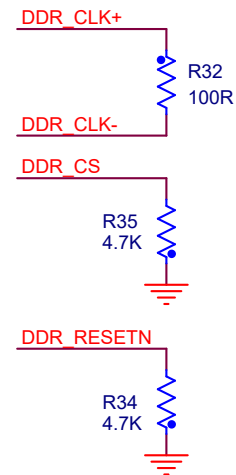
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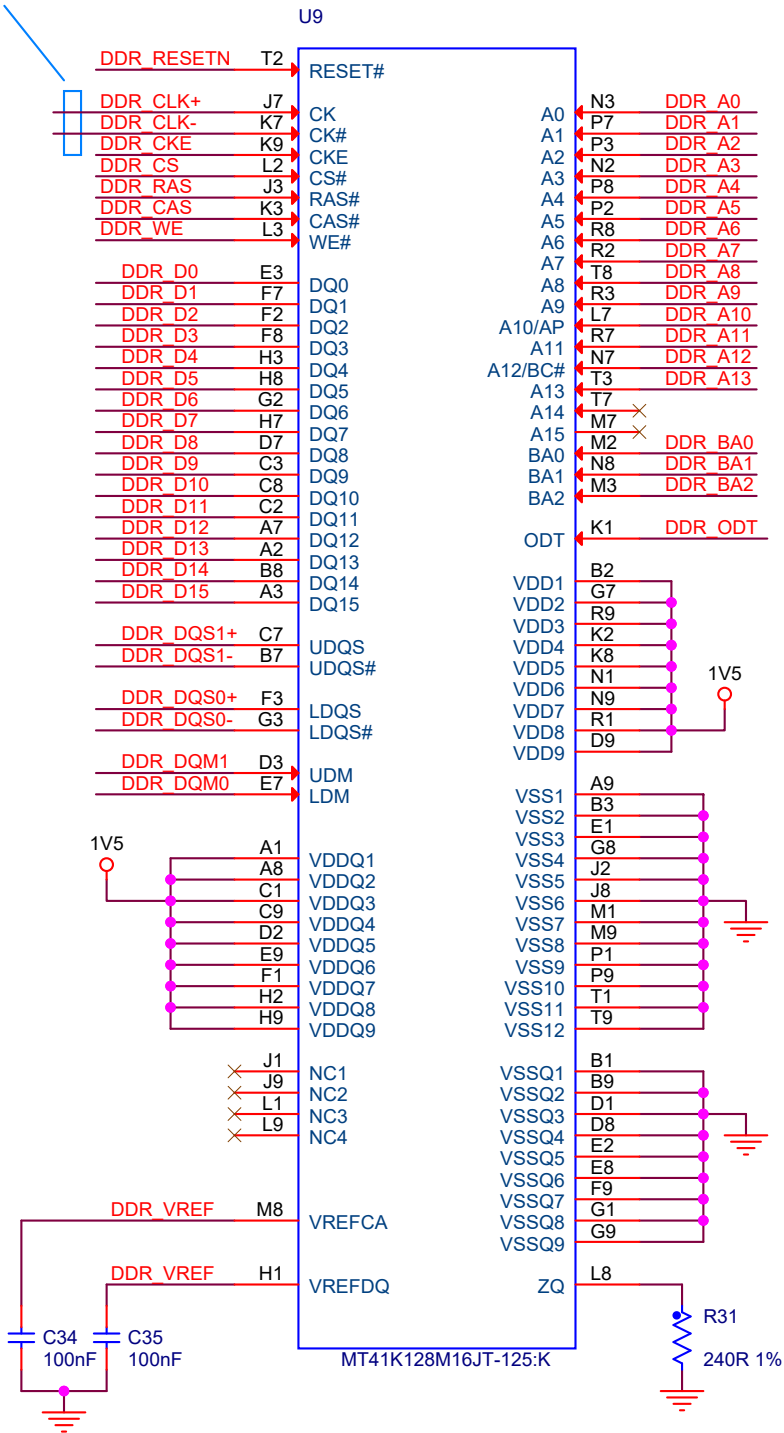
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IO_L2N_T0_16	F17	DDR A2
IO_L3P_T0_DQS_16	F18	DDR CLK+
IO_L3N_T0_DQS_16	F19	DDR CLK-
IO_L4P_T0_16	G15	DDR A13
IO_L4N_T0_16	F15	DDR A9
IO_L5P_T0_16	G19	DDR ODT
IO_L5N_T0_16	F20	DDR A10
IO_L6P_T0_16	G16	DDR A4
IO_L6N_T0_VREF_16	C17	DDR A3
IO_L7P_T1_16	B17	DDR BA0
IO_L7N_T1_16	E16	DDR A7
IO_L8P_T1_16	D16	DDR A5
IO_L8N_T1_16	A17	DDR BA2
IO_L9P_T1_DQS_16	A18	DDR WE
IO_L9N_T1_DQS_16	B19	DDR CAS
IO_L10P_T1_16	A19	DDR RAS
IO_L10N_T1_16	E17	DDR A0
IO_L11P_T1_SRCC_16	E18	DDR CKE
IO_L11N_T1_SRCC_16	D18	DDR BA1
IO_L12P_T1_MRCC_16	C18	DDR A12
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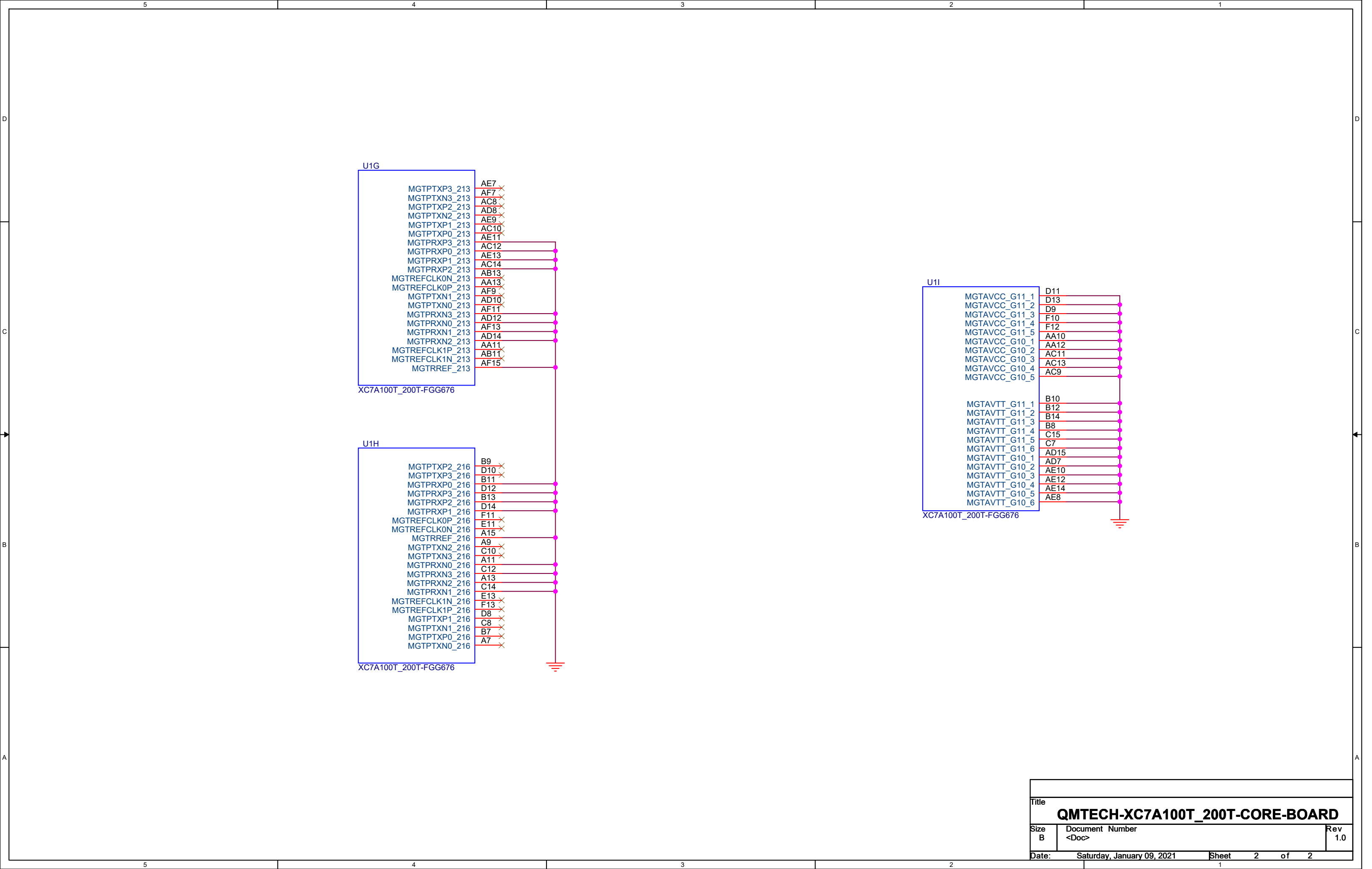
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100 ohms differential trace impedance  
Routing top or bottom



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