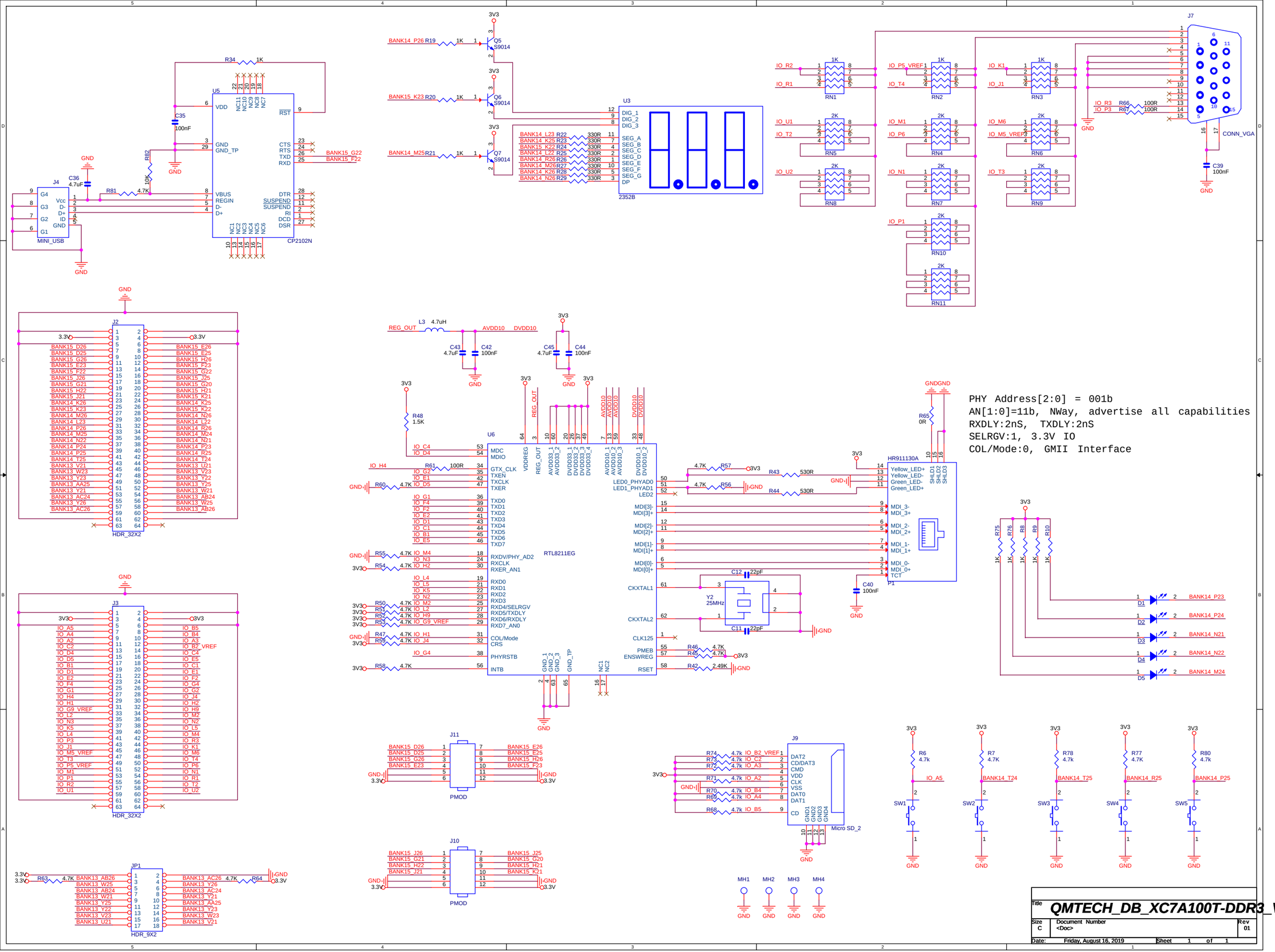
PHY Address[2:0] = 001b
AN[1:0]=11b, NWay, advertise all capabilities
RXDLY:2nS, TXDLY:2nS
SELRGV:1, 3.3V IO
COL/Mode:0, GMII Interface
U5
CP2102N
J4
MINI_USB
U3
2352B
J7
CONN_VGA
J2
HDR_32X2
J3
HDR_32X2
U6
RTL8211EG
HR911130A
P1
J11
PMOD
J10
PMOD
JP1
HDR_9X2
J9
Micro SD_2
MH1
MH2
MH3
MH4
Title
QMTECH_DB_XC7A100T-DDR3_V01
Size
C
Document Number
<Doc>
Rev
01
Date:
Friday, August 16, 2019
Sheet
1
of
1