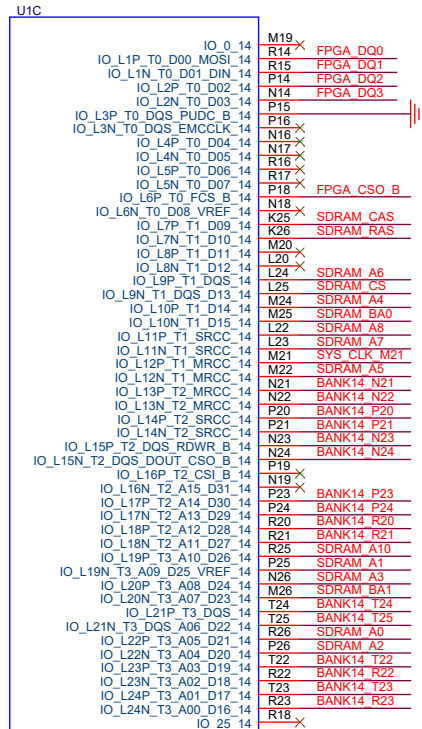
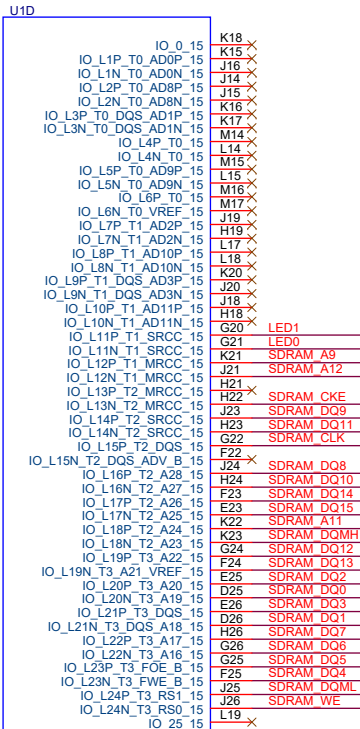


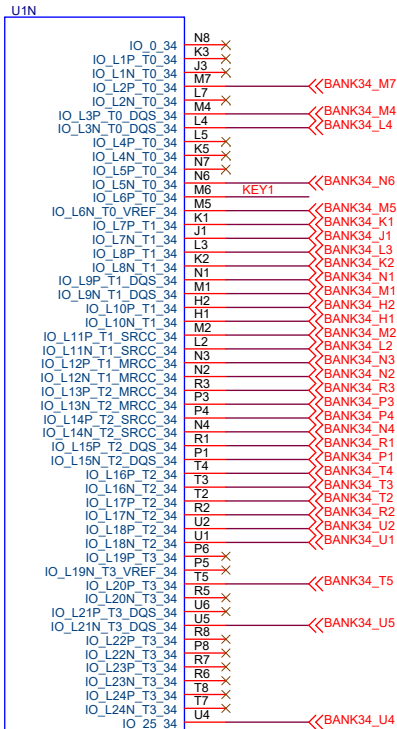
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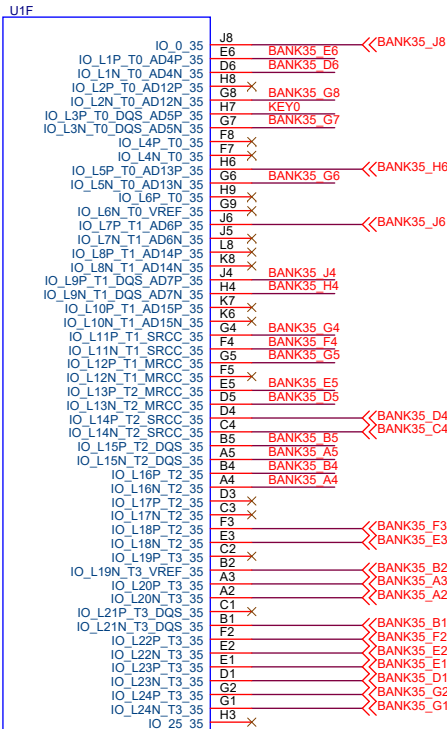
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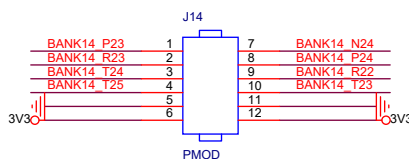
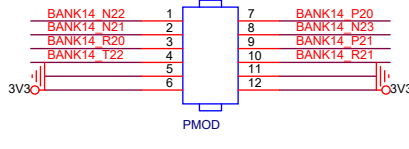
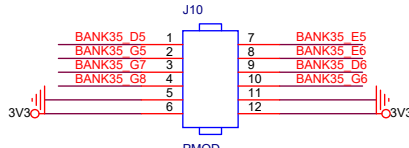
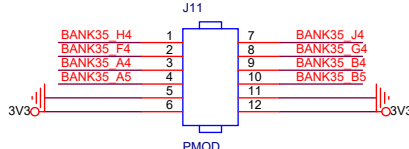
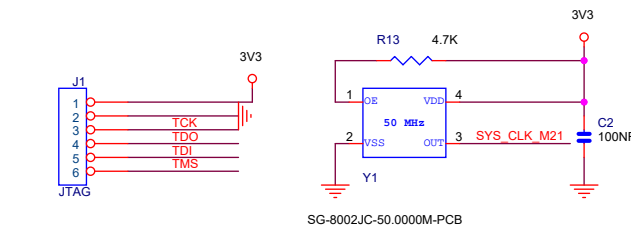
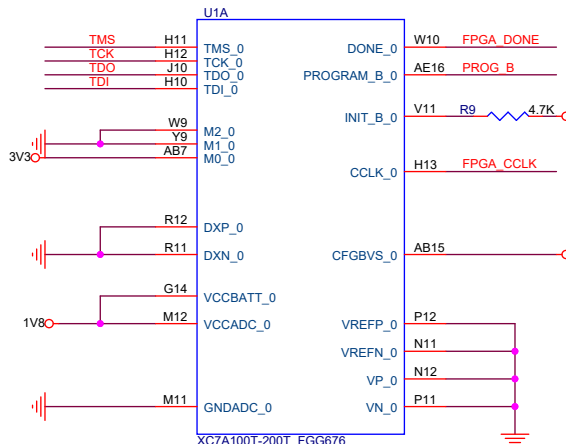
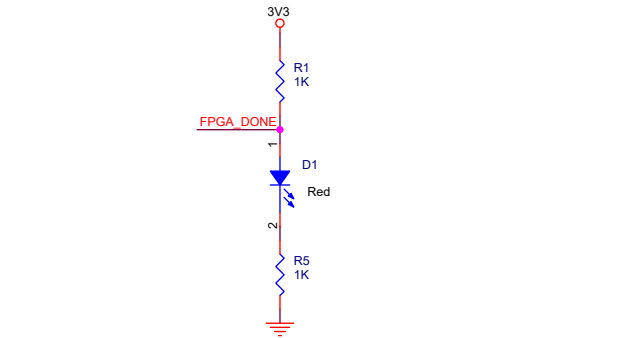
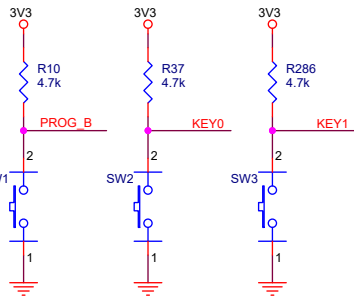
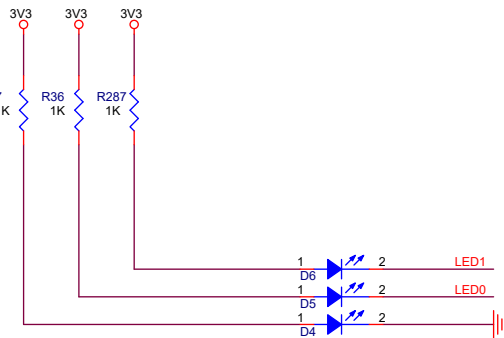
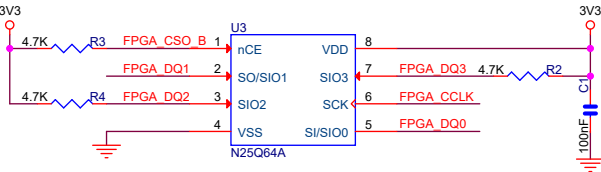
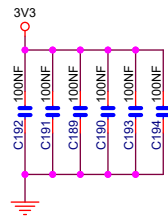
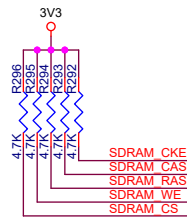
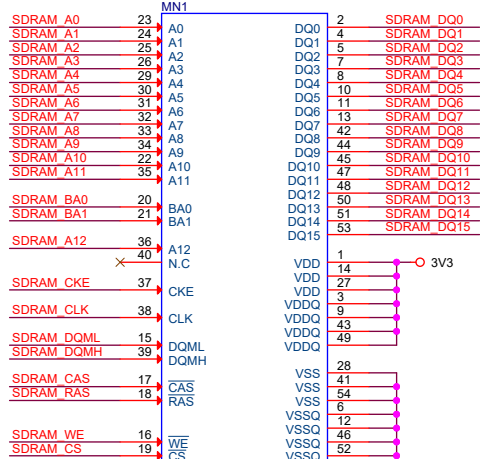
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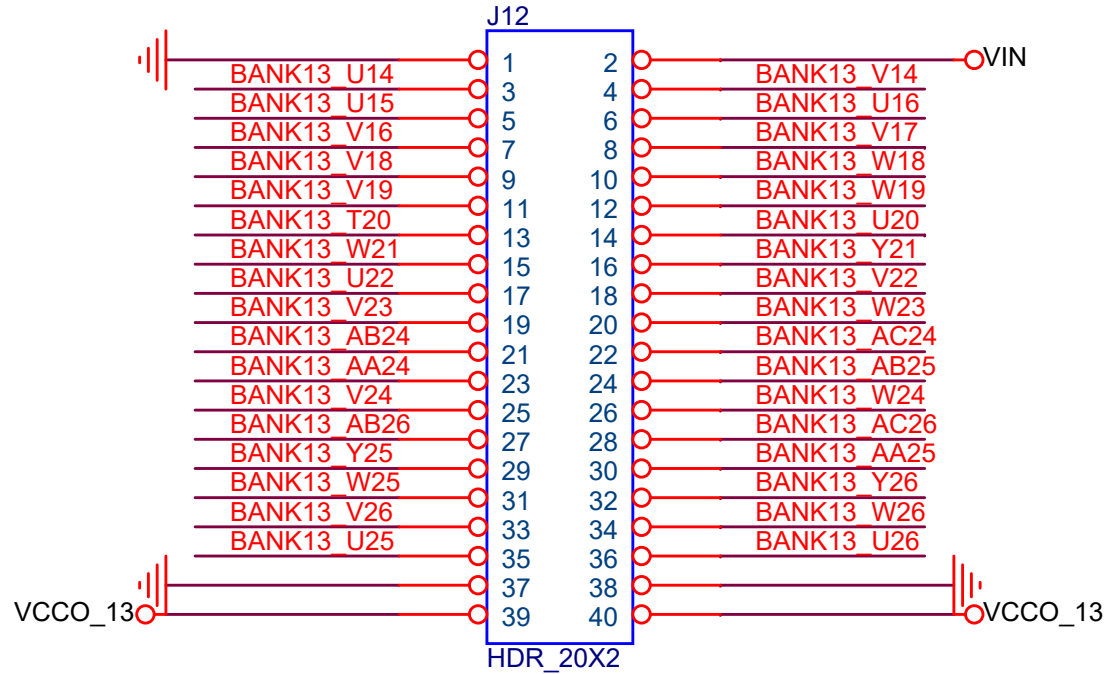


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U1B

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IO_L1N_T0_13	U26	BANK13 U26
IO_L2P_T0_13	V26	BANK13 V26
IO_L2N_T0_13	W26	BANK13 W26
IO_L3P_T0_DQS_13	AB26	BANK13 AB26
IO_L3N_T0_DQS_13	AC26	BANK13 AC26
IO_L4P_T0_13	W25	BANK13 W25
IO_L4N_T0_13	Y26	BANK13 Y26
IO_L5P_T0_13	Y25	BANK13 Y25
IO_L5N_T0_13	AA25	BANK13 AA25
IO_L6P_T0_13	V24	BANK13 V24
IO_L6N_T0_VREF_13	W24	BANK13 W24
IO_L7P_T1_13	AA24	BANK13 AA24
IO_L7N_T1_13	AB25	BANK13 AB25
IO_L8P_T1_13	AA22	
IO_L8N_T1_13	AA23	
IO_L9P_T1_DQS_13	AB24	BANK13 AB24
IO_L9N_T1_DQS_13	AC24	BANK13 AC24
IO_L10P_T1_13	V23	BANK13 V23
IO_L10N_T1_13	W23	BANK13 W23
IO_L11P_T1_SRCC_13	Y22	
IO_L11N_T1_SRCC_13	Y23	
IO_L12P_T1_MRCC_13	U22	BANK13 U22
IO_L12N_T1_MRCC_13	V22	BANK13 V22
IO_L13P_T2_MRCC_13	U21	
IO_L13N_T2_MRCC_13	V21	
IO_L14P_T2_SRCC_13	W21	BANK13 W21
IO_L14N_T2_SRCC_13	Y21	BANK13 Y21
IO_L15P_T2_DQS_13	T20	BANK13 T20
IO_L15N_T2_DQS_13	U20	BANK13 U20
IO_L16P_T2_13	W20	
IO_L16N_T2_13	Y20	
IO_L17P_T2_13	T19	
IO_L17N_T2_13	U19	
IO_L18P_T2_13	V19	BANK13 V19
IO_L18N_T2_13	W19	BANK13 W19
IO_L19P_T3_13	V18	BANK13 V18
IO_L19N_T3_VREF_13	W18	BANK13 W18
IO_L20P_T3_13	T14	
IO_L20N_T3_13	T15	
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IO_L21N_T3_DQS_13	T18	
IO_L22P_T3_13	U15	BANK13 U15
IO_L22N_T3_13	U16	BANK13 U16
IO_L23P_T3_13	U14	BANK13 U14
IO_L23N_T3_13	V14	BANK13 V14
IO_L24P_T3_13	V16	BANK13 V16
IO_L24N_T3_13	V17	BANK13 V17
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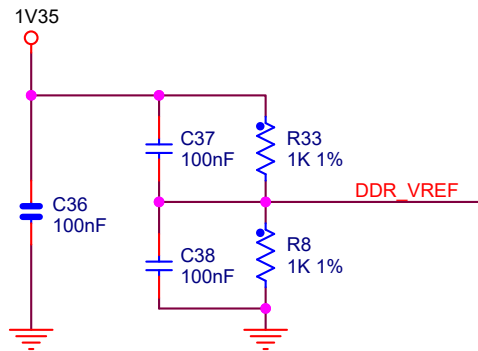
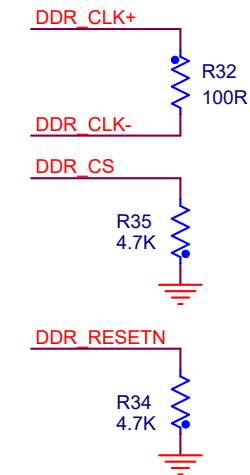
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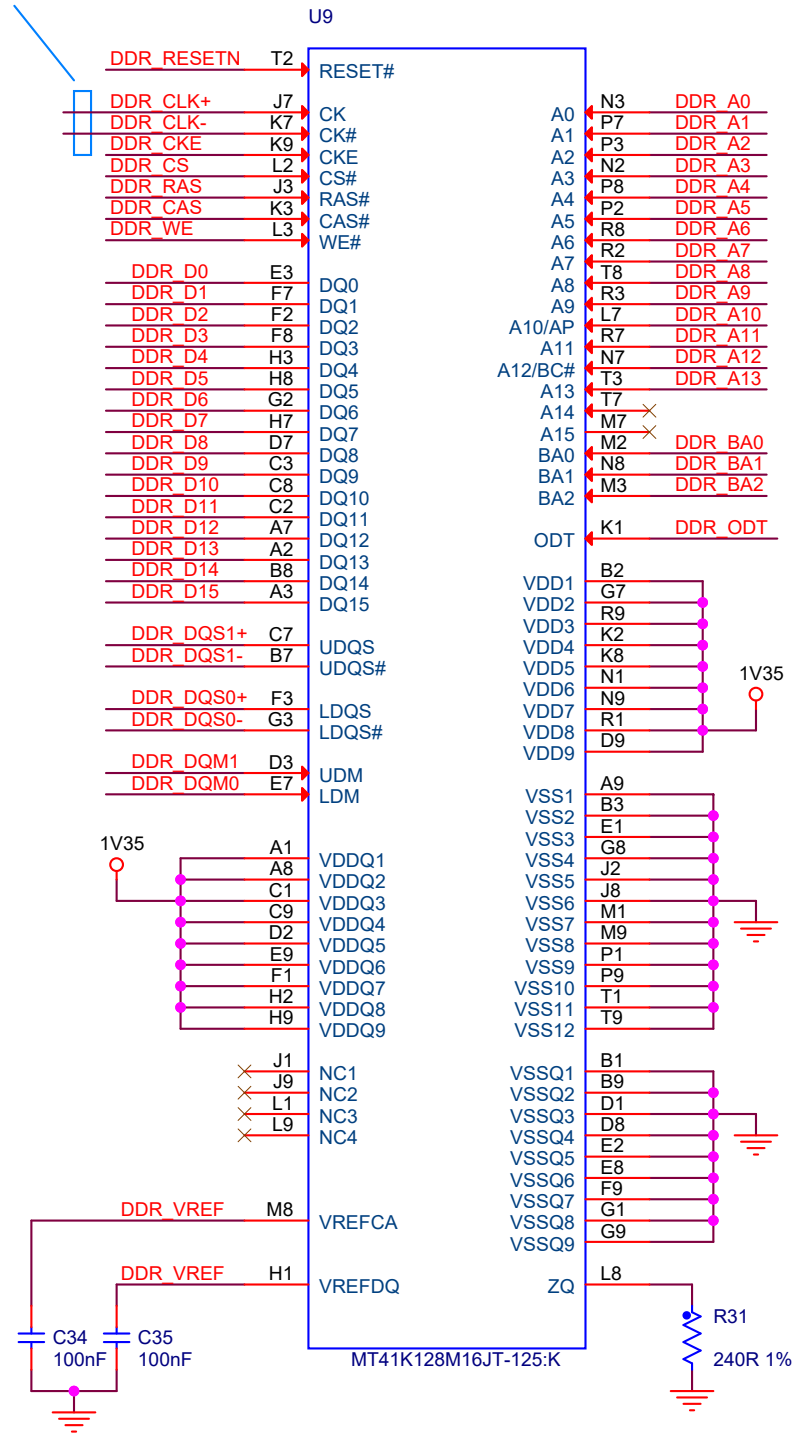
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IO_L1N_T0_16	H15	DDR A11
IO_L2P_T0_16	G17	DDR A1
IO_L2N_T0_16	F17	DDR A2
IO_L3P_T0_DQS_16	F18	DDR CLK+
IO_L3N_T0_DQS_16	F19	DDR CLK-
IO_L4P_T0_16	G15	DDR A13
IO_L4N_T0_16	F15	DDR A9
IO_L5P_T0_16	G19	DDR ODT
IO_L5N_T0_16	F20	DDR A10
IO_L6P_T0_16	G16	DDR A4
IO_L6N_T0_VREF_16	C17	DDR A3
IO_L7P_T1_16	B17	DDR BA0
IO_L7N_T1_16	E16	DDR A7
IO_L8P_T1_16	D16	DDR A5
IO_L8N_T1_16	A17	DDR BA2
IO_L9P_T1_DQS_16	A18	DDR WE
IO_L9N_T1_DQS_16	B19	DDR CAS
IO_L10P_T1_16	A19	DDR RAS
IO_L10N_T1_16	E17	DDR A0
IO_L11P_T1_SRCC_16	E18	DDR CKE
IO_L11N_T1_SRCC_16	D18	DDR BA1
IO_L12P_T1_MRCC_16	C18	DDR A12
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IO_L13P_T2_MRCC_16	C19	DDR D6
IO_L13N_T2_MRCC_16	E20	DDR D5
IO_L14P_T2_SRCC_16	D20	DDR D7
IO_L14N_T2_SRCC_16	B20	DDR DQS0+
IO_L15P_T2_DQS_16	A20	DDR DQS0-
IO_L15N_T2_DQS_16	C21	DDR D1
IO_L16P_T2_16	B21	DDR D3
IO_L16N_T2_16	B22	DDR D2
IO_L17P_T2_16	A22	DDR DQM0
IO_L17N_T2_16	E21	
IO_L18P_T2_16	D21	DDR D0
IO_L18N_T2_16	C22	DDR DQM1
IO_L19P_T3_16	C23	DDR D8
IO_L19N_T3_VREF_16	B25	DDR D11
IO_L20P_T3_16	A25	DDR D14
IO_L20N_T3_16	A23	DDR DQS1+
IO_L21P_T3_DQS_16	A24	DDR DQS1-
IO_L21N_T3_DQS_16	C26	DDR D13
IO_L22P_T3_16	B26	DDR D15
IO_L22N_T3_16	C24	DDR D12
IO_L23P_T3_16	B24	DDR D10
IO_L23N_T3_16	D23	DDR D9
IO_L24P_T3_16	D24	
IO_L24N_T3_16	E22	
IO_25_16		

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100 ohms differential trace impedance
Routing top or bottom



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