Bits of Architecture

Introduction to Instructions

Computers Understand Instructions

Back to Basics

- **Instructions** are the language of processors
 - **Instruction Sets** are the vocabulary
- There are many instruction sets out there
 - **RISC-V**, x86, MIPS, Power
- Stored-Program Concept
 - Instructions and data can be stored in memory and are easy to change

High-Level Languages vs Instructions

How Assembly and High-Level Code Differs

- High-level languages often allow us to be more expressive
 - std::sort
- Instructions are (generally) primitive operations
 - add, multiply, divide, etc.
- Instructions use **registers**



a = b + c + d + e;

Assembly

add a, b, c add a, a, d add a, a, e

Classes of Instructions

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- Arithmetic
 - E.g., Add/Subtract
- Data Transfer
 - E.g., Load/Store
- Logical
 - E.g., And/Xor
- Shift
 - E.g., Shift Logical Left/Right
- Control Flow
 - E.g., Conditional/Unconditional Branches

add x5, x6, x7

lw x5, 40(x6)

and x5, x6, c7

sll x5, x6, x7

beq x5, x6, 100

Design Principles

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- 1. Simplicity favors regularity
- 2. Smaller is Faster
- 3. Good design demands good compromises