Process Management in xv6

We begin understanding xv6 process management by looking at the proc data structure (line 2353), that corresponds to the PCB. Especially note the fields corresponding to the kernel stack pointer, the trap frame (which stores context before handling a trap), and the context structure (which stores context during a context switch). We will now walk through some of the paths in the code: interrupt handling (sheets 32–34), new process creation using fork (sheets 24–25), and the workings of the first init and shell processes (sheets 24–25, sheets 83–88).

Clique o link https://www.cse.iitb.ac.in/~mythili/teaching/cs347_autumn2016/notes/03-xv6-process.pdf para abrir o recurso

Memory Management

Memory Management

Clique o link https://www.cse.iitb.ac.in/~mythili/teaching/cs347_autumn2016/notes/07-memory.pdf para abrir o recurso

Memory Management in xv6

Memory Management in xv6

Clique o link https://www.cse.iitb.ac.in/~mythili/teaching/cs347_autumn2016/notes/08-xv6-memory.pdf para abrir o recurso

https://www.youtube.com/watch?v=2OobPx246zg