Time

clk=1

counter[3:0]=5 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3 4 5 6 7 8 9 A 0 1 2 3

InstrOut[31:0]=14450003 38050006 38020002 38010002 00411020 14450003 00411020 14450003 00A11822 +

ALUOp[2:0]=001 010 000 001 000 001

register1[31:0]=2 XXX 2

register2[31:0]=4 XXX 2 4 6

register3[31:0]=XXX XXX 4

register5[31:0]=6 XXX 6

RegWrite=0

Branch=1

zero=0

1 us   2 us