

Main Circuit

CLIENT
FILE NO.
PROJECT

REF. DWG.

SHEET

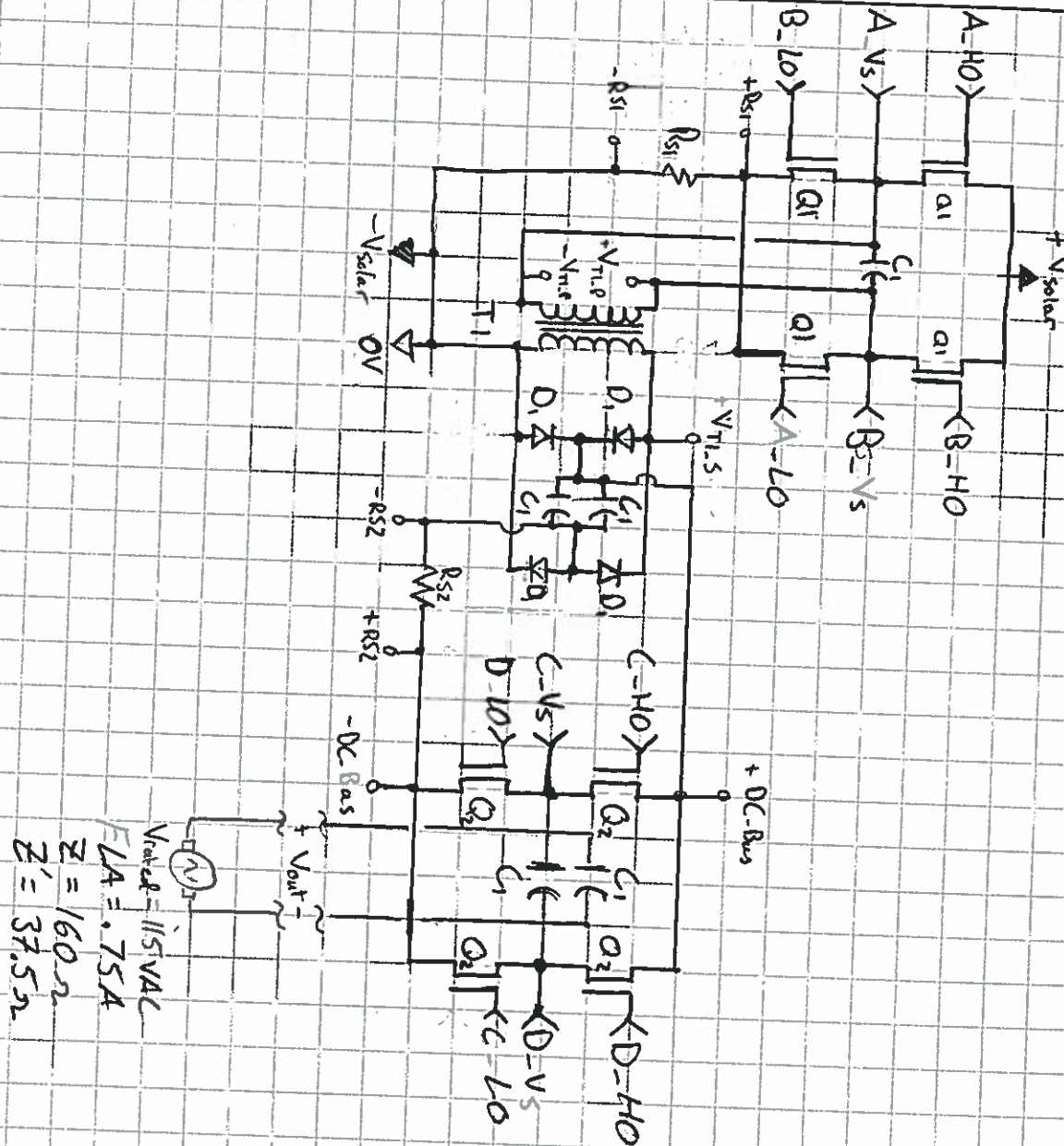
BY

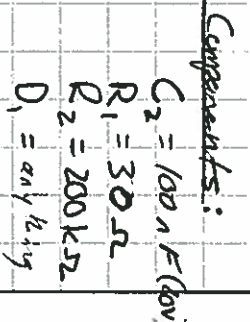
CHD

OF

DATE

DATE





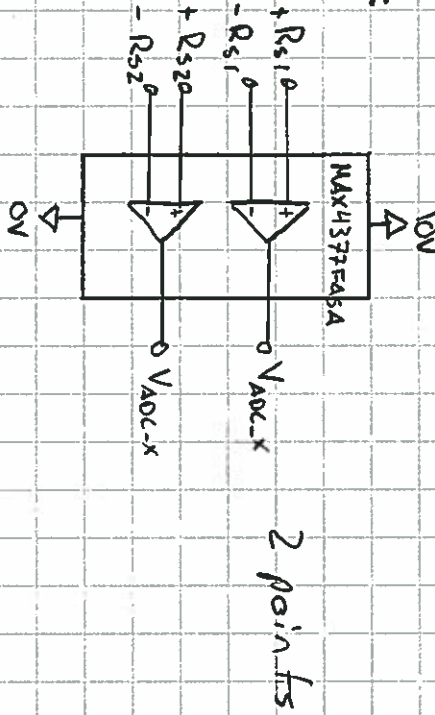
A-In³ B-In cannot be li simultaneously or short circuit will result. Same for C-In³ D-In. Therefore the following output logic is required:

Voltage, Current & Temp Sensing:

V_{sense} : All $\pm 1\%$ resistors

Point	+V _{sense}	$\pm V_{TLP}$	+V _{TLP}	+V _{DC-Bus}	$\pm V_{out}$	7 points
R ₁	1M Ω	1M Ω	1M Ω	1M Ω	1M Ω	
R ₂	50k Ω	50k Ω	15k Ω	15k Ω	15k Ω	
	1/20	1/20	1/66.7	1/66.7	1/66.7	

I_{sense} :



Temp : 3.3V

27.5k Ω ($\pm 0.1\%$)

4 points

