

# 1. Description

## 1.1. Project

Project Name	FreeRTOS_L2_F4
Board Name	custom
Generated with:	STM32CubeMX 6.6.1
Date	05/17/2023

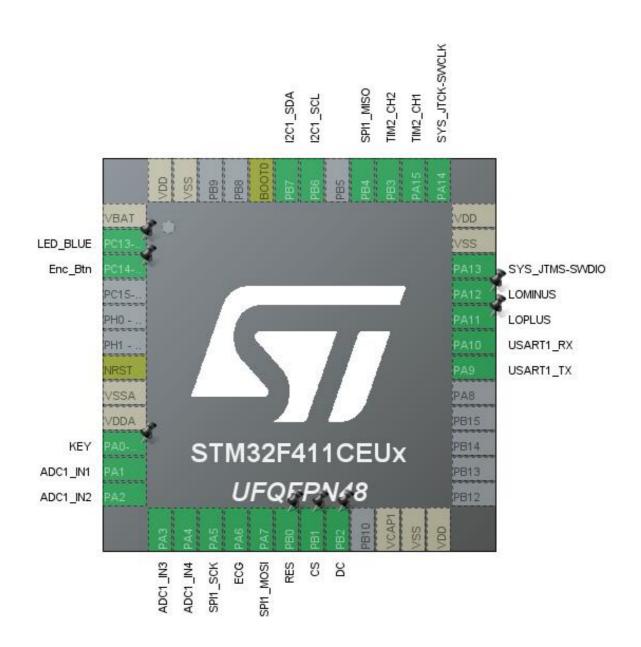
## 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F411
MCU name	STM32F411CEUx
MCU Package	UFQFPN48
MCU Pin number	48

## 1.3. Core(s) information

Core(s)	Arm Cortex-M4

# 2. Pinout Configuration

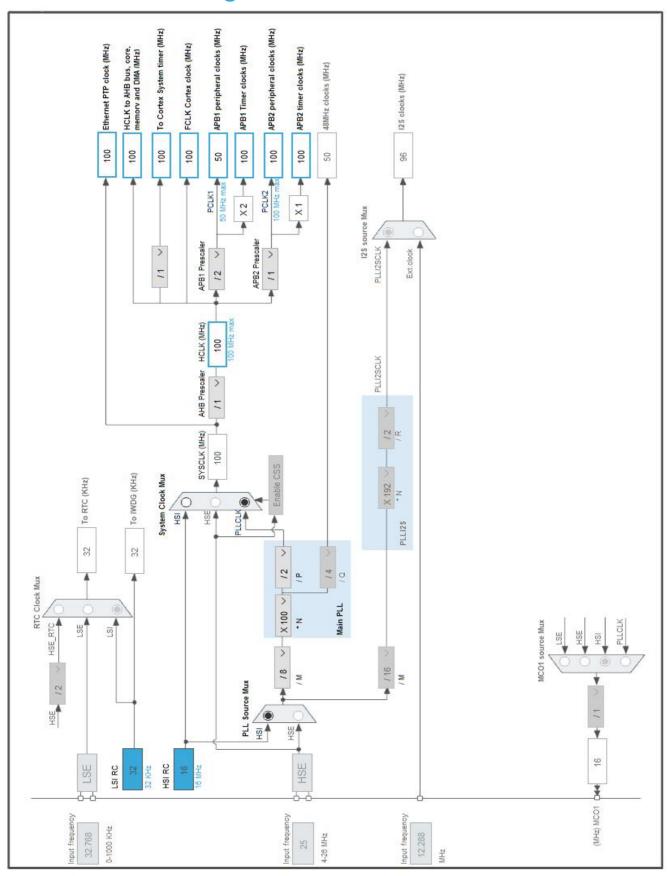


# 3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
UFQFPN48	(function after		Function(s)	
	reset)		,	
1	VBAT	Power		
2	PC13-ANTI_TAMP *	I/O	GPIO_Output	LED_BLUE
3	PC14-OSC32_IN *	I/O	GPIO_Input	Enc_Btn
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0-WKUP	I/O	GPIO_EXTI0	KEY
11	PA1	I/O	ADC1_IN1	
12	PA2	I/O	ADC1_IN2	
13	PA3	I/O	ADC1_IN3	
14	PA4	I/O	ADC1_IN4	
15	PA5	I/O	SPI1_SCK	
16	PA6	I/O	ADC1_IN6	ECG
17	PA7	I/O	SPI1_MOSI	
18	PB0 *	I/O	GPIO_Output	RES
19	PB1 *	I/O	GPIO_Output	CS
20	PB2 *	I/O	GPIO_Output	DC
22	VCAP1	Power		
23	VSS	Power		
24	VDD	Power		
30	PA9	I/O	USART1_TX	
31	PA10	I/O	USART1_RX	
32	PA11 *	I/O	GPIO_Input	LOPLUS
33	PA12 *	I/O	GPIO_Input	LOMINUS
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
38	PA15	I/O	TIM2_CH1	
39	PB3	I/O	TIM2_CH2	
40	PB4	I/O	SPI1_MISO	
42	PB6	I/O	I2C1_SCL	
43	PB7	I/O	I2C1_SDA	
44	ВООТ0	Boot		
47	VSS	Power		
48	VDD	Power		

* The pin is affected with an I/O function	

# 4. Clock Tree Configuration



# 5. Software Project

## 5.1. Project Settings

Name	Value
Project Name	FreeRTOS_L2_F4
Project Folder	C:\Users\ICG\Documents\STM_CUB\FreeRTOS_L2_F4
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.27.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

## 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name	
1	SystemClock_Config	RCC	
2	MX_GPIO_Init	GPIO	
3	MX_USART1_UART_Init	USART1	
4	MX_TIM2_Init	TIM2	
5	MX_SPI1_Init	SPI1	
6	MX_I2C1_Init	I2C1	
7	MX_ADC1_Init	ADC1	

# 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32F4
Line	STM32F411
мси	STM32F411CEUx
Datasheet	DS10314_Rev6

### 6.2. Parameter Selection

Temperature	25
Vdd	1.7

## 6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

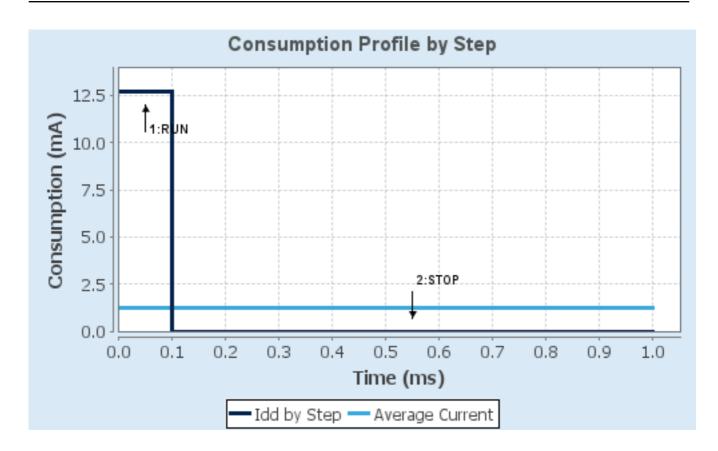
## 6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	1.7	1.7
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	SRAM	n/a
CPU Frequency	100 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator_LPLV Flash-
		PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	12.7 mA	9 μΑ
Duration	0.1 ms	0.9 ms
DMIPS	125.0	0.0
Ta Max	104.31	105
Category	In DS Table	In DS Table

## 6.5. Results

Sequence Time	1 ms	Average Current	1.28 mA
Battery Life	3 months, 19	Average DMIPS	125.0 DMIPS
	days, 6 hours		

## 6.6. Chart



# 7. Peripherals and Middlewares Configuration

7.1. ADC1 mode: IN1 mode: IN2 mode: IN3 mode: IN4 mode: IN6

7.1.1. Parameter Settings:

ADCs\_Common\_Settings:

Mode Independent mode

ADC\_Settings:

Clock Prescaler PCLK2 divided by 4

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment Right alignment

Scan Conversion Mode Enabled
Continuous Conversion Mode Disabled
Discontinuous Conversion Mode Disabled
DMA Continuous Requests Disabled

End Of Conversion Selection EOC flag at the end of single channel conversion

ADC\_Regular\_ConversionMode:

Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None Rank 1

Channel 6 \*

Sampling Time 3 Cycles

ADC\_Injected\_ConversionMode:

Number Of Conversions 4 \*

External Trigger Source Injected Conversion launched by software

External Trigger Edge None
Injected Conversion Mode None
Injected Rank 1

Channel Channel 1
Sampling Time 3 Cycles
Injected Offset 0
Injected Rank 2 \*

Channel 2 \*

Sampling Time 3 Cycles

Injected Offset 0

Injected Rank 3 \*

Channel 3 \*

Sampling Time 3 Cycles

Injected Offset 0

Injected Rank 4 \*

Channel 4 \*

Sampling Time 3 Cycles

Injected Offset 0

WatchDog:

Enable Analog WatchDog Mode false

7.2. I2C1 I2C: I2C

#### 7.2.1. Parameter Settings:

#### **Master Features:**

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

**Slave Features:** 

Clock No Stretch Mode Disabled
Primary Address Length selection 7-bit
Dual Address Acknowledged Disabled
Primary slave address 0

General Call address detection Disabled

#### 7.3. RCC

#### 7.3.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 3 WS (4 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16

TIM Prescaler Selection Disabled
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

#### 7.4. SPI1

### **Mode: Full-Duplex Master**

#### 7.4.1. Parameter Settings:

#### **Basic Parameters:**

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

**Clock Parameters:** 

Prescaler (for Baud Rate) 2

Baud Rate 50.0 MBits/s \*

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

**Advanced Parameters:** 

CRC Calculation Disabled
NSS Signal Type Software

#### 7.5. SYS

**Debug: Serial Wire** 

**Timebase Source: TIM5** 

#### 7.6. TIM2

**Combined Channels: Encoder Mode** 

#### 7.6.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 0

Counter Mode Up

Counter Period (AutoReload Register - 32 bits value) 2 \*

Internal Clock Division (CKD) No Division

auto-reload preload	Disable		
Trigger Output (TRGO) Parameters:			
Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)		
Trigger Event Selection	Reset (UG bit from TIMx_EGR)		
Encoder:			
Encoder Mode	Encoder Mode TI1		
Parameters for Channel 1			
Polarity	Rising Edge		
IC Selection	Direct		
Prescaler Division Ratio	No division		
Input Filter	0		
Parameters for Channel 2			
Polarity	Rising Edge		
IC Selection	Direct		
Prescaler Division Ratio	No division		
Input Filter	0		
7.7. USART1			
Mode: Asynchronous			
7.7.1. Parameter Settings:			
Basic Parameters:			
Baud Rate	115200		
Word Length	8 Bits (including Parity)		
Parity	None		
Stop Bits	1		
Advanced Parameters:			
Data Direction	Transmit Only *		
Over Sampling	16 Samples		
7.8. FREERTOS			
Interface: CMSIS_V2			
7.8.1. Config parameters:			
API:			
FreeRTOS API	CMSIS v2		
Versions:			

FreeRTOS version 10.3.1 CMSIS-RTOS version 2.00

MPU/FPU:

ENABLE\_MPU Disabled
ENABLE\_FPU Disabled

Kernel settings:

USE\_PREEMPTION Enabled

CPU\_CLOCK\_HZ SystemCoreClock

TICK\_RATE\_HZ 1000 MAX\_PRIORITIES 56 MINIMAL\_STACK\_SIZE 128 MAX\_TASK\_NAME\_LEN 16 USE\_16\_BIT\_TICKS Disabled IDLE\_SHOULD\_YIELD Enabled USE\_MUTEXES Enabled USE\_RECURSIVE\_MUTEXES Enabled Enabled USE\_COUNTING\_SEMAPHORES QUEUE\_REGISTRY\_SIZE 8

USE\_APPLICATION\_TASK\_TAG Disabled
ENABLE\_BACKWARD\_COMPATIBILITY Enabled
USE\_PORT\_OPTIMISED\_TASK\_SELECTION Disabled
USE\_TICKLESS\_IDLE Disabled
USE\_TASK\_NOTIFICATIONS Enabled
RECORD\_STACK\_HIGH\_ADDRESS Disabled

Memory management settings:

Memory Allocation Dynamic / Static

TOTAL\_HEAP\_SIZE 15360
Memory Management scheme heap\_4

Hook function related definitions:

USE\_IDLE\_HOOK Disabled
USE\_TICK\_HOOK Disabled
USE\_MALLOC\_FAILED\_HOOK Disabled
USE\_DAEMON\_TASK\_STARTUP\_HOOK Disabled
CHECK\_FOR\_STACK\_OVERFLOW Disabled

Run time and task stats gathering related definitions:

GENERATE\_RUN\_TIME\_STATS Disabled
USE\_TRACE\_FACILITY Enabled
USE\_STATS\_FORMATTING\_FUNCTIONS Disabled

Co-routine related definitions:

USE\_CO\_ROUTINES Disabled MAX\_CO\_ROUTINE\_PRIORITIES 2

Software timer definitions:

USE\_TIMERS Enabled
TIMER\_TASK\_PRIORITY 2
TIMER\_QUEUE\_LENGTH 10
TIMER\_TASK\_STACK\_DEPTH 256

#### Interrupt nesting behaviour configuration:

LIBRARY\_LOWEST\_INTERRUPT\_PRIORITY 15
LIBRARY\_MAX\_SYSCALL\_INTERRUPT\_PRIORITY 5

#### Added with 10.2.1 support:

MESSAGE\_BUFFER\_LENGTH\_TYPE size\_t
USE\_POSIX\_ERRNO Disabled

#### **CMSIS-RTOS V2 flags:**

USE\_OS2\_THREAD\_SUSPEND\_RESUME Enabled
USE\_OS2\_THREAD\_ENUMERATE Enabled
USE\_OS2\_EVENTFLAGS\_FROM\_ISR Enabled
USE\_OS2\_THREAD\_FLAGS Enabled
USE\_OS2\_TIMER Enabled
USE\_OS2\_MUTEX Enabled

#### 7.8.2. Include parameters:

#### Include definitions:

vTaskPrioritySet Enabled uxTaskPriorityGet Enabled Enabled vTaskDelete Disabled vTaskCleanUpResources Enabled vTaskSuspend vTaskDelayUntil Enabled Enabled vTaskDelay Enabled xTaskGetSchedulerState xTaskResumeFromISR Enabled Enabled xQueueGetMutexHolder Disabled xSemaphoreGetMutexHolder Disabled pcTaskGetTaskName Enabled uxTaskGetStackHighWaterMark xTaskGetCurrentTaskHandle Enabled eTaskGetState Enabled Disabled xEventGroupSetBitFromISR xTimerPendFunctionCall Enabled xTaskAbortDelay Disabled Disabled xTaskGetHandle uxTaskGetStackHighWaterMark2 Disabled

## 7.8.3. Advanced settings:

Newlib settings (see parameter description first):

USE\_NEWLIB\_REENTRANT Enabled \*

Project settings (see parameter description first):

Use FW pack heap file Enabled

<sup>\*</sup> User modified value

# 8. System Configuration

## 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA1	ADC1_IN1	Analog mode	No pull-up and no pull-down	n/a	
ADCT	PA2	ADC1_IN1  ADC1_IN2	Analog mode	No pull-up and no pull-down	n/a	
	PA3	ADC1_IN2  ADC1_IN3	Analog mode	No pull-up and no pull-down	n/a	
	PA4	ADC1_IN3  ADC1_IN4			n/a	
	PA6		Analog mode  Analog mode	No pull-up and no pull-down	n/a	ECG
I2C1	PB6	ADC1_IN6	Alternate Function Open Drain	No pull-up and no pull-down  No pull-up and no pull-down	Very High	
	PB7	I2C1_SDA	Alternate Function Open Drain	No pull-up and no pull-down	Very High	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PB4	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
TIM2	PA15	TIM2_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PB3	TIM2_CH2	Alternate Function Push Pull	No pull-up and no pull-down	Low	
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PC13- ANTI_TAMP	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED_BLUE
	PC14- OSC32_IN	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	Enc_Btn
	PA0-WKUP	GPIO_EXTI0	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	KEY
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RES
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	CS
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DC
	PA11	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	LOPLUS
	PA12	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	LOMINUS

8.2.	DMA	confid	uration
V.L.		OUTTING	ui atioii

nothing configured in DMA service

## 8.3. NVIC configuration

# 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	15	0
System tick timer	true	15	0
EXTI line0 interrupt	true	5	0
TIM5 global interrupt	true	15	0
PVD interrupt through EXTI line 16		unused	
Flash global interrupt		unused	
RCC global interrupt		unused	
ADC1 global interrupt		unused	
TIM2 global interrupt		unused	
I2C1 event interrupt		unused	
I2C1 error interrupt		unused	
SPI1 global interrupt	unused		
USART1 global interrupt	unused		
FPU global interrupt		unused	

## 8.3.2. NVIC Code generation

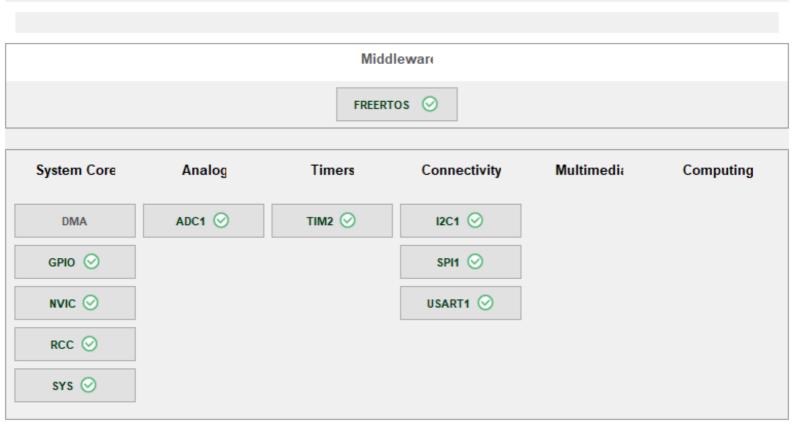
Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
'		tide	
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	false	false
Debug monitor	false	true	false
Pendable request for system service	false	false	false
System tick timer	false	false	true
EXTI line0 interrupt	false	true	true
TIM5 global interrupt	false	true	true

FreeRTOS_L2_F4 Project
Configuration Report

* User modified value		

# 9. System Views

- 9.1. Category view
- 9.1.1. Current



### 10. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f411\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis\_model/stm32f411\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4\_svd.zip

Description

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f411\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis\_model/stm32f411\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4\_svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_eval-

tools\_portfolio.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_stm8\_functi

onal-safety-packages.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_software\_development\_tools.pdf

Training Material https://www.st.com/resource/en/sales\_guide/sg\_sc2154.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32f4x1.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32nucleo.pdf

Flyers https://www.st.com/resource/en/flyer/flstmcsuite.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32trust.pdf

Product https://www.st.com/resource/en/certification\_document/stm32\_authenticat

Certifications ion\_can.pdf

Application Notes https://www.st.com/resource/en/application\_note/an1181-electrostatic-

discharge-sensitivity-measurement-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an1709-emc-design-

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2606-stm32-

- microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

  Application Notes https://www.st.com/resource/en/application\_note/an2639-solderingrecommendations-and-package-information-for-leadfree-ecopack-mcusand-mpus-stmicroelectronics.pdf

  Application Notes https://www.st.com/resource/en/application\_note/an2834-how-to-get-thebest-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an2867-oscillator-design-guide-for-stm8afals-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3997-audio-playback-and-recording-using-the-stm32f4discovery-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3998-pdm-audio-software-decoding-on-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4031-using-the-

- stm32f2-stm32f4-and-stm32f7-series-dma-controller-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4230-stm32-microcontroller-random-number-generation-validation-using-the-nist-statistical-test-suite-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4277-using-stm32-device-pwm-shutdown-features-for-motor-control-and-digital-power-conversion-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4488-getting-started-with-stm32f4xxxx-mcu-hardware-development-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4566-extending-the-dac-performance-of-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4616-migrating-from-stm32f401-and-stm32f411-lines-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4646-peripheral-interconnections-on-stm32f401-and-stm32f411-lines-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4739-stm32cube-firmware-examples-for-stm32f4-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf
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