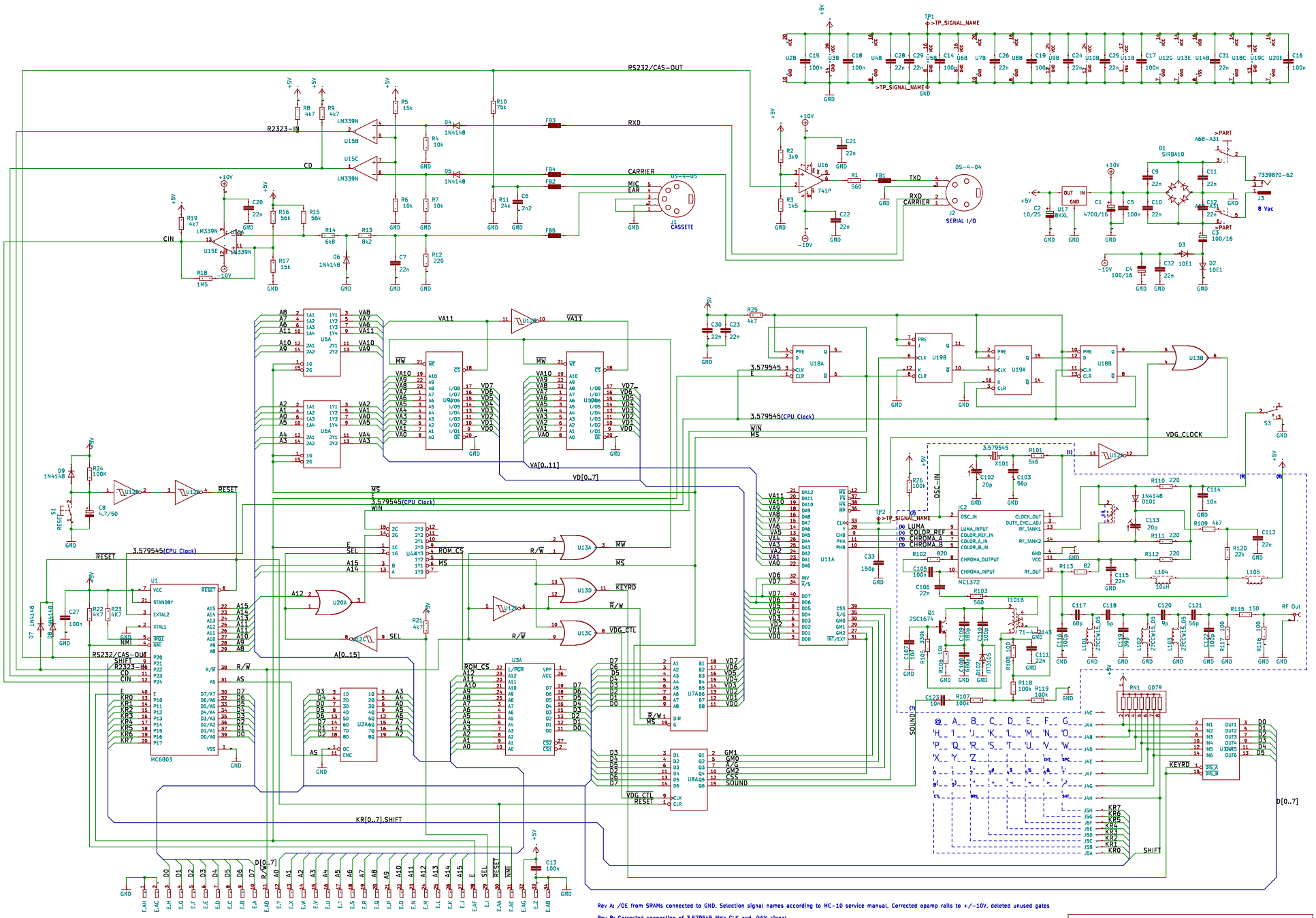
RS232/CAS-OUT
R2323-IN
CD
CIN
RXD
CARRIER
MIC
EAR
DS-4-05
J1
CASSETTE
TXD
DS-4-04
J2
SERIAL I/O
J3
8 Vac
RESET
3.579545(CPU Clock)
VA11
VA[0..11]
VD[0..7]
MW
MS
WIN
ROM_CS
KEYRD
VDG_CTL
VDG_CLOCK
A[0..15]
KR[0..7]SHIFT
D[0..7]
OSC_IN
LUMA
COLOR_REF
CHROMA_A
CHROMA_B
RF Out
SOUND
SHIFT
MC6803
U11A
U7A
U2A
U3A
U4A
U5A
U6A
U13A
U13B
U13C
U13D
U20A
U18A
U18B
U19A
U19B
U16
U15B
U15C
U15E
U17
U1
IC2
T101B
RN1
G07R
Rev A: /OE from SRAMs connected to GND, Selection signal names according to MC-10 service manual, Corrected opamp rails to +/-10V, deleted unused gates
Rev B: Corrected connection of 3.579545 MHz CLK and /WIN signal
Rev C: Updated DIN connectors, added pinout on RF modulator, added Test Points
Redrawn by: Danjovic 2021
TITLE: >DRAWING_NAME
Document Number:
REV: C
Date: >LAST_DATE_TIME
Sheet: >SHEET