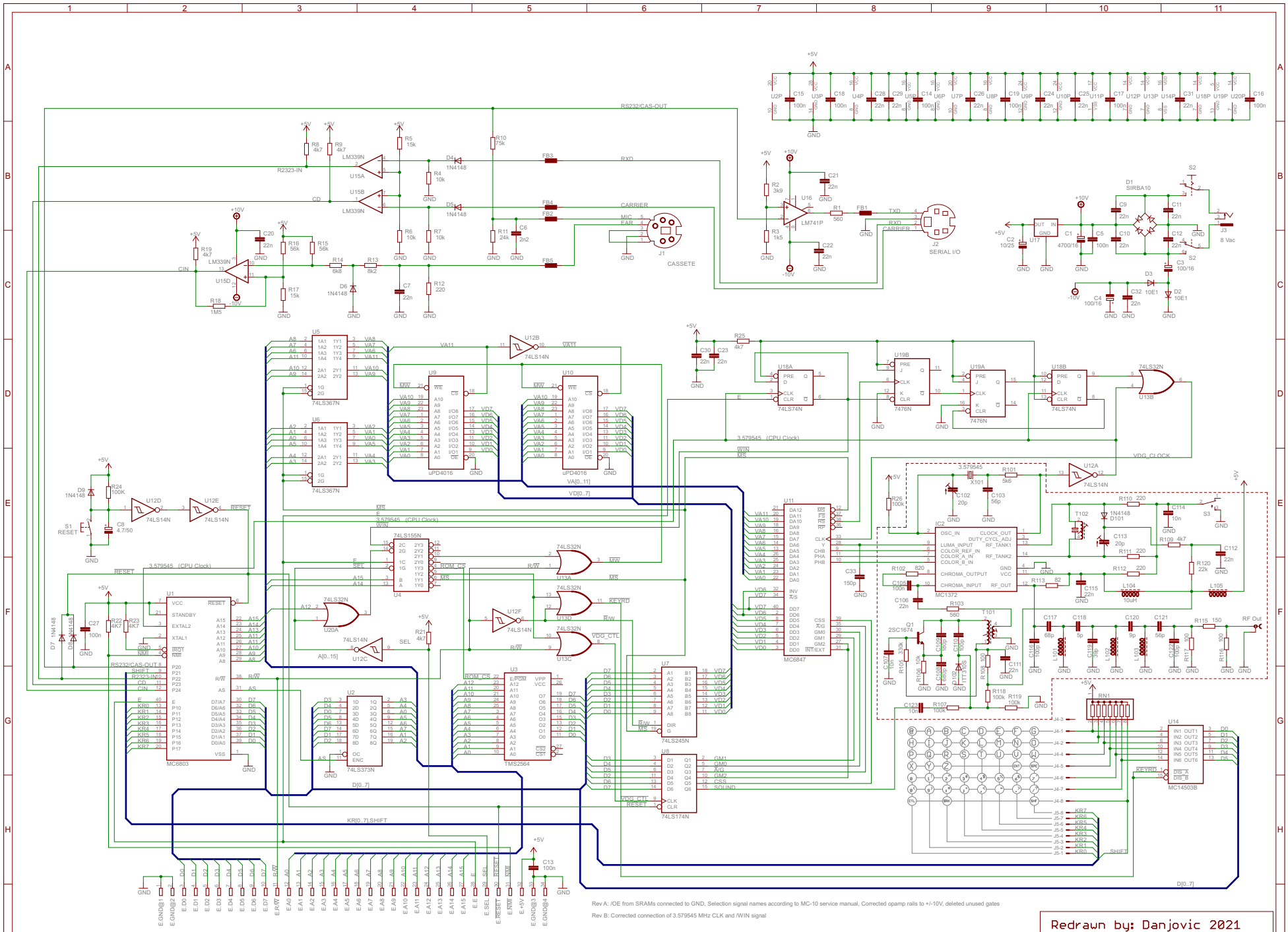
RS232/CAS-OUT
RXD
CARRIER
MIC
EAR
CASSETE
J1
J2
SERIAL I/O
TXD
RXD
CARRIER
LM339N
U15A
U15B
U15D
LM741P
U16
CIN
CD
R2323-IN
RESET
S1
S2
S3
J3
8 Vac
D1
SIRBA10
U17
U12B
U12D
U12E
U12A
U12F
U13A
U13B
U13C
U13D
U20A
U12C
U18A
U19A
U18B
U19B
74LS74N
7476N
74LS32N
74LS14N
74LS367N
74LS155N
uPD4016
VA[0..11]
VD[0..7]
VDG_CLOCK
3.579545 (CPU Clock)
WIN
MS
ROM_CS
KEYRD
VDG_CTL
SEL
A[0..15]
D[0..7]
KR[0..7],SHIFT
MC6803
MC6847
MC1372
IC2
X101
MC14503B
RN1
TMS2564
74LS245N
74LS174N
74LS373N
RF Out
Rev A: /OE from SRAMs connected to GND, Selection signal names according to MC-10 service manual, Corrected opamp rails to +/-10V, deleted unused gates
Rev B: Corrected connection of 3.579545 MHz CLK and /WIN signal
Redrawn by: Danjovic 2021
TITLE: MC-10 Schematics
Document Number:
REV: B
Date: 29/09/2021 11:58:10
Sheet: 1/1