

| Cycle | Instruction Retired | Reason                   |
|-------|---------------------|--------------------------|
| 1     | nop                 | startup                  |
| 2     | nop                 | startup                  |
| 3     | nop                 | startup                  |
| 4     | nop                 | startup                  |
| 5     | lbi r0, 0           | instruction in writeback |
| 6     | lbi r5, 43          | instruction in writeback |
| 7     | lbi r6, 7           | instruction in writeback |
| 8     | lbi r7, 43          | instruction in writeback |
| 9     | ld r1, r0, 0        | instruction in writeback |
| 10    | nop                 | stall                    |
| 11    | st r5, r1, 0        | instruction in writeback |
| 12    | ld r1, r0, 2        | instruction in writeback |
| 13    | nop                 | stall                    |
| 14    | st r6, r1, 1        | instruction in writeback |
| 15    | ld r1, r0, 4        | instruction in writeback |
| 16    | nop                 | stall                    |
| 17    | st r7, r1, 1        | instruction in writeback |
| 18    | halt                | instruction in writeback |