

Induced 6-Cycle Counting in Bipartite Graphs

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Abstract

Finding subgraphs in a bipartite graph is crucial to understanding its underlying structure. The smallest non-trivial subgraph in a bipartite graph is a 4-cycle, which is also known as a butterfly. Shi and Shun recently used the affordances of parallelization to develop efficient butterfly counting algorithms. However, parallel approaches to counting larger cycles is a relatively unexplored area. In this paper, we propose a parallel algorithm for efficiently counting induced 6-cycles.

1 Introduction

Many real-world networks are represented by a bipartite graph. For example, recommendation networks often are represented as a bipartite graph with users on one partition and items on the other [5]. Finding graph motifs that form the building blocks of these networks can reveal the underlying structure within bipartite graphs.

In unipartite graphs, the smallest cycle is a 3-cycle, which is also known as a triangle. However, since bipartite graphs contain no odd cycles, triangles do not exist in bipartite graphs. The smallest cycle in a bipartite graph is a 4-cycle, which is also known as a butterfly. Butterflies are the smallest building blocks for community structures in bipartite graphs.

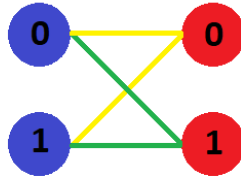


Figure 1: This graph depicts a butterfly. Nodes in u are in blue while nodes in v are in red. The butterfly is made of two wedges, which are highlighted in yellow and green.

Counting cycles in bipartite graphs is NP-hard [3]. Sequential algorithms for butterfly counting use the concept of combining wedges (2-paths) to count butterflies (see Figure 1) [1, 6, 8]. When dealing with larger graphs, however, the runtime of sequential algorithms may be problematic. Adapting sequential algorithms for parallelization can significantly reduce the runtime of these algorithms. Shi and Shun [7] recently designed a parallel butterfly counting algorithm which modified Chiba and Nishizeki's wedge retrieval process [1] to enable parallelization.

Given the relevance of bipartite graphs in real-world relationships, it is desirable to find larger cycles within these graphs. Karimi and Banihashemi [4] designed a message-passing algorithm for counting cycles of length g to $2g-2$ in bipartite graphs, where g is the girth of the graph. Dehghan and Banihashemi [2] proposed an algorithm that uses breadth-first search to count cycles of length g to $g+4$ in bipartite graphs. However, these sequential algorithms have only been tested on small bipartite graphs.

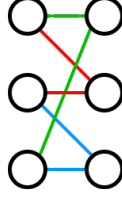


Figure 2: In this figure, a triangle of wedges (colored as red, blue, and green) forms a 6-cycle.

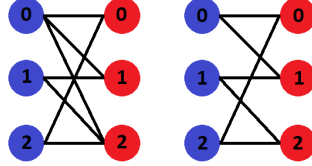


Figure 3: The graph on the left depicts a non-induced 6-cycle. The graph on the right depicts an induced 6-cycle. In the non-induced graph, the removal of the edge from 0 to 2 won't affect the 6-cycle.

Recently, Yang et al. introduced sequential algorithms for counting non-induced 6-cycles in large bipartite networks [9]. Their algorithms are based on the concept that a 6-cycle is a triangle of wedges (see Figure 2) or a pair of 3-paths. In this paper, we propose a parallel algorithm that is also based on the concept that an 6-cycle is a triangle of wedges. Parallelizing algorithms that count larger cycles is extremely valuable due to their increased complexity. This paper presents a framework for counting induced 6-cycles (see Figure 3) in bipartite graphs which uses the affordances of parallelization to maximize efficiency.

2 Notation

We work on a simple and undirected bipartite graph $G = (U, V, E)$ where U is the set of nodes in the left set, V is the set of nodes in right set, and E is the set of edges. The neighbors of a node v is denoted by $N(v)$. A **wedge** is a set of three nodes $u1, u2 \in U$ and $v \in V$ composed of edges $(u1, v), (u2, v) \in E$. We call the nodes $u1, u2$ **endpoints** and the node v the **center**. An **induced 6-cycle** is a set of six nodes $u1, u2, u3 \in U$ and $v1, v2, v3 \in V$ such that its internal edges exactly form a cycle.

3 Algorithm

We introduce a parallel algorithm for counting induced 6-cycles in bipartite graphs. Our algorithm extends the parallel wedge retrieval algorithm proposed by Shi and Shun [7].

Algorithm 1 PREPROCESSING(G)

Input: G : graph**Output:** G' : ordered graph

```
1:  $w_u \leftarrow 0$ 
2: parallel for each  $x \in U$  do
3:    $n \leftarrow |G(x)|$ 
4:    $w_u \leftarrow w_u + \frac{n*(n-1)}{2}$ 
5:  $w_v \leftarrow 0$ 
6: parallel for each  $x \in V$  do
7:    $n \leftarrow |G(x)|$ 
8:    $w_v \leftarrow w_v + \frac{n*(n-1)}{2}$ 
9: if  $w_u < w_v$  then
10:    $Swap(U, V)$  ▷ swap  $U$  and  $V$ 
11:  $X \leftarrow Sort(U)$  ▷ sort  $U$  in increasing order of degree
12: Let  $x$ 's rank  $R(x)$  be its index in  $X$ 
13: parallel for each  $x \in U \cup V$  do
14:   if  $x \in U$  then
15:      $G'(R(x)) \leftarrow \{y | (x, y) \in E\}$ 
16:   else
17:      $G'(x) \leftarrow Sort(\{R(y) | (x, y) \in E\})$  ▷ sort neighbors' ranks by descending order
18: return  $G'$ 
```

We give a preprocessing algorithm (Algorithm 1) that takes as input a bipartite graph, swaps left and right sets depending on their wedge counts, and renames nodes in increasing order of degree. *Preprocessing* swaps U and V if the number of wedges whose center is $\in U$ is less than the number of wedges whose center is $\in V$. This helps reduce the number of processed wedges. *Preprocessing* then orders nodes in increasing order of degree, which we found to yield efficient results. Shi and Shun [7] proved that using approximate degree ordering, complement degeneracy ordering, and approximate complement degeneracy ordering also gives work-efficient bounds. By establishing an ordering of nodes, we can avoid traversing the same induced 6-cycle twice.

Algorithm 2 GetWedges(G)

Input: G : graph**Output:** W : list of wedges

```
1: parallel for each  $u1 \in U$  do
2:   parallel for each  $v \in N(u1)$  do
3:     parallel for each  $u2 \in N(v)$  do
4:       if  $u2 > u1$  then
5:          $W((u1, v, u2))$  ▷ add wedge to  $W$ 
6:       else
7:         break
8: return  $W$ 
```

We define a wedge retrieval algorithm, *GetWedges* (Algorithm 2), that takes as input a preprocessed graph and outputs a list of wedges. We use W to denote a parallel container such that $W(x)$ stores x in the container. W should allow for fast access of wedges based on endpoints such that

when accessing all wedges whose smallest endpoint is $u1 \in U$, wedges are sorted based on the other endpoint. *GetWedges* is based off of Shi and Shun [7] wedge retrieval algorithm which enables the parallel processing of wedges. For all nodes $u1 \in U$, the algorithm retrieves all wedges with endpoints $u1, u2$ and center v such that $u2$ is greater than $u1$. Note that Shi and Shun proved how once we have retrieved all wedges with endpoint $u1$, there is no need to consider wedges with center $u1$. Although this point was originally applied to butterfly counting, the same logic can be applied to induced 6-cycles.

Algorithm 3 *Par6CycleCount*(G, W)

Input: G : graph
Output: c : count of induced 6-cycles

```

1:  $G \leftarrow \text{Preprocessing}(G)$ 
2:  $W \leftarrow \text{GetWedges}(G)$ 
3:  $c \leftarrow 0$ 
4: parallel for each  $u1 \in U$  do
5:   for each  $w1 \in W(u1)$  do  $\triangleright u1$  is smallest endpoint in  $w1$ 
6:      $c' \leftarrow 0$ 
7:      $x, y \leftarrow -1$ 
8:      $skip \leftarrow false$ 
9:      $u1, v1, u2 \leftarrow w1$ 
10:    for each  $w2 \in W(u2)$  do  $\triangleright u2$  is smallest endpoint in  $w2$ 
11:       $u2, v2, u3 \leftarrow w2$ 
12:      if  $skip \cap (x = u3)$  then
13:        continue
14:       $x \leftarrow u3$ 
15:       $skip \leftarrow v1 \in N(u3)$ 
16:      if  $!skip \cap (v2 \notin N(u1))$  then
17:        if  $y \neq u3$  then
18:           $c' \leftarrow 0$ 
19:          for each  $w3 \in W(u1, u3)$  do  $\triangleright u1, u3$  are endpoints of  $w3$ 
20:             $u1, v3, u3 \leftarrow w3$ 
21:            if  $v3 \notin N(u2)$  then
22:               $c' \leftarrow c' + 1$ 
23:             $y \leftarrow u3$ 
24:           $c \leftarrow c + c'$ 
25: return  $c$ 

```

We now describe the induced 6-cycle counting algorithm, which is given as *Par6CycleCount* in Algorithm 3. Given a bipartite graph G , this algorithm applies G to the preprocessing and wedge retrieval algorithms described in Algorithms 1 and 2, respectively. We use $W(x)$ to denote the set of wedges where x is the smallest of the two endpoints and $W(x, y)$ to denote the set of wedges whose endpoints are x and y . *Par6CycleCount* finds triangles of wedges $\{u1, v1, u2\}$, $\{u2, v2, u3\}$, and $\{u1, v3, u3\}$ such that $u1 < u2 < u3$. For every triangle of wedges, there are a maximum of three checks for inducedness (see Figure 4). Inducedness checks are skipped if a similar set of wedges was processed previously (see Figure 5).

Proof of correctness. We will prove Algorithm 3, *Par6CycleCount*, is correct by contradiction.

Suppose there exists a set of nodes that is inaccurately counted as an induced 6-cycle. Since lines

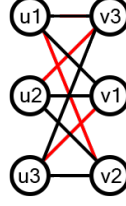


Figure 4: In this figure, the red edges prevent inducedness for the 6-cycle colored in black. Each inducedness check (lines 15, 16, and 21) in Algorithm 3, *Par6CycleCount*, corresponds to a red edge.

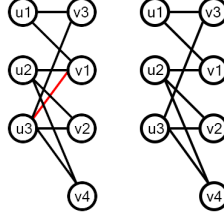


Figure 5: The speedups implemented in lines 12 (left) and 17 (right) of *Par6CycleCount*. The figure on the left shows only one inducedness check (the red edge) is needed for wedges $\{u2, v2, u3\}$ and $\{u2, v4, u3\}$. The figure on the right shows that the count of 6-cycles for wedge $\{u2, v2, u3\}$ is the same as wedge $\{u2, v4, u3\}$.

Therefore, we only need to do calculations for one wedge.

15, 16, and 21 check for inducedness given a set of 6 nodes, every node set counted must be an induced 6-cycle, contradicting our claim.

Suppose there exists an induced 6-cycle that is counted twice. In Algorithm 2, *GetWedges*, the ordering of node traversal in line 4 prevents a wedge from being traversed multiple times, causing each wedge to be stored only once in line 5. There also exists a specific ordering of wedge traversal for *Par6CycleCount* in lines 5, 10, and 19. For every induced 6-cycle, the wedge which contains the smallest two endpoints is traversed first (line 5). Then, the wedge containing the largest two endpoints is traversed second (line 10). Finally, the wedge which contains the smallest and the largest endpoint is traversed last (line 19). This prevents an induced 6-cycle from being traversed multiple times, contradicting our claim of duplicity in induced 6-cycle counting.

Suppose there exists an induced 6-cycle $x = \{u1, u2, u3, v1, v2, v3\}$ that isn't counted. Then x contains three wedges $a = \{u1, v1, u2\}$, $b = \{u2, v2, u3\}$, and $c = \{u1, v3, u3\}$, which the algorithm processes in that order (lines 5, 10, and 19). Therefore, the algorithm counts the triangle of wedges a , b , and c as an induced 6-cycle, contradicting our claim.

Since all possible cases lead to contradiction, *Par6CycleCount* returns the correct induced 6-cycle count. \square

4 Results

In this section, we present the runtime of our algorithm on real-world datasets, whose main characteristics are shown in Table 1.

All experiments were performed on a Linux operating system running on a machine with Intel Xeon processors. We ran our algorithm on 2 nodes with each having 16 cores and a total of 64 GB memory. We implemented our algorithm in C++ and compiled using gcc 10.2.0 at the -O3 level. The runtime, in seconds, is shown in Table 2. The runtime shown is the average runtime for 3 different runs of *Par6CycleCount* with the specified number of cores.

The most interesting result is that of IMDB and Kindle. Although they have comparable graph sizes,

Table 1: Bipartite networks

Real-world datasets	U	V	E	# induced 6-cycles
CondMat	16,727	22,016	58,595	1.92×10^4
Database	11,193	8,915	30,716	5.02×10^4
Marvel	6,487	12,942	96,662	1.70×10^9
DBLP	4,000,150	1,425,813	8,649,016	5.10×10^7
IMDB	1,232,031	419,661	5,596,667	2.01×10^{10}
Github	56,555	123,345	440,237	1.37×10^{11}
Kindle	1,406,890	430,530	3,205,467	5.20×10^9

Table 2: Runtime (seconds)

Real-world datasets	# cores					
	1	2	4	8	16	32
CondMat	0.019	0.014	0.009	0.005	0.003	0.003
Database	0.022	0.015	0.01	0.005	0.004	0.003
Marvel	3.694	1.819	0.951	0.511	0.301	0.231
DBLP	515.112	281.172	147.85	77.022	41.182	23.512
IMDB	1,183.665	600.422	310.789	165.543	87.768	50.456
Github	2,754.367	1,396.634	733.507	379.085	206.212	117.866
Kindle	8,248.202	4,324.843	2,543.986	2,115.458	1,000.188	923.248

Kindle takes significantly longer to process compared to IMDB. This indicates that the structure of the graph is important in terms of runtime. In *Par6CycleCount*, lines 12 and 17 avoid duplicate processing if the current 6-cycle is similar to a previous 6-cycle (see Figure 5). This results in significant speedups for graphs composing of similar 6-cycles in terms of wedges.

In terms of runtime in relation to the number of cores, DBLP, IMDB, and Github had significant speedups of about 23x when comparing 32 cores to a single core. There also isn't a significant decline in expected improvement when using 32 cores for these graphs, suggesting that additional cores may be used to significantly speedup the algorithm. The smaller graphs, CondMat and Database, have this decline at around 8 cores. Since our implementation of the algorithm parallelizes based on the size of the node sets, larger graphs typically have a higher ideal number of cores compared to smaller graphs in terms of expected runtime improvement over cost.

5 Final Remarks

The next steps are to experiment with different orderings besides degree ordering, such as degeneracy ordering (ordering by core numbers), and to identify which sections of the algorithm are the most problematic. I can also experiment with different wedge traversal schemes. For example, will traversing wedges which contain endpoints in the smaller set be faster than traversing wedges which contain endpoints in the larger set? Runtime analysis with >32 cores can also be analyzed to find the ideal number of cores for a given graph.

The code used for this report is available at <https://github.com/Deerjason/par6cycle>. Parallelization is achieved through the use of Intel Threading Building Blocks due to its multi-core capabilities and strong tasking support. Since we are dealing with graphs with millions of nodes and edges, an efficient parallel library is needed. Currently, parallel induced 6-cycle counting is implemented in the develop branch in *6CycleCount.cpp*.

Table 3: New Runtime (seconds, 4 sockets)

Real-world datasets	# cores						
	1	2	4	8	16	32	52
CondMat	0.02	0.014	0.011	0.007	0.004	0.004	0.003
Database	0.022	0.012	0.008	0.007	0.004	0.003	0.003
Marvel	3.957	1.999	1.066	0.584	0.323	0.248	0.206
DBLP	8.277	4.68	2.36	1.302	0.705	0.416	0.324
IMDB	1,224.275	617.813	318.777	169.048	87.145	49.363	35.859
Github	658.617	331.518	165.961	88.864	45.684	26.22	19.315
Kindle	1,196.831	606.19	311.103	166.424	87.102	49.055	34.691

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