

# Module: Main

## Basic Info

- File: parent.v
- 0 params, 6 ports
- top module ✓



## Params

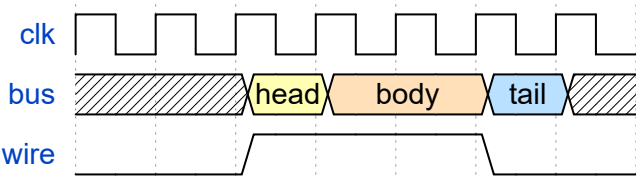
no params info

## Ports

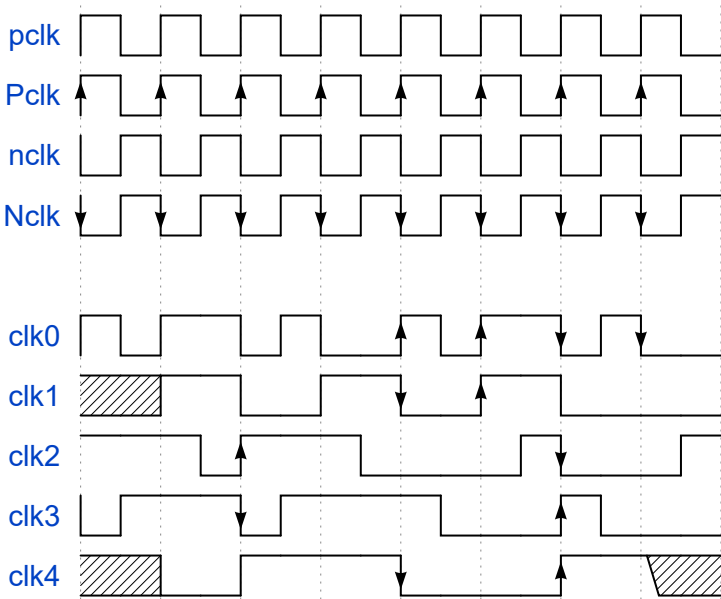
Port Name	Direction	Range	Description
a	input	——	Main input
b	input	——	Main input
c	input	——	Main input
Qus	output	——	Main output
Qs	output	——	Main output
`main	output	——	Main output

## Dependency

name	module	path
u_dependence_1_1	dependence_1	child_1.v
u_dependence_1_2	dependence_1	child_1.v
u_dependence_3	dependence_3	



this is wavedrom demo1



this is wavedrom demo2