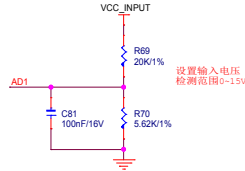
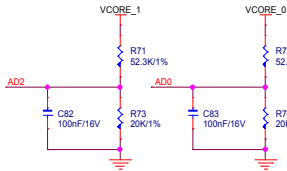
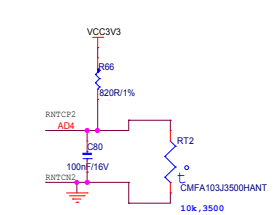
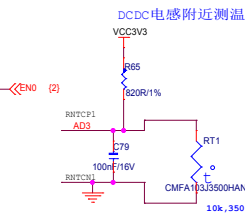
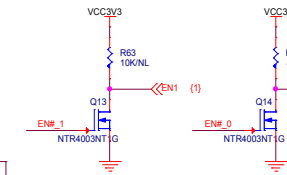
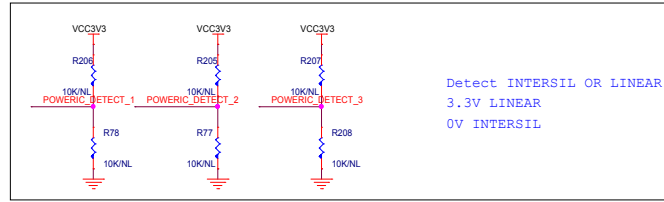
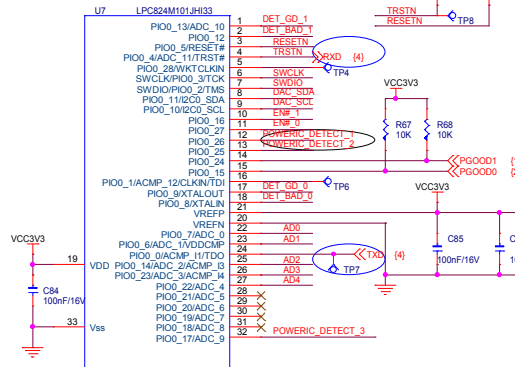
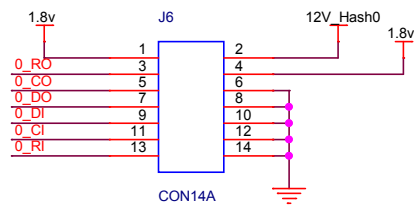
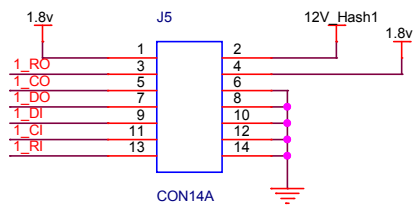


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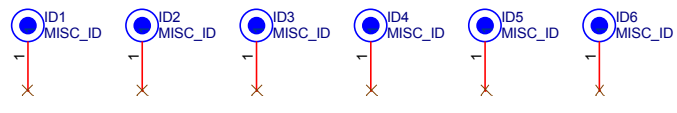
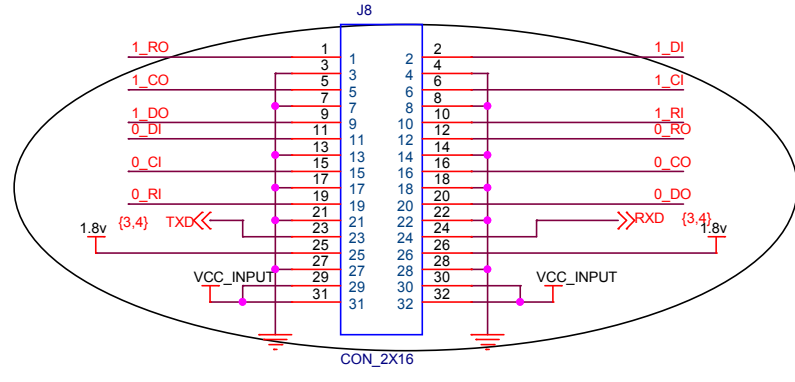
UART pin definition is from FPGA side. TXD means TX on FPGA, RXD means RX on FPGA



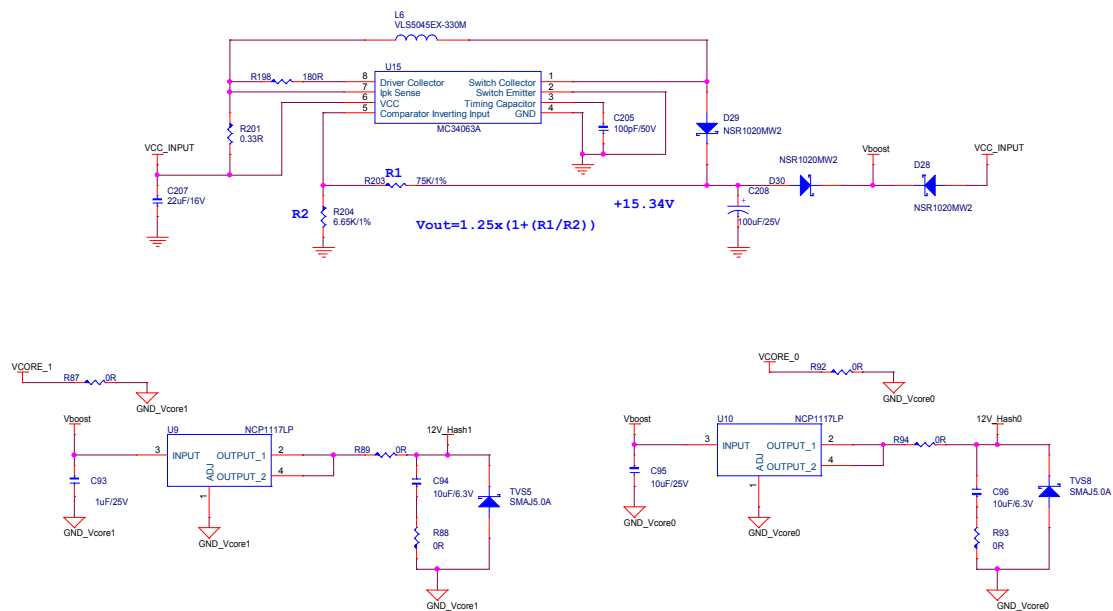
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| Size | | Page Name | | Rev | |
| B | | 003_PMU851_MCU | | 1.0.1 | |
| Date | | Monday, March 11, 2019 | | Sheet 3 of 5 | |



已经与MM板线序核对完成，
不能进行线序修改！



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| PMU851-INTERSIL-R | | | | | |
| Size | Page Name | | | | Rev |
| A | 004_PMU851_CONNECTOR | | | | 1.0.1 |
| Date: | | Monday, March 11, 2019 | | Sheet | 4 of 5 |



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| Title | | PMU851-INTERSIL-R | | |
| Size B | Page Name 002_PMU851_power_part2 | | | Rev 1.0.1 |
| Date: | Monday, March 11, 2019 | | Sheet | 5 of 5 |